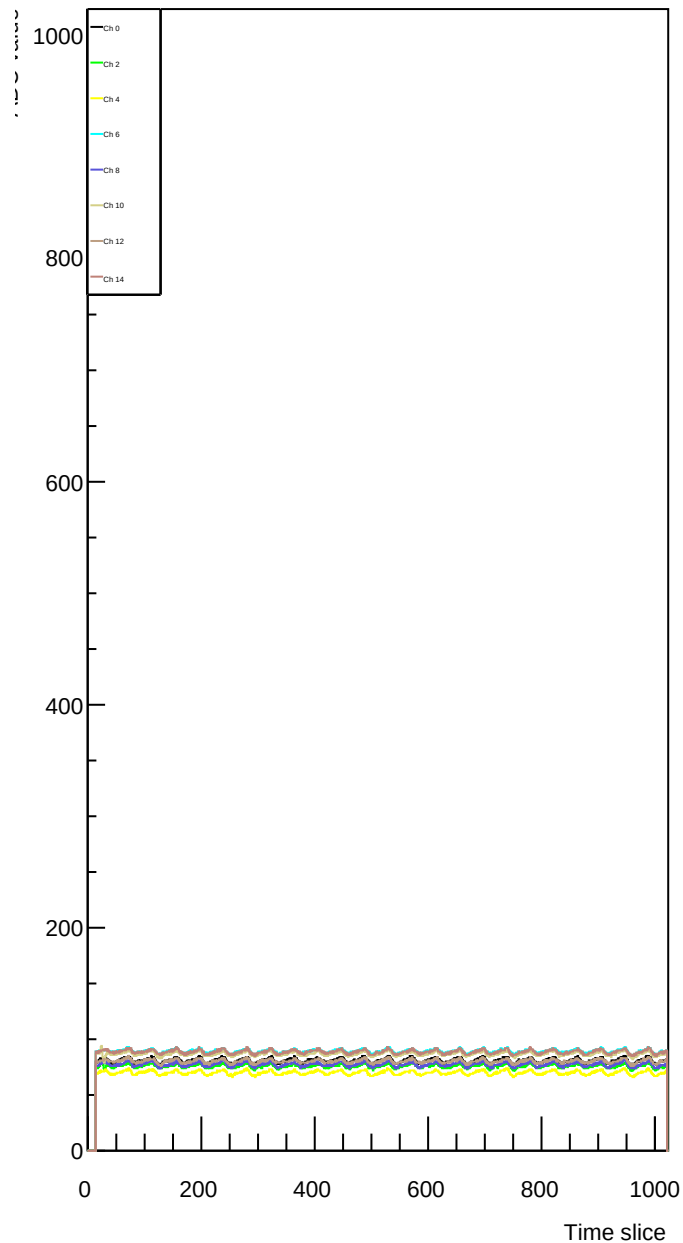
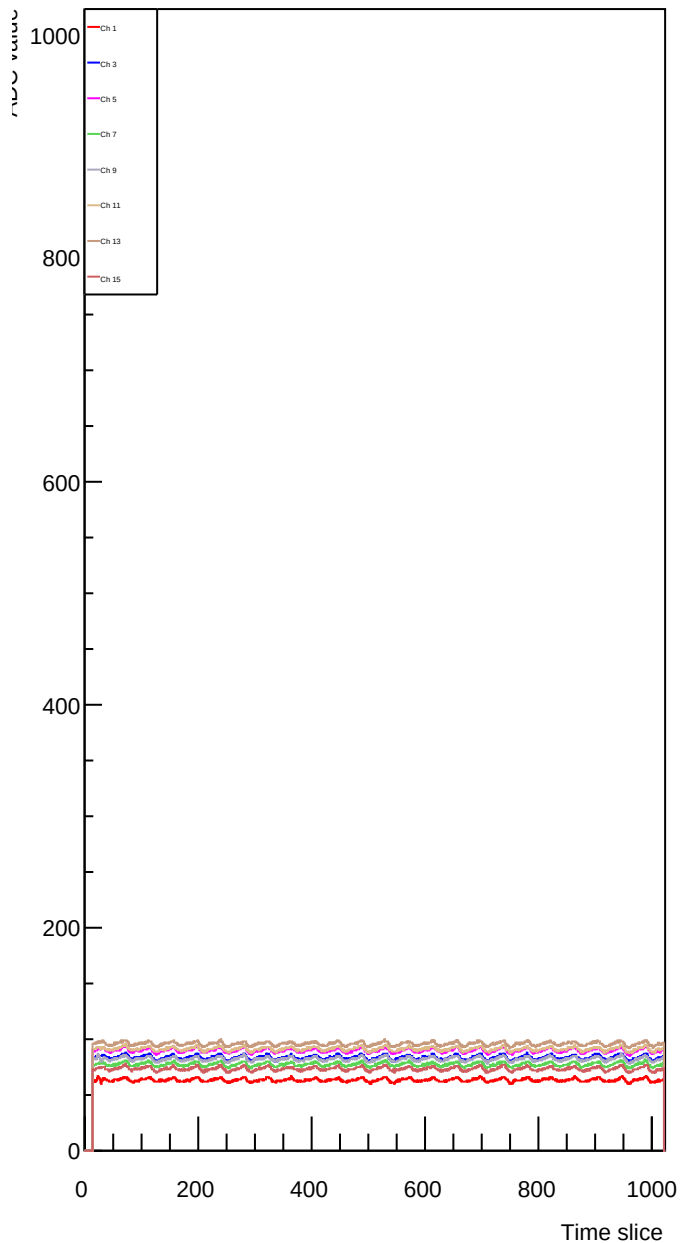


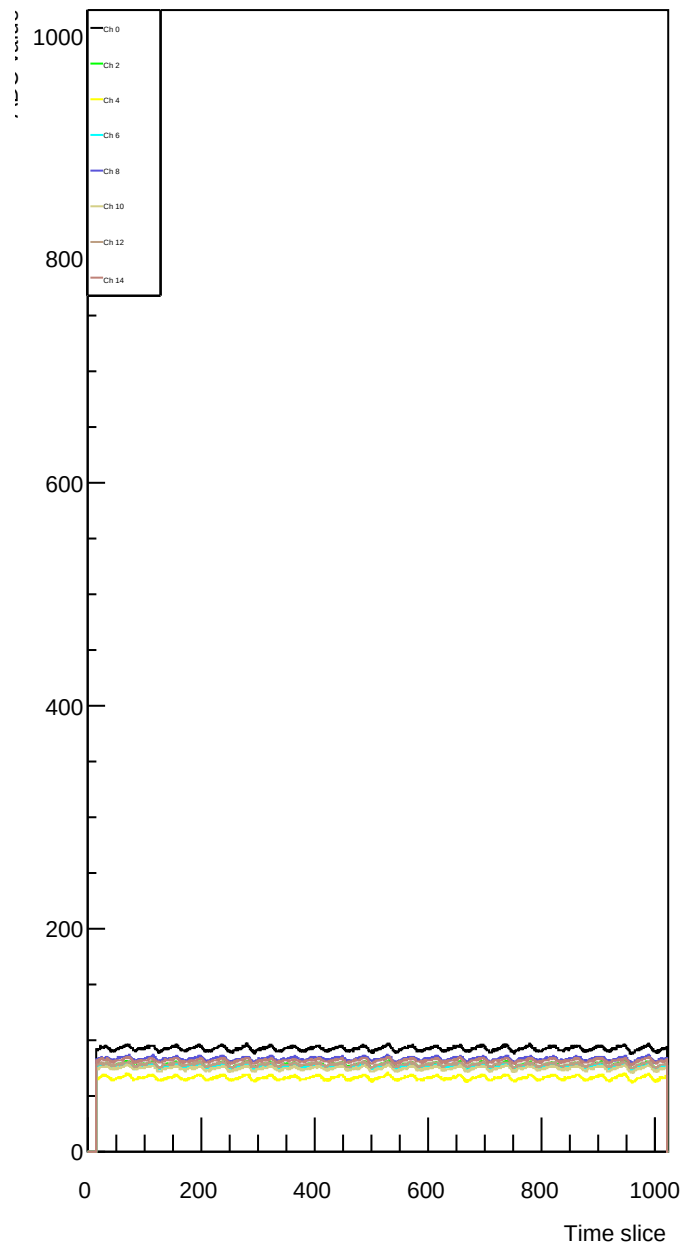
Run 0 event 20 mcm 0 chip 0 g0-s0 even ch



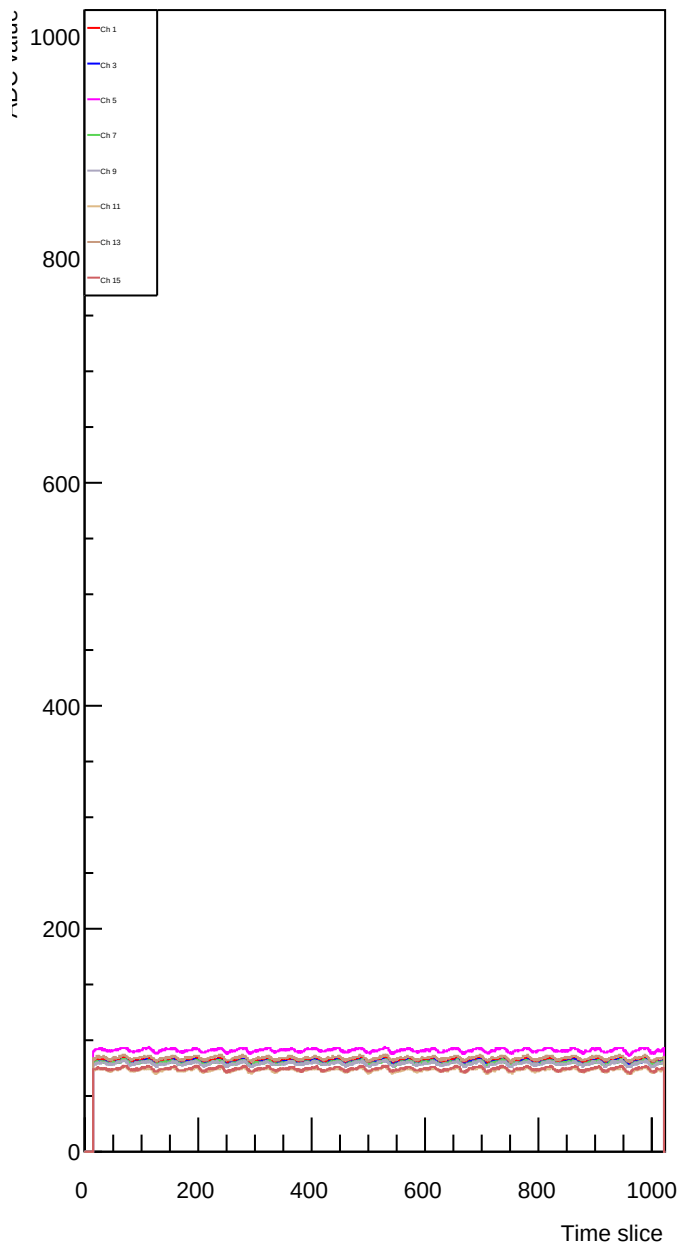
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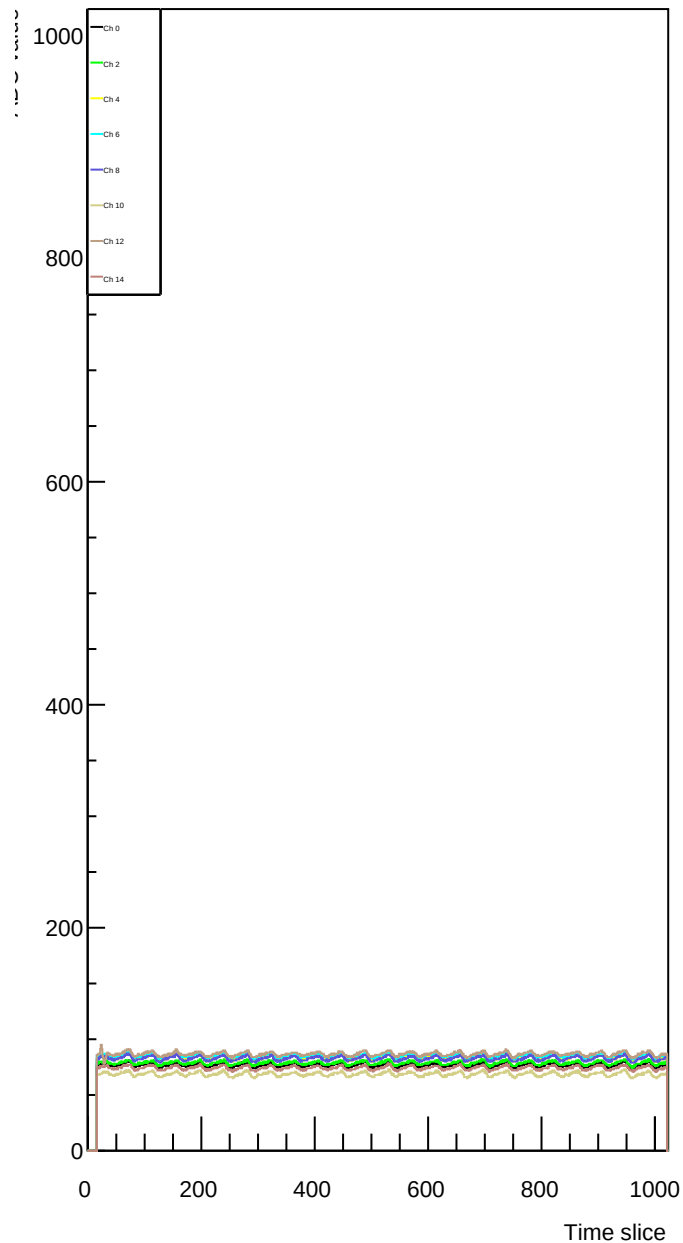
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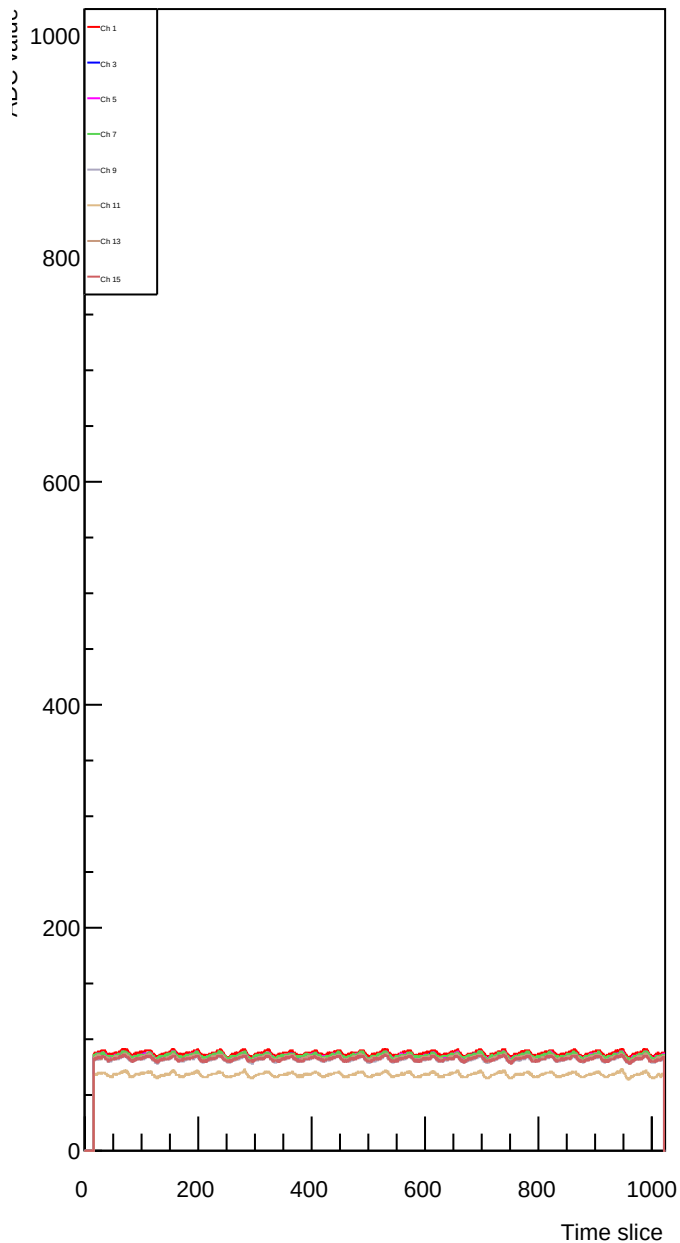
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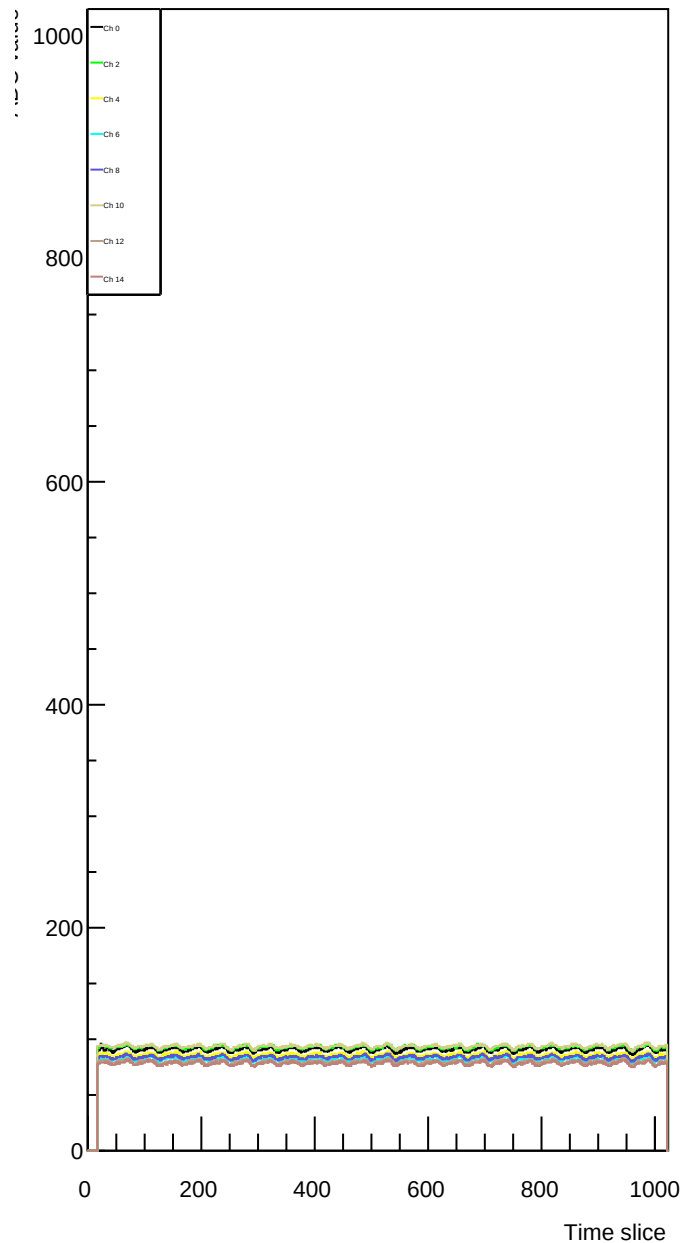
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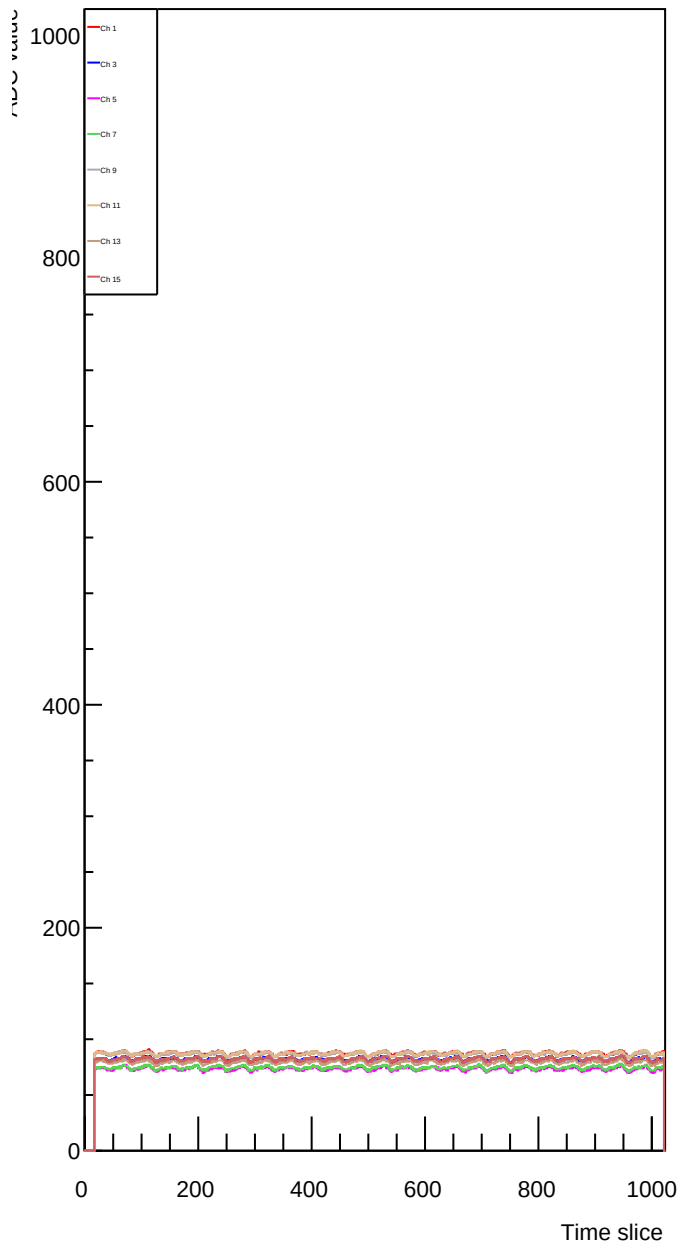
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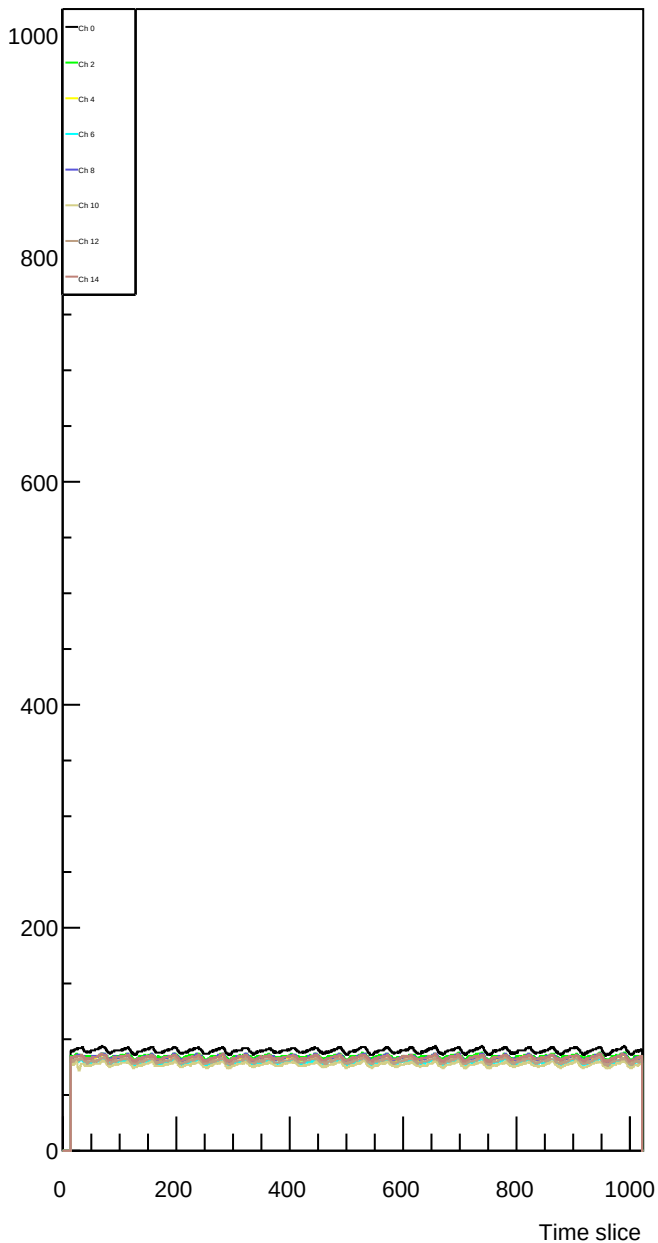
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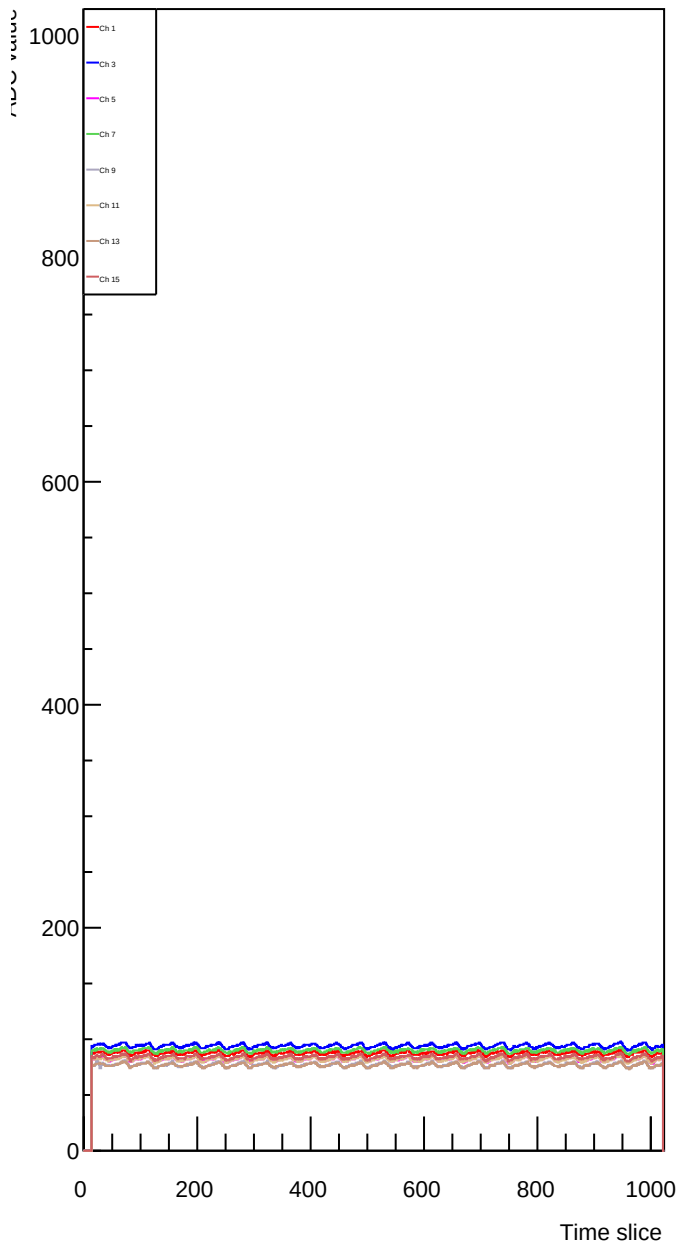
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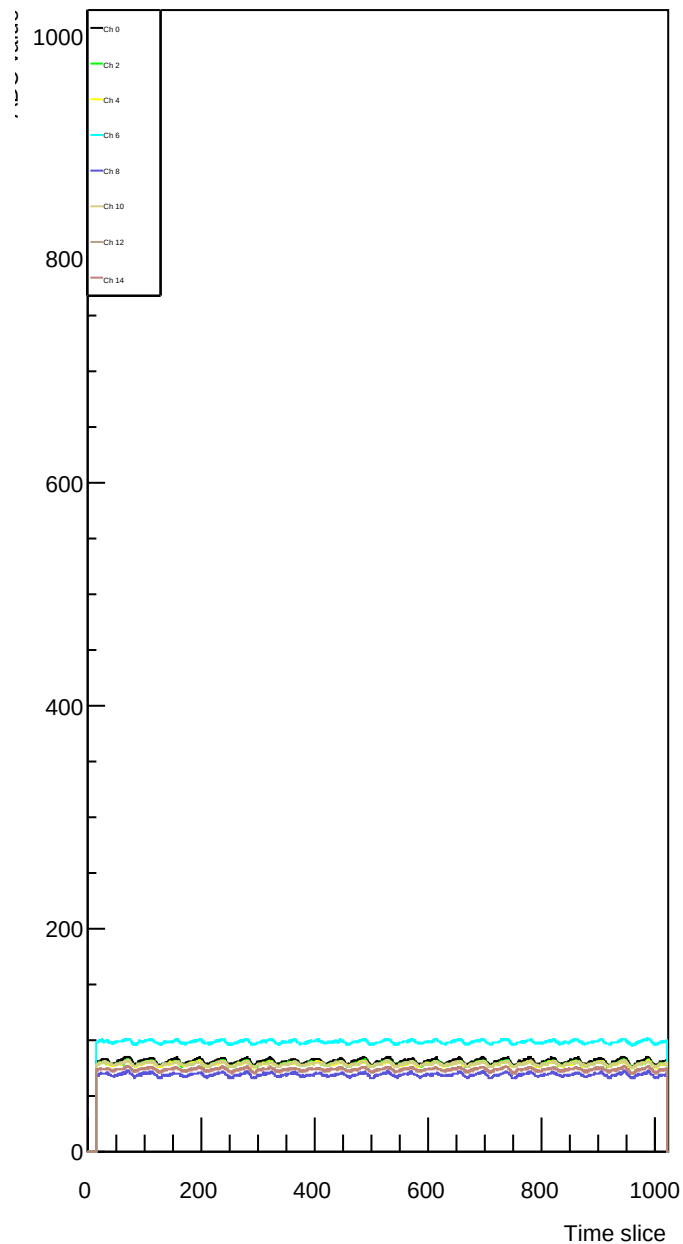
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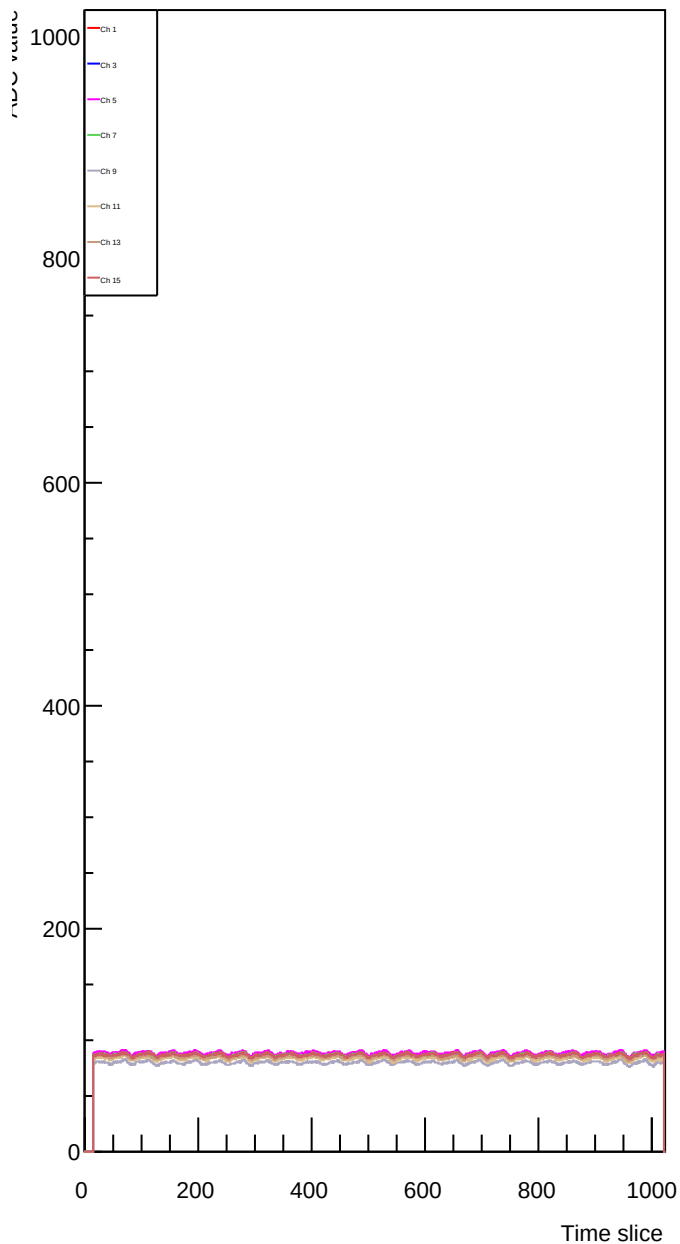
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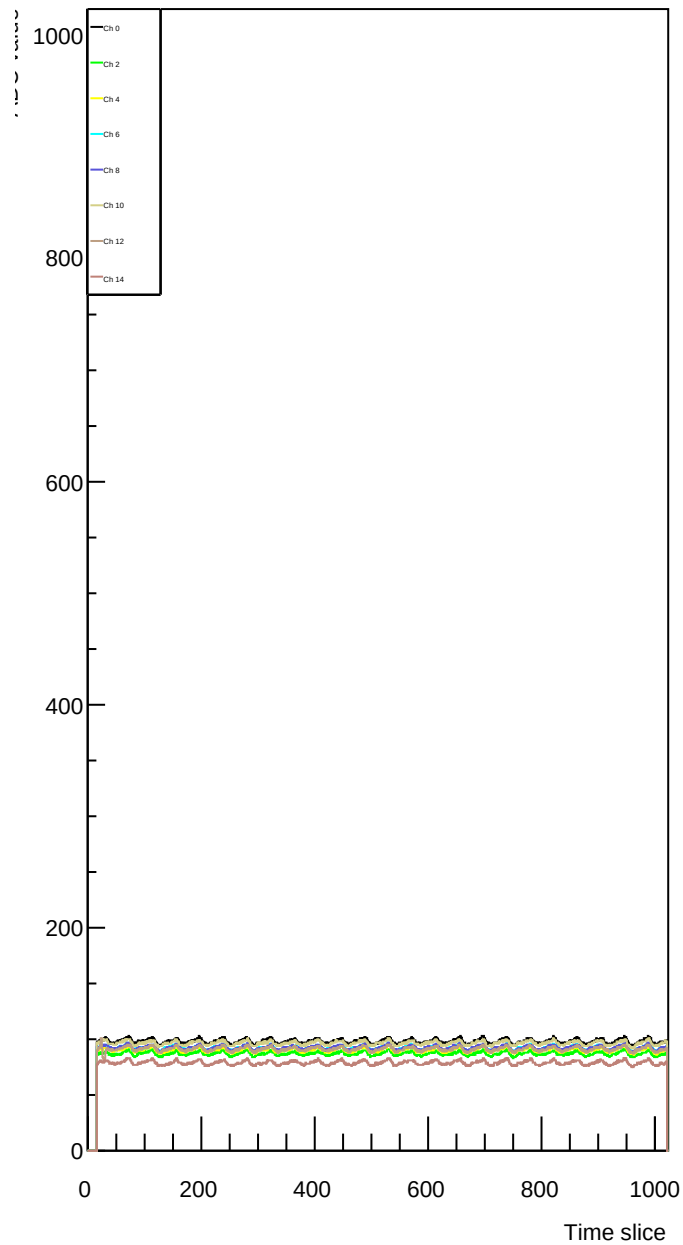
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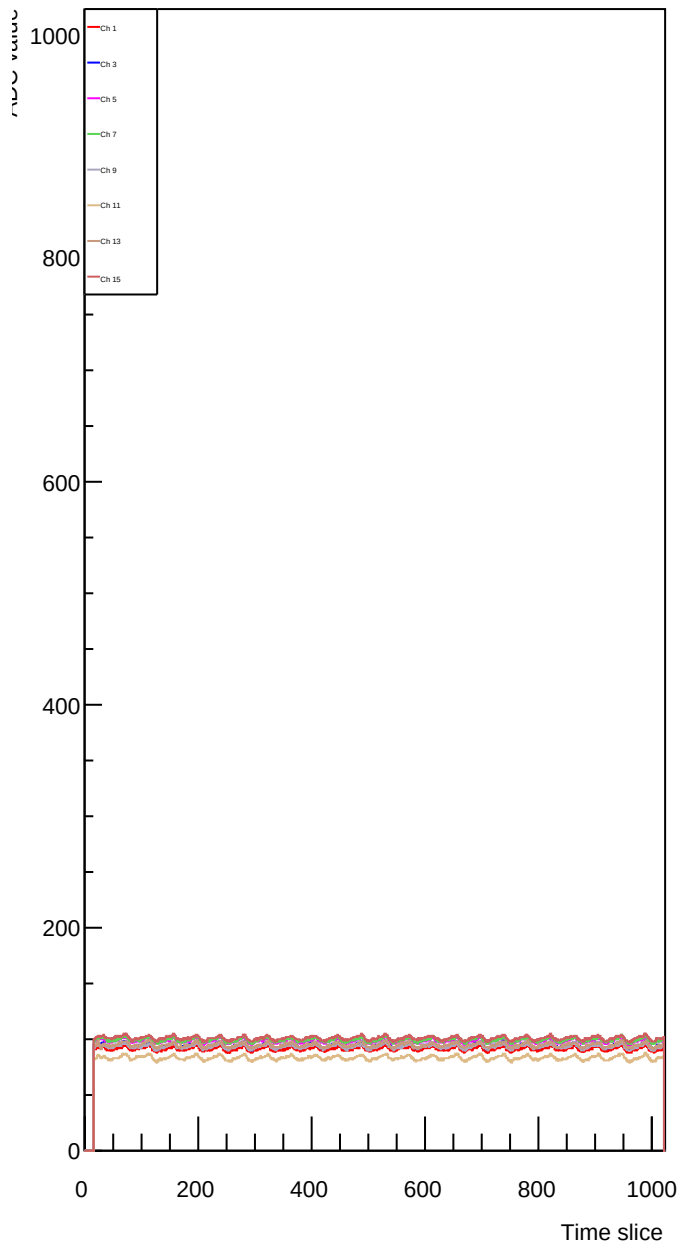
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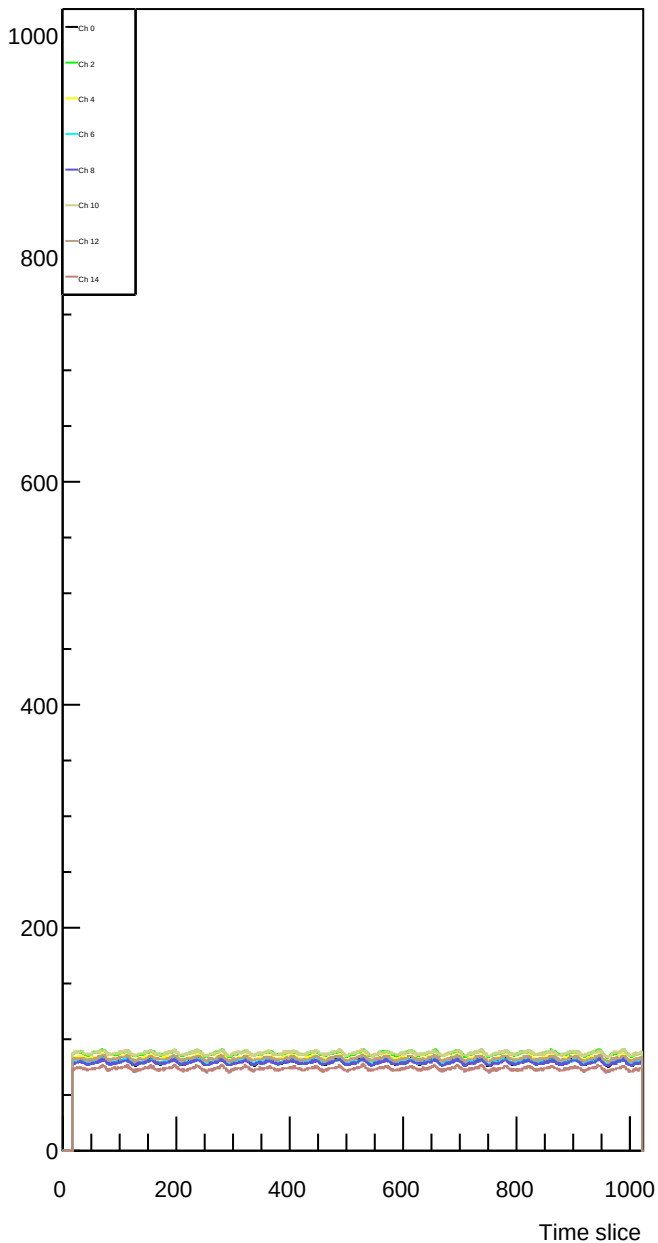
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Run 0 event 20 mcm 0 chip 6 g0-s0 odd ch



Run 0 event 20 mcm 0 chip 7 g0-s0 even ch



Run 0 event 20 mcm 0 chip 7 g0-s0 odd ch

