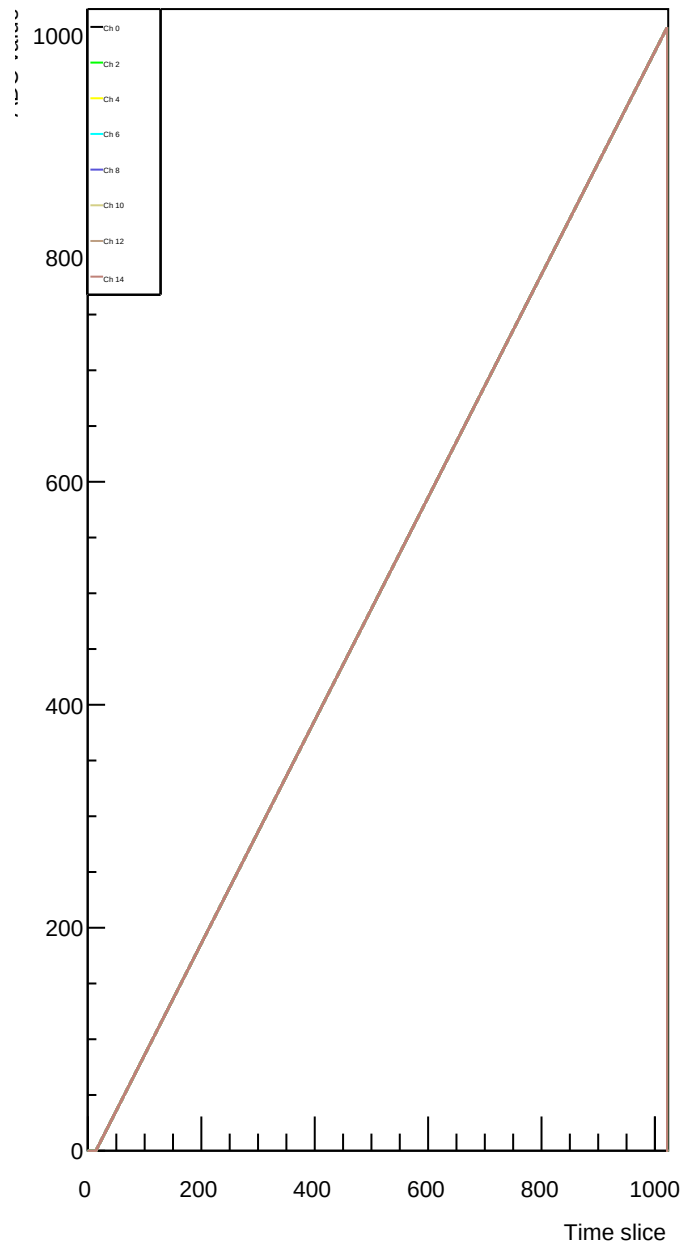
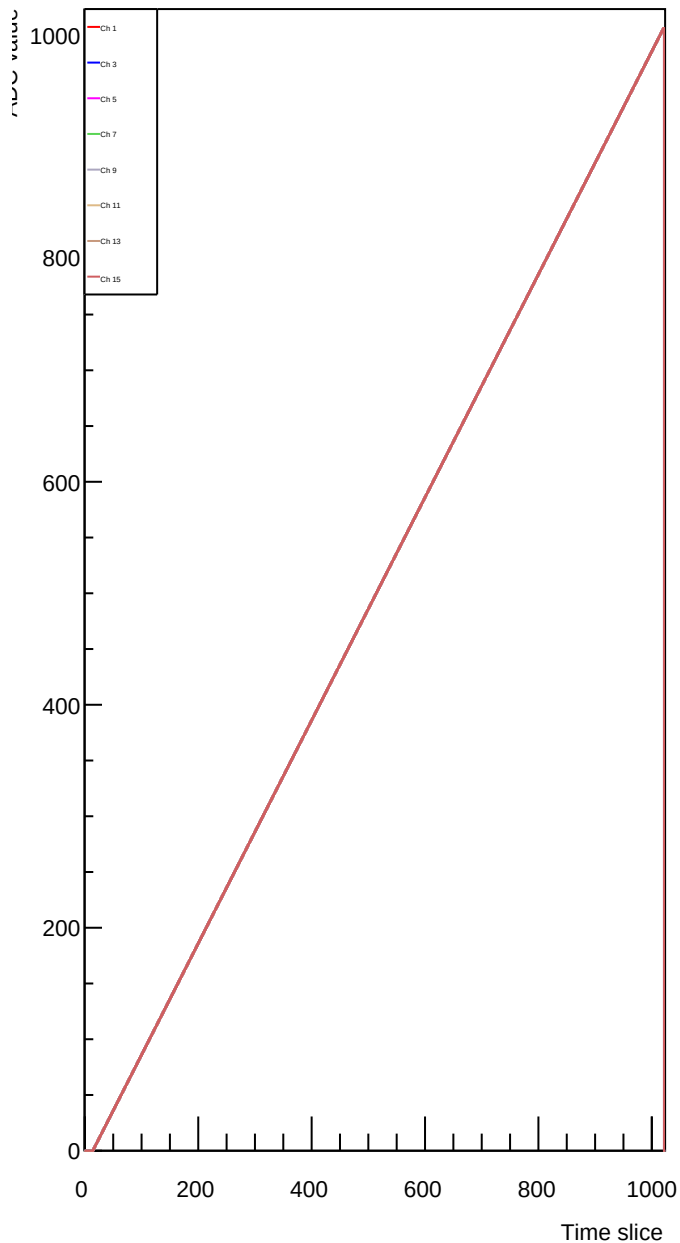


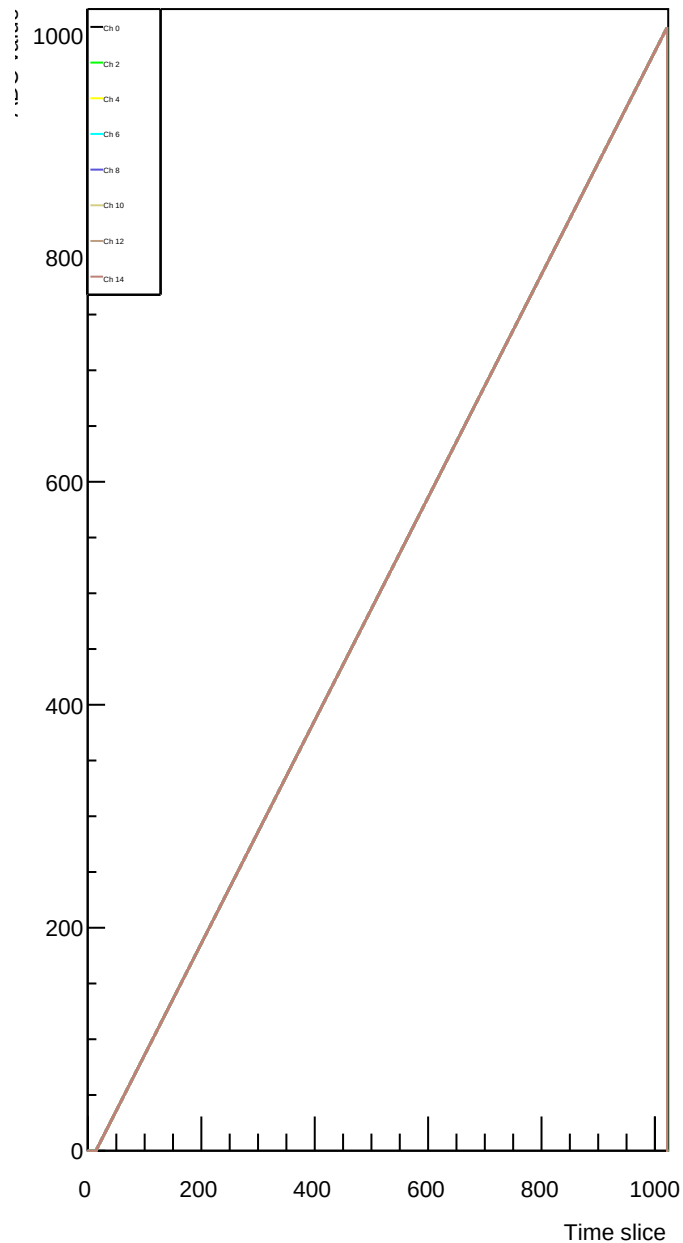
Run 0 event 20 mcm 0 chip 0 ramp2 even ch



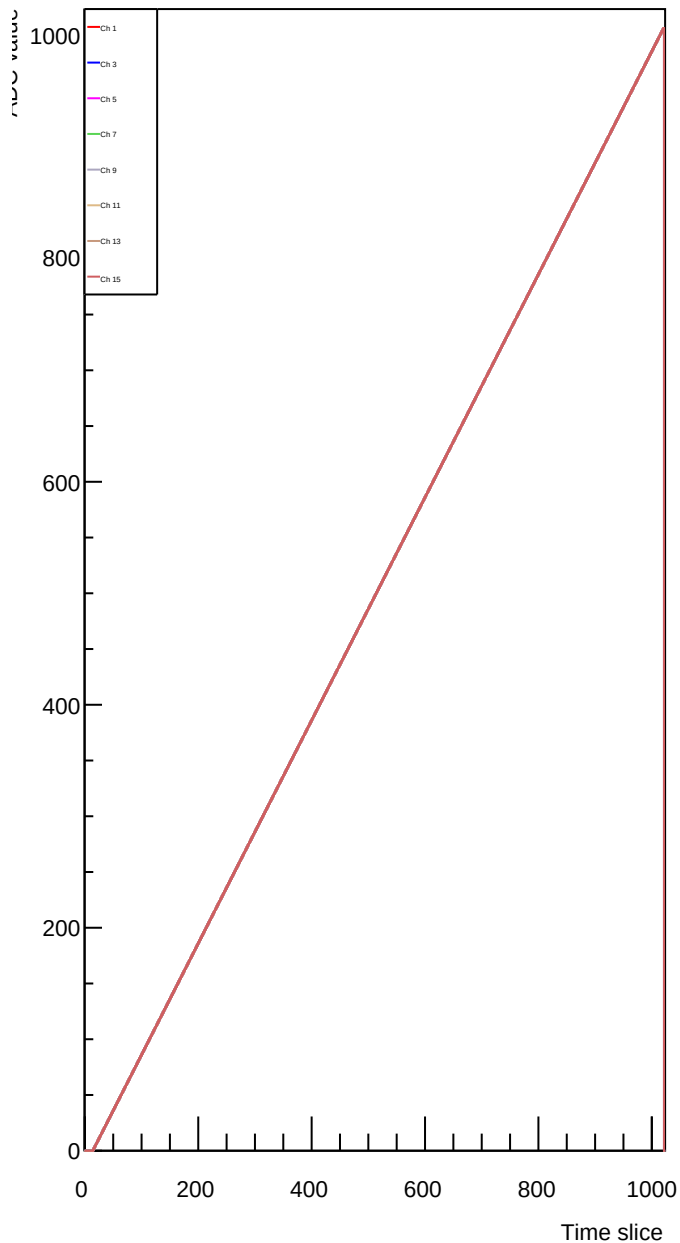
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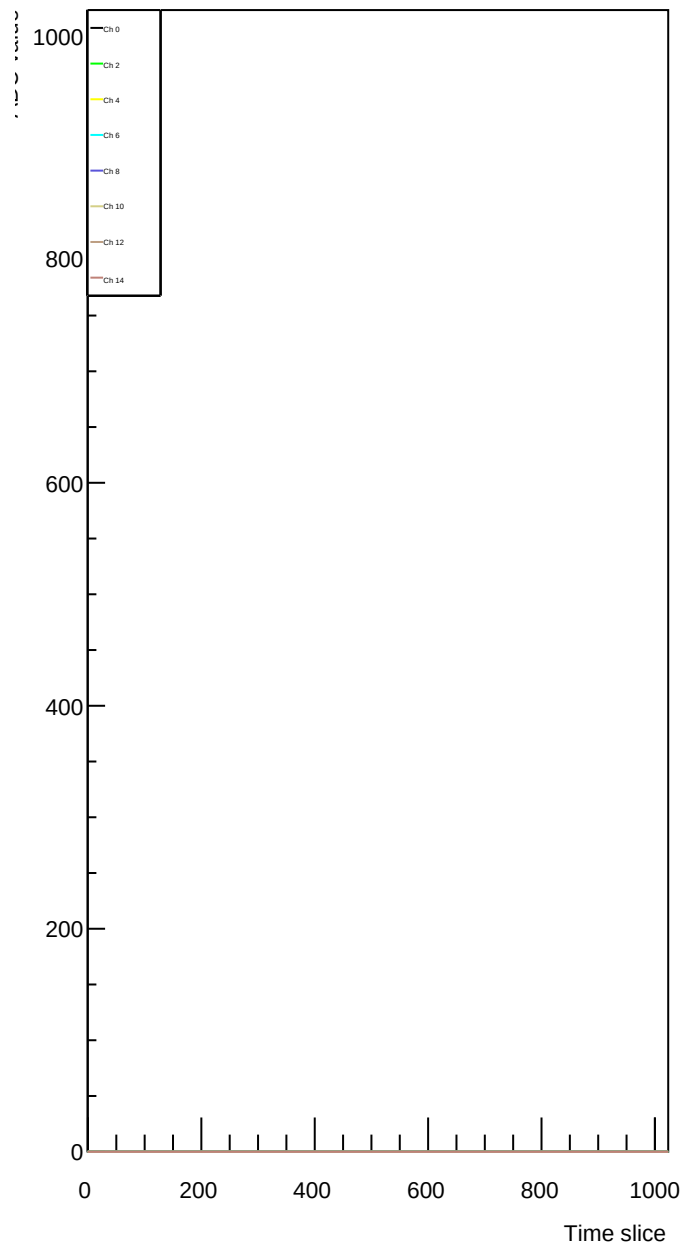
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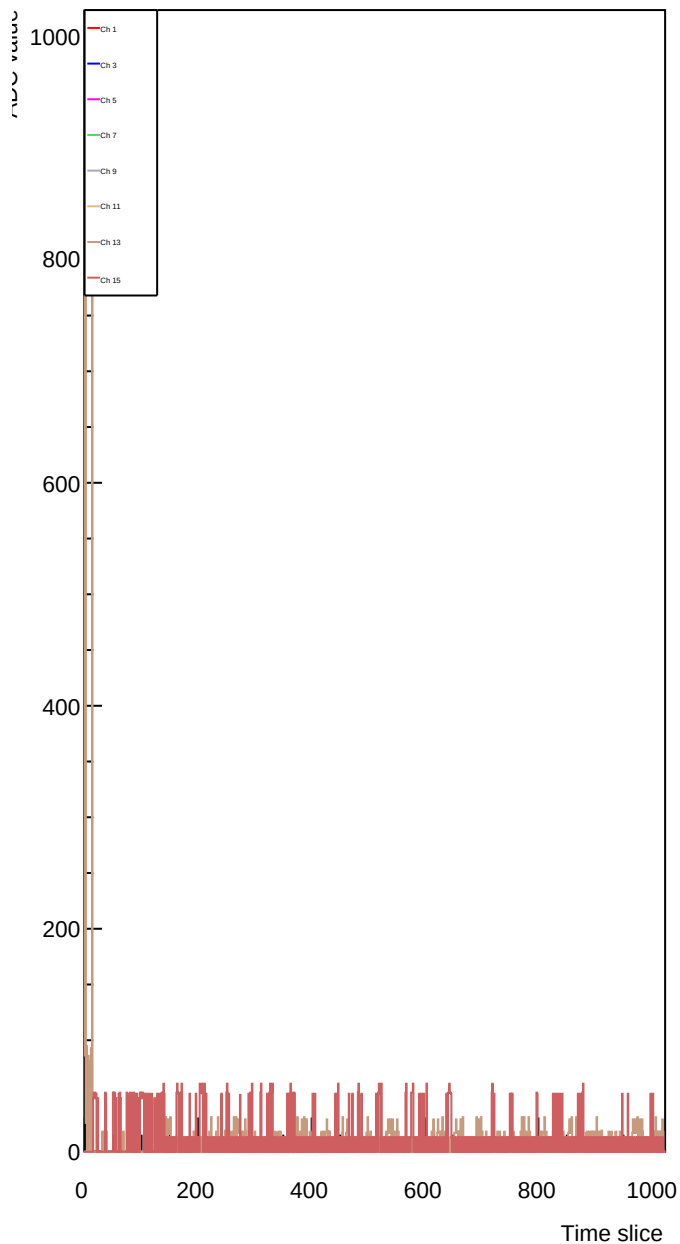
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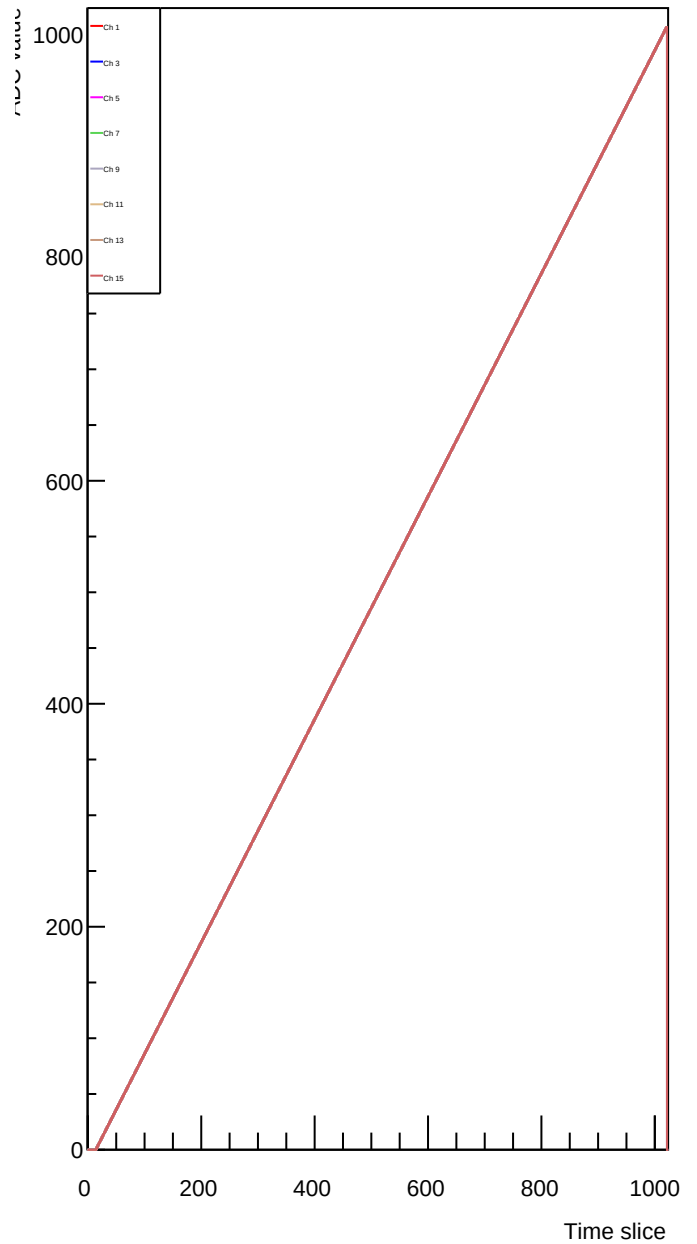
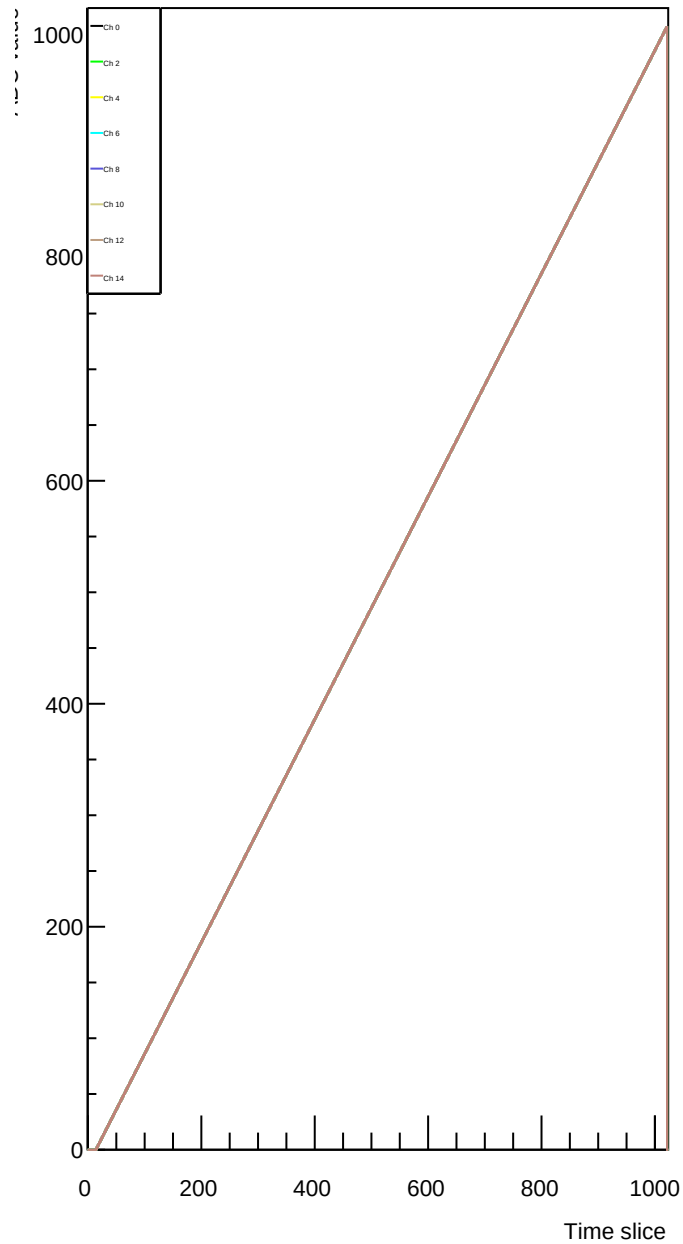
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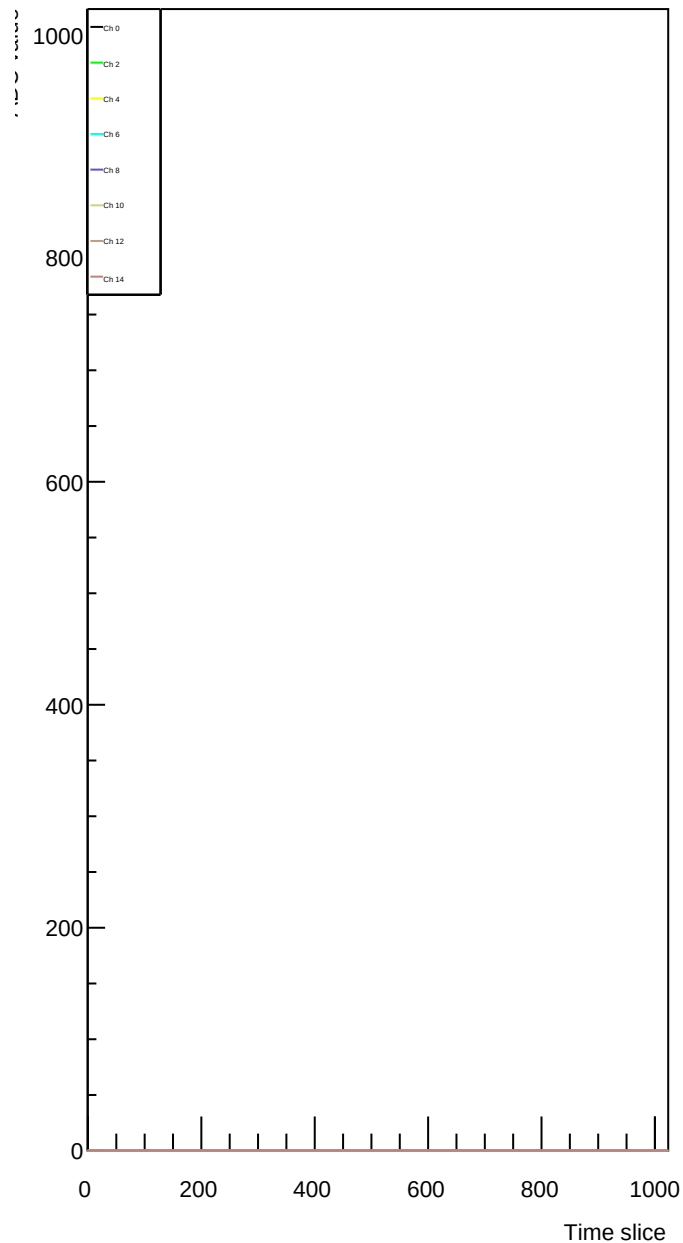
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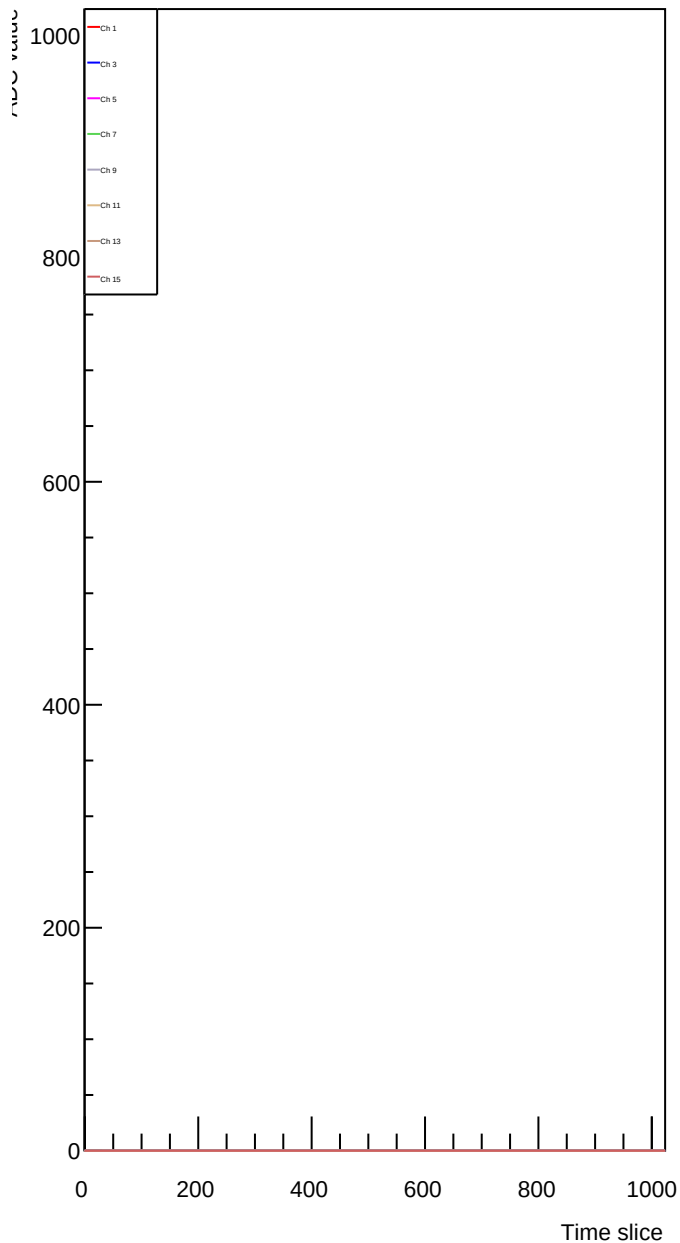
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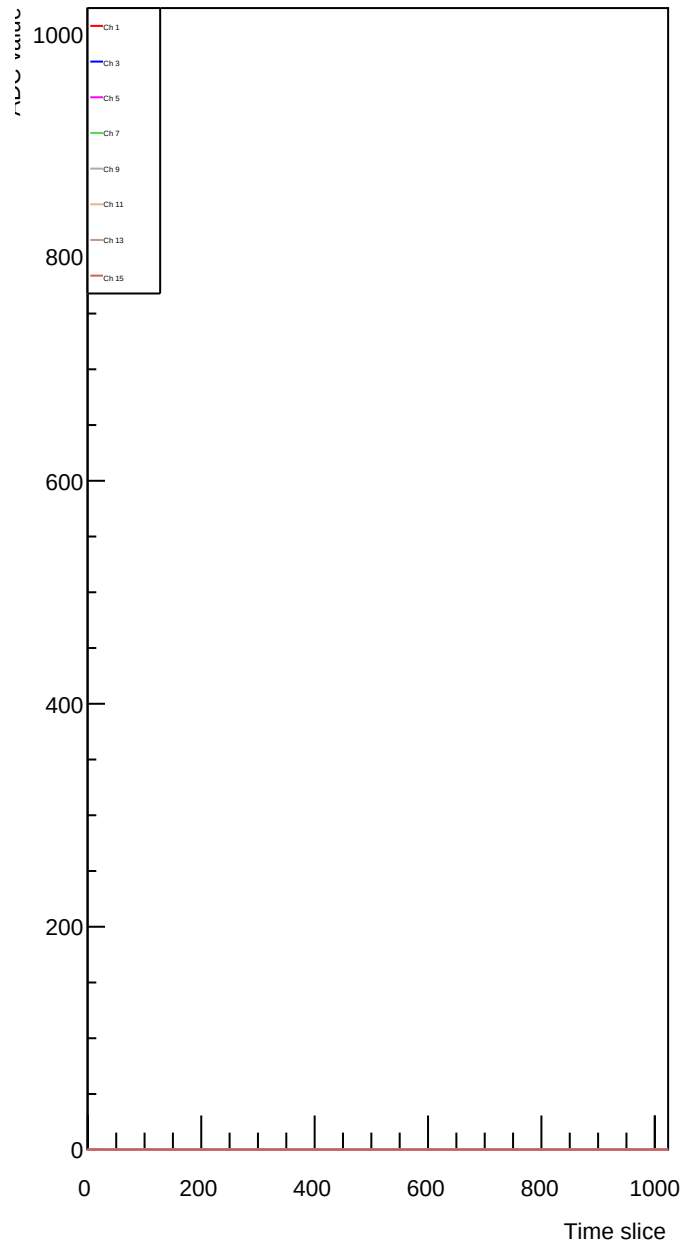
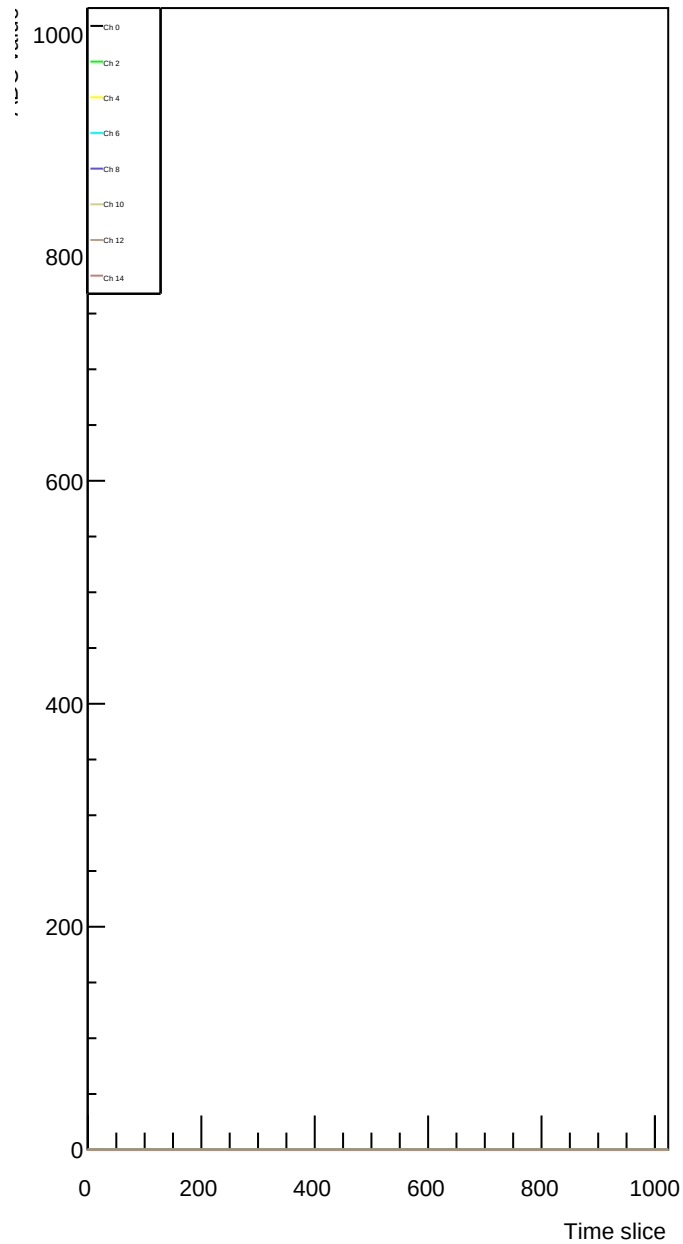
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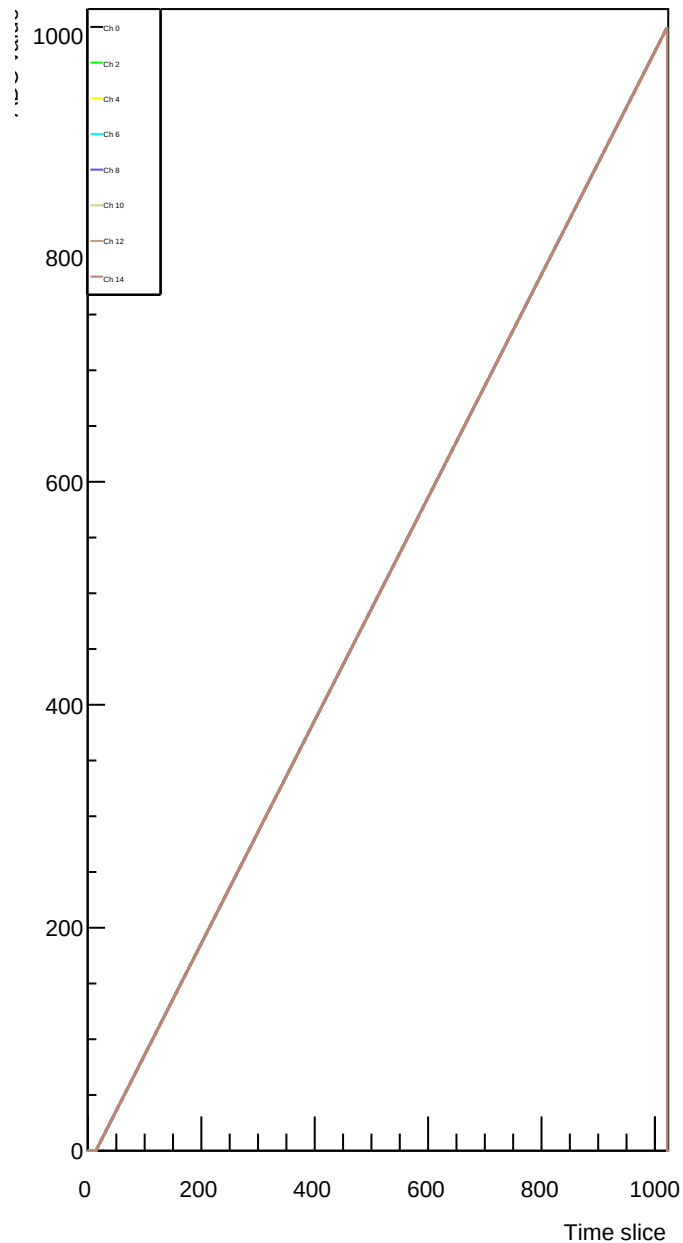
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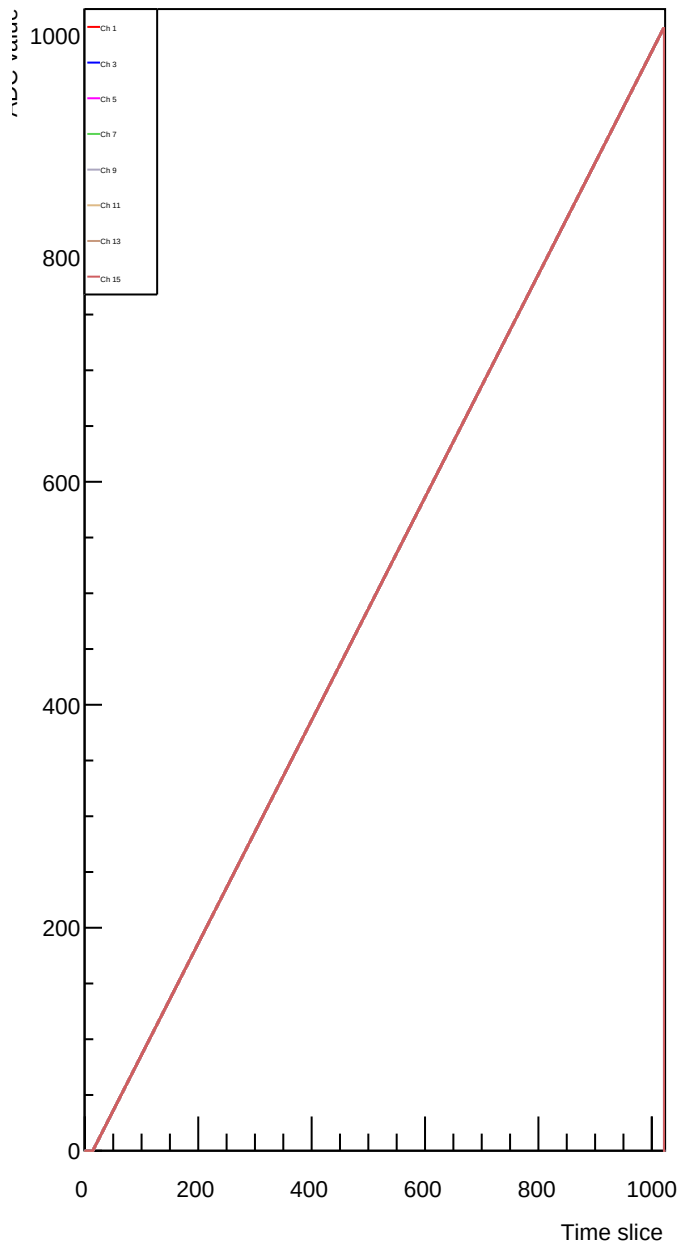
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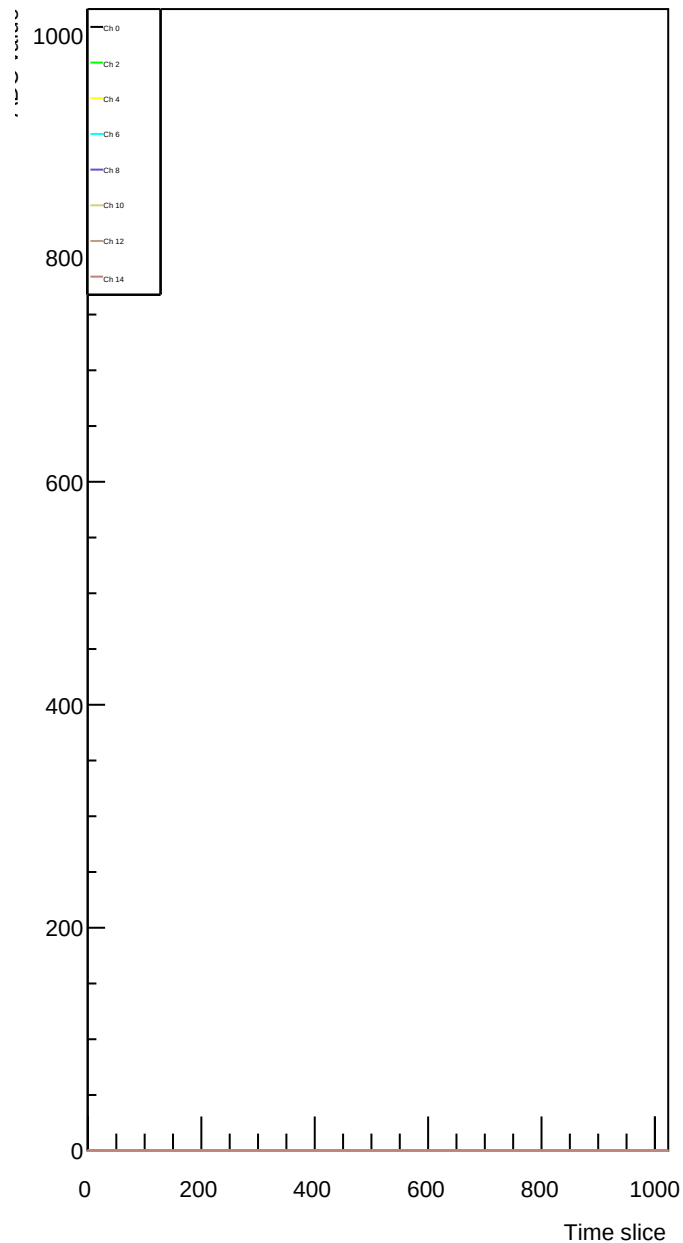
Run 0 event 20 mcm 0 chip 6 ramp2 even ch



Run 0 event 20 mcm 0 chip 6 ramp2 odd ch



Run 0 event 20 mcm 0 chip 7 ramp2 even ch



Run 0 event 20 mcm 0 chip 7 ramp2 odd ch

