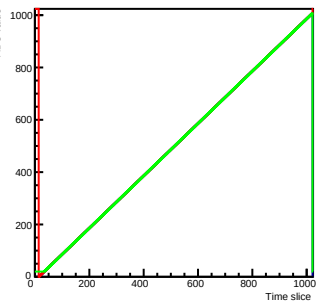
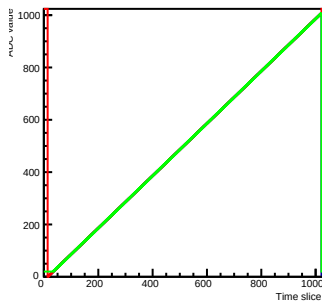


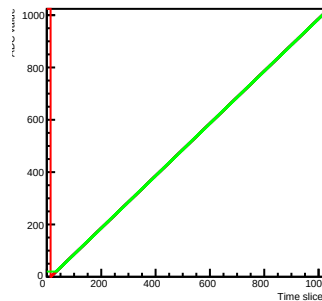
Run 0 Time mcm 0 chip 0 chan 0 debug



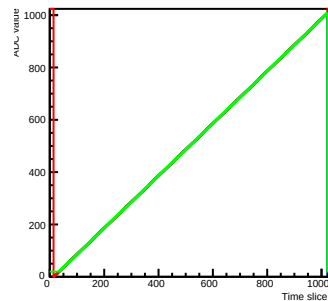
Run 0 Time mcm 0 chip 0 chan 1 debug



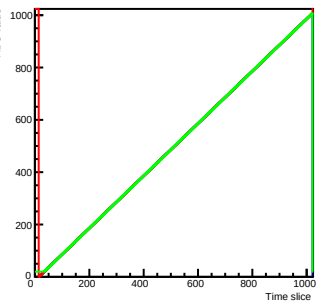
Run 0 Time mcm 0 chip 0 chan 2 debug



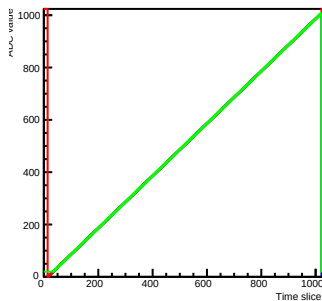
Run 0 Time mcm 0 chip 0 chan 3 debug



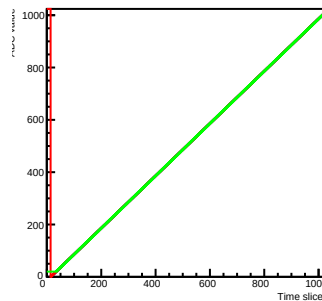
Run 0 Time mcm 0 chip 0 chan 4 debug



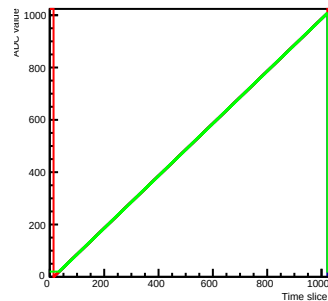
Run 0 Time mcm 0 chip 0 chan 5 debug



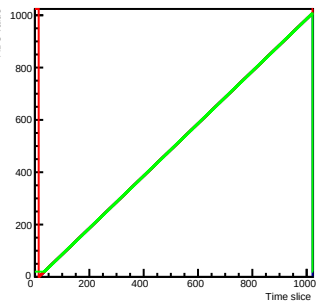
Run 0 Time mcm 0 chip 0 chan 6 debug



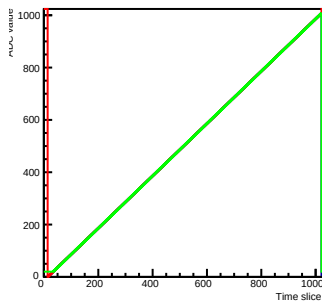
Run 0 Time mcm 0 chip 0 chan 7 debug



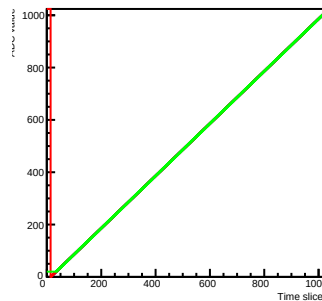
Run 0 Time mcm 0 chip 0 chan 8 debug



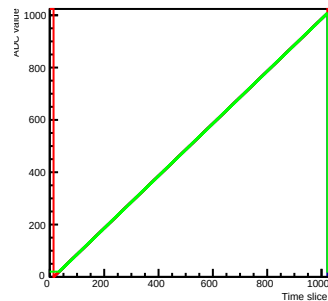
Run 0 Time mcm 0 chip 0 chan 9 debug



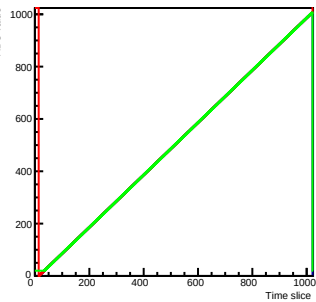
Run 0 Time mcm 0 chip 0 chan 10 debug



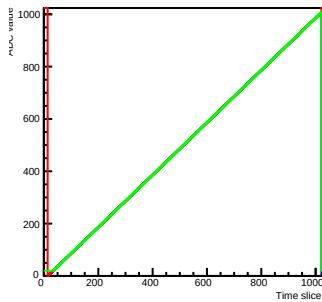
Run 0 Time mcm 0 chip 0 chan 11 debug



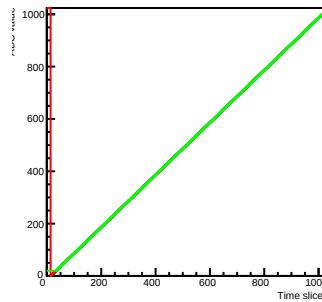
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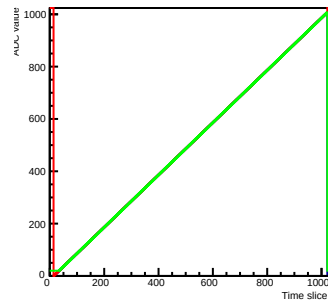
Run 0 Time mcm 0 chip 0 chan 13 debug



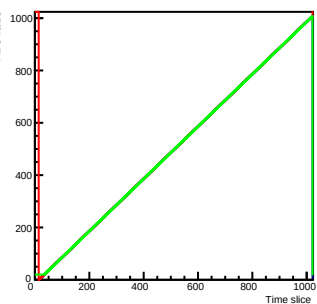
Run 0 Time mcm 0 chip 0 chan 14 debug



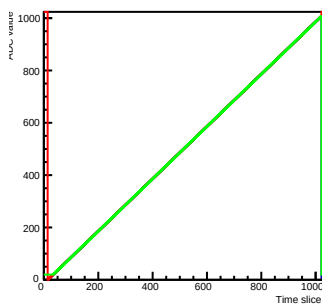
Run 0 Time mcm 0 chip 0 chan 15 debug



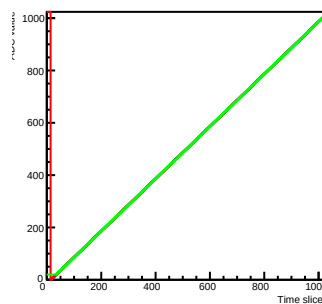
Run 0 Time mcm 0 chip 1 chan 0 debug



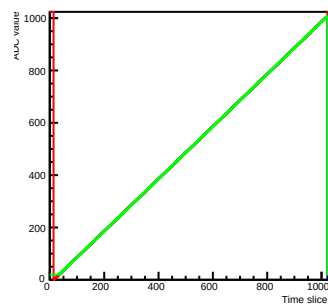
Run 0 Time mcm 0 chip 1 chan 1 debug



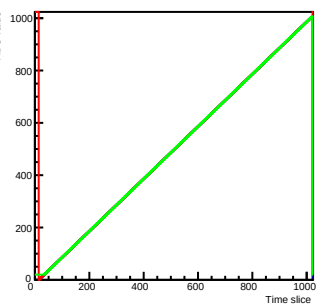
Run 0 Time mcm 0 chip 1 chan 2 debug



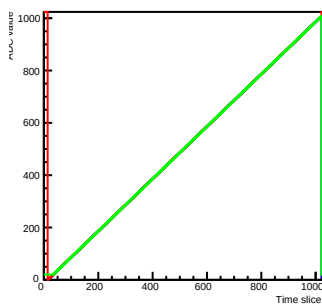
Run 0 Time mcm 0 chip 1 chan 3 debug



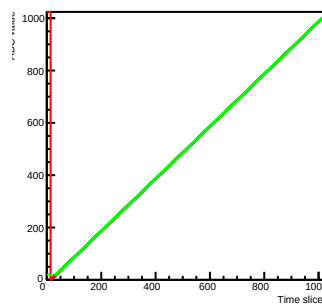
Run 0 Time mcm 0 chip 1 chan 4 debug



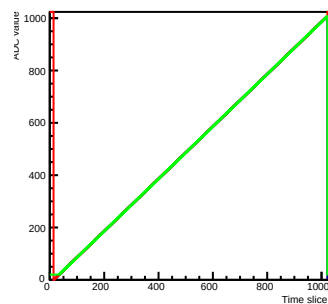
Run 0 Time mcm 0 chip 1 chan 5 debug



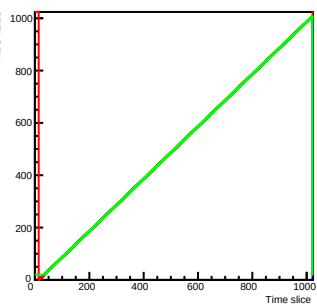
Run 0 Time mcm 0 chip 1 chan 6 debug



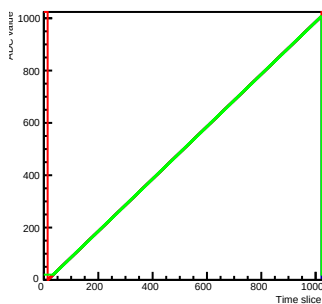
Run 0 Time mcm 0 chip 1 chan 7 debug



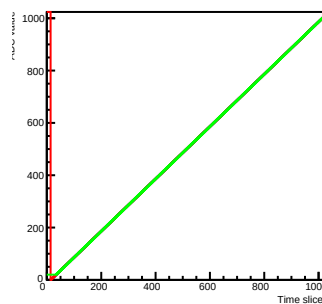
Run 0 Time mcm 0 chip 1 chan 8 debug



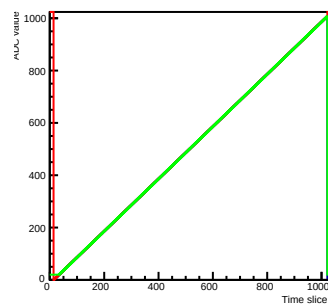
Run 0 Time mcm 0 chip 1 chan 9 debug



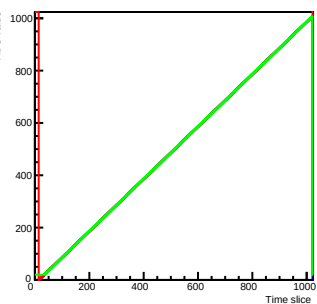
Run 0 Time mcm 0 chip 1 chan 10 debug



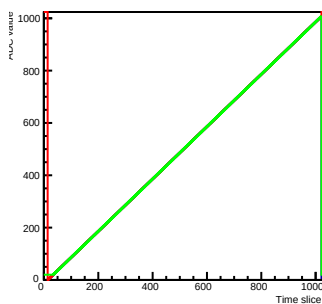
Run 0 Time mcm 0 chip 1 chan 11 debug



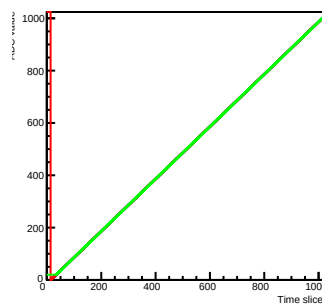
Run 0 Time mcm 0 chip 1 chan 12 debug



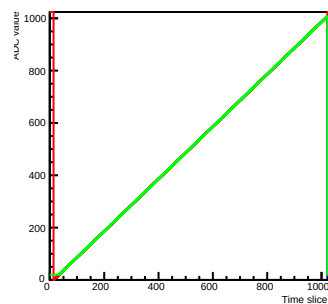
Run 0 Time mcm 0 chip 1 chan 13 debug



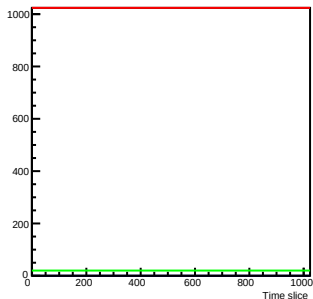
Run 0 Time mcm 0 chip 1 chan 14 debug



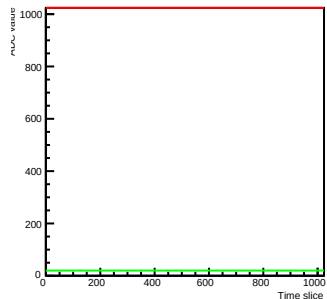
Run 0 Time mcm 0 chip 1 chan 15 debug



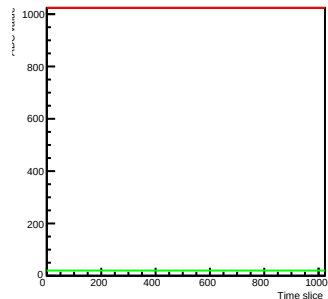
Run 0 Time mcm 0 chip 2 chan 0 debug



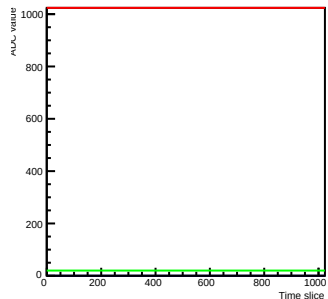
Run 0 Time mcm 0 chip 2 chan 1 debug



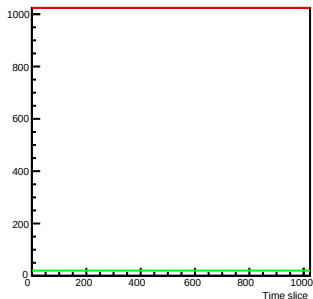
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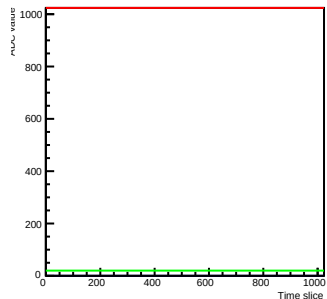
Run 0 Time mcm 0 chip 2 chan 3 debug



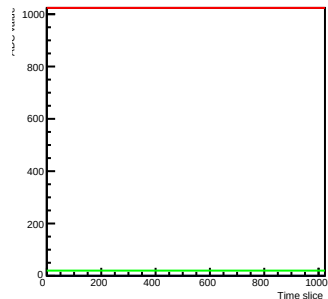
Run 0 Time mcm 0 chip 2 chan 4 debug



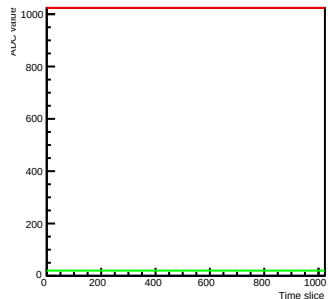
Run 0 Time mcm 0 chip 2 chan 5 debug



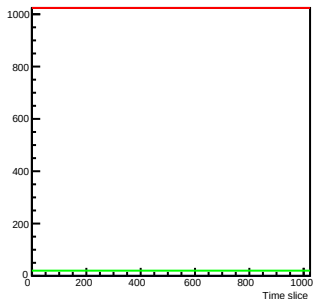
Run 0 Time mcm 0 chip 2 chan 6 debug



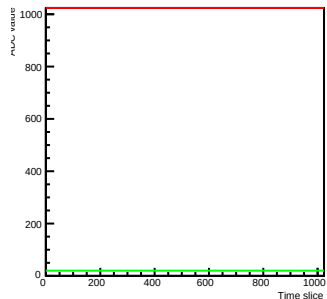
Run 0 Time mcm 0 chip 2 chan 7 debug



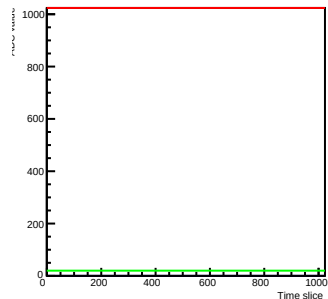
Run 0 Time mcm 0 chip 2 chan 8 debug



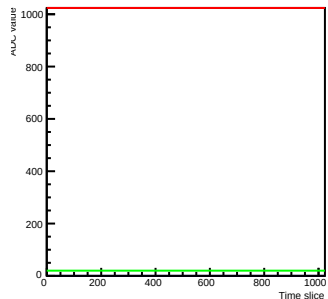
Run 0 Time mcm 0 chip 2 chan 9 debug



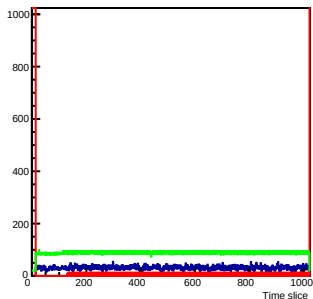
Run 0 Time mcm 0 chip 2 chan 10 debug



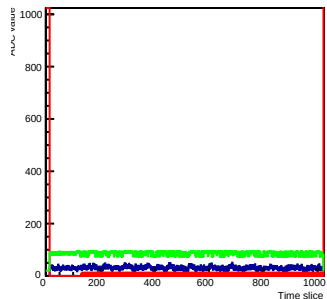
Run 0 Time mcm 0 chip 2 chan 11 debug



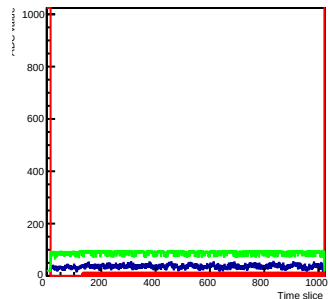
Run 0 Time mcm 0 chip 2 chan 12 debug



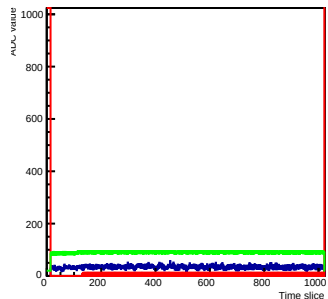
Run 0 Time mcm 0 chip 2 chan 13 debug



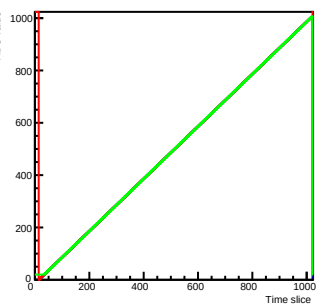
Run 0 Time mcm 0 chip 2 chan 14 debug



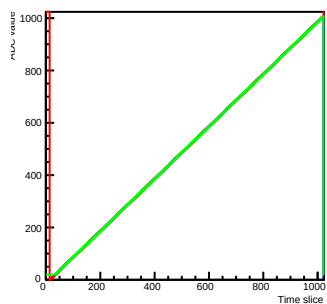
Run 0 Time mcm 0 chip 2 chan 15 debug



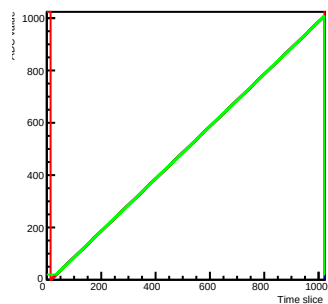
Run 0 Time mcm 0 chip 3 chan 0 debug



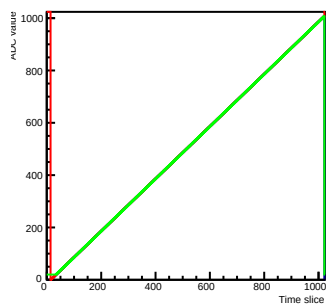
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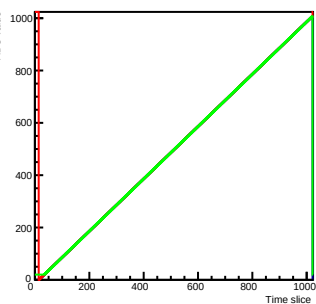
Run 0 Time mcm 0 chip 3 chan 2 debug



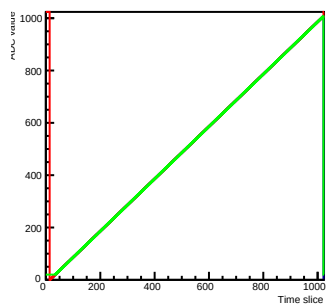
Run 0 Time mcm 0 chip 3 chan 3 debug



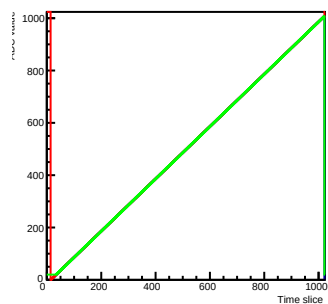
Run 0 Time mcm 0 chip 3 chan 4 debug



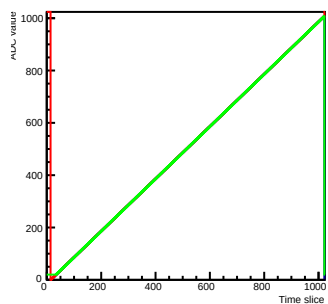
Run 0 Time mcm 0 chip 3 chan 5 debug



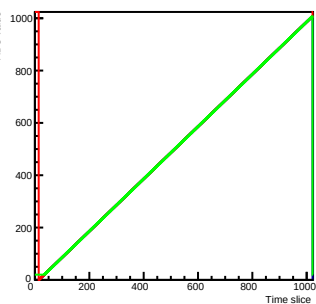
Run 0 Time mcm 0 chip 3 chan 6 debug



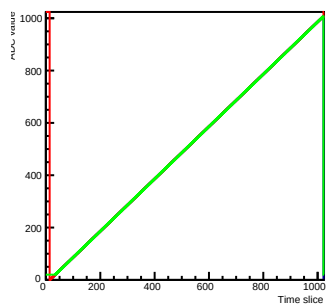
Run 0 Time mcm 0 chip 3 chan 7 debug



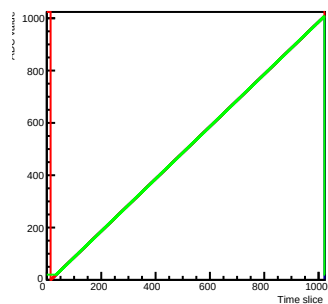
Run 0 Time mcm 0 chip 3 chan 8 debug



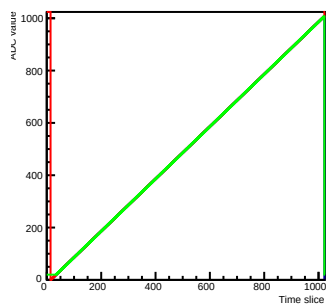
Run 0 Time mcm 0 chip 3 chan 9 debug



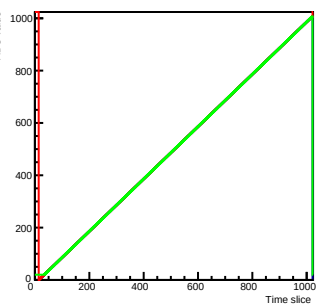
Run 0 Time mcm 0 chip 3 chan 10 debug



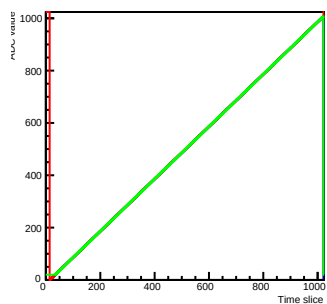
Run 0 Time mcm 0 chip 3 chan 11 debug



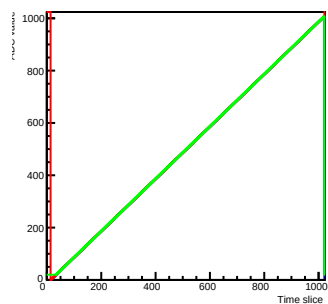
Run 0 Time mcm 0 chip 3 chan 12 debug



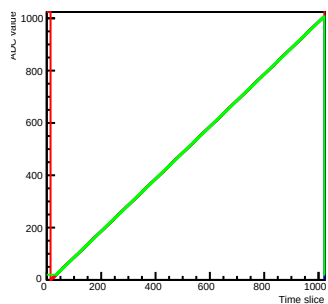
Run 0 Time mcm 0 chip 3 chan 13 debug

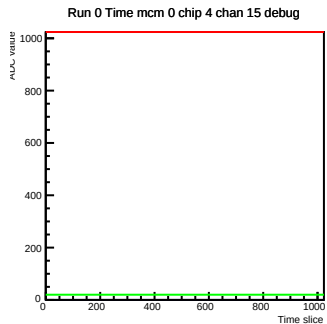
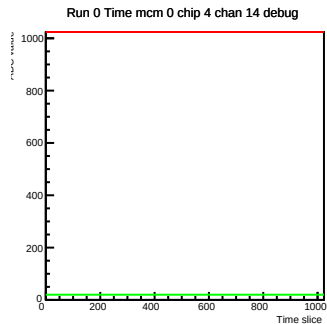
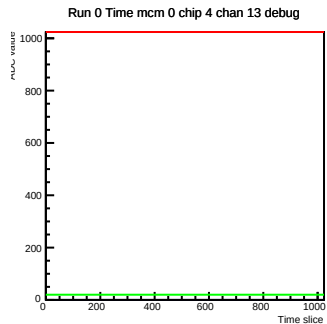
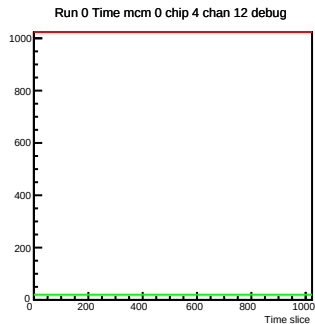
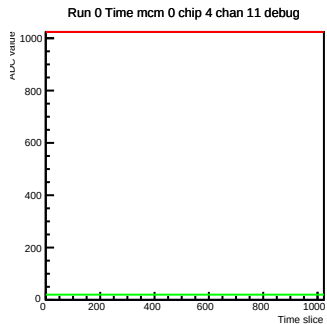
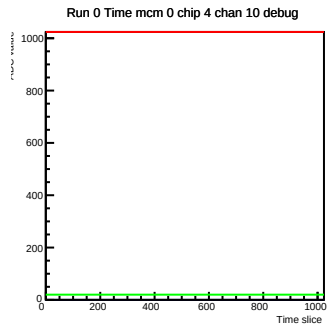
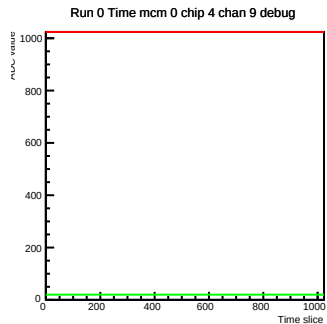
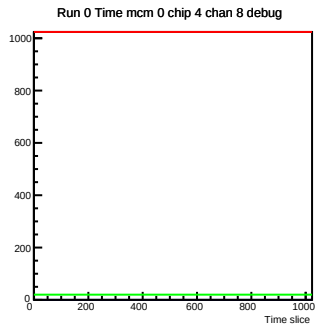
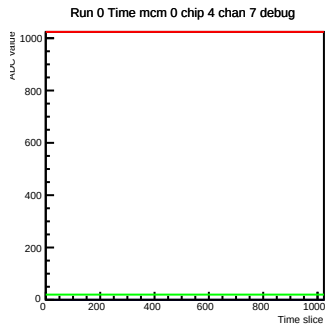
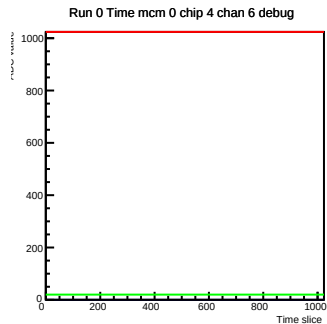
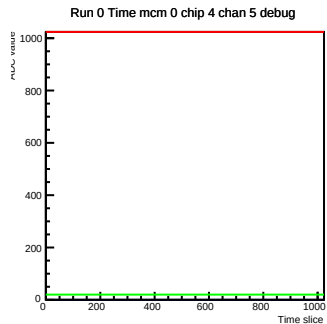
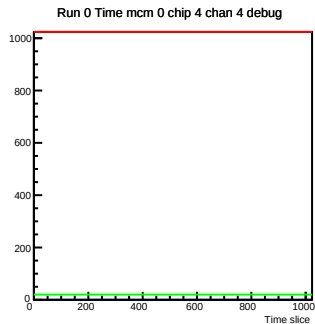
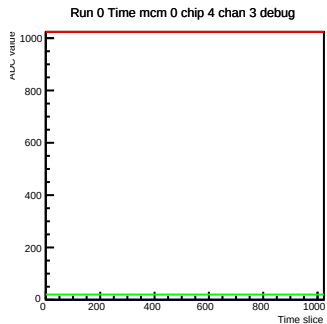
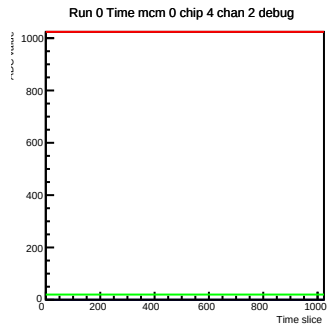
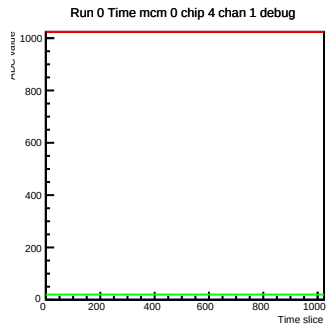
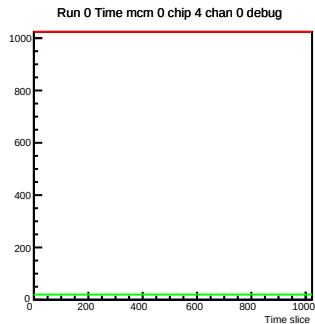


Run 0 Time mcm 0 chip 3 chan 14 debug

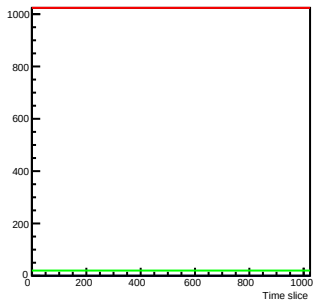


Run 0 Time mcm 0 chip 3 chan 15 debug

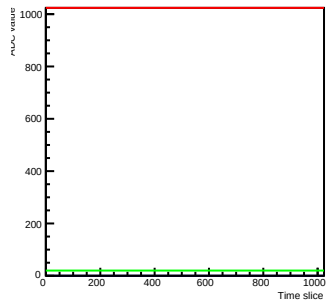




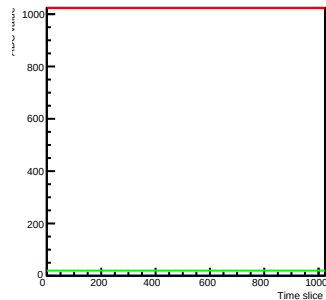
Run 0 Time mcm 0 chip 5 chan 0 debug



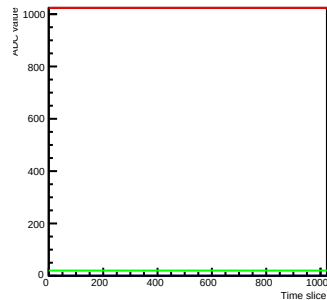
Run 0 Time mcm 0 chip 5 chan 1 debug



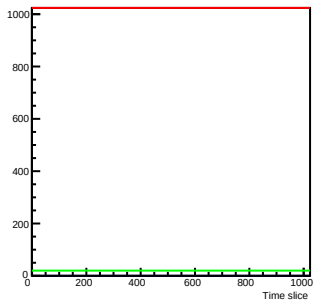
Run 0 Time mcm 0 chip 5 chan 2 debug



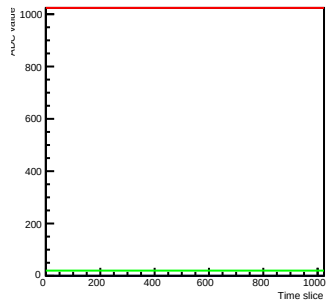
Run 0 Time mcm 0 chip 5 chan 3 debug



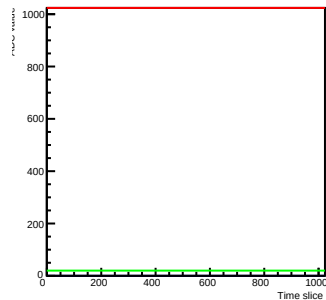
Run 0 Time mcm 0 chip 5 chan 4 debug



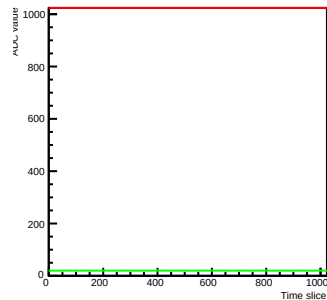
Run 0 Time mcm 0 chip 5 chan 5 debug



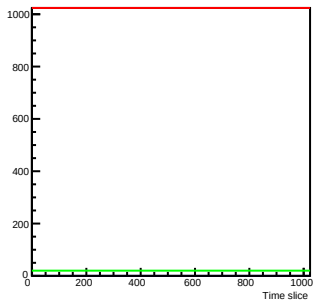
Run 0 Time mcm 0 chip 5 chan 6 debug



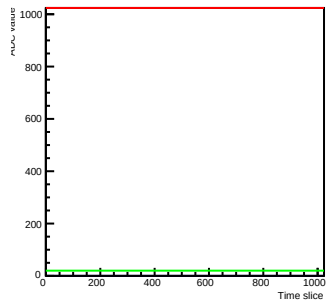
Run 0 Time mcm 0 chip 5 chan 7 debug



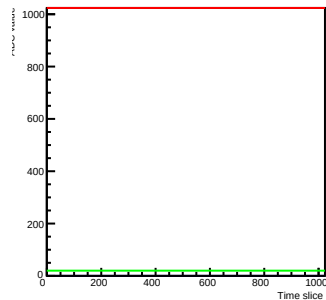
Run 0 Time mcm 0 chip 5 chan 8 debug



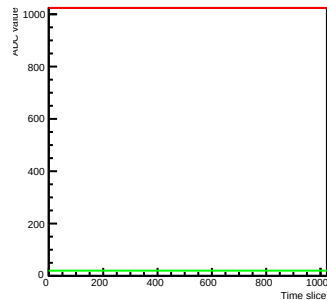
Run 0 Time mcm 0 chip 5 chan 9 debug



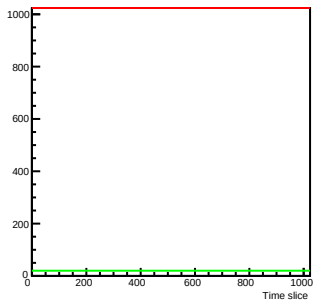
Run 0 Time mcm 0 chip 5 chan 10 debug



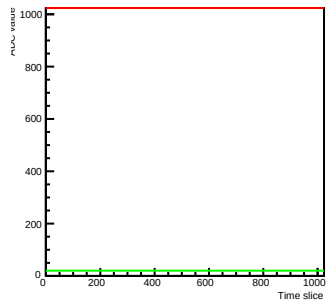
Run 0 Time mcm 0 chip 5 chan 11 debug



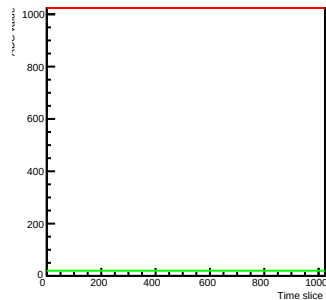
Run 0 Time mcm 0 chip 5 chan 12 debug



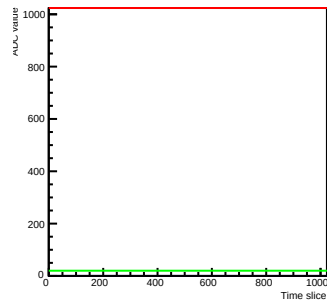
Run 0 Time mcm 0 chip 5 chan 13 debug



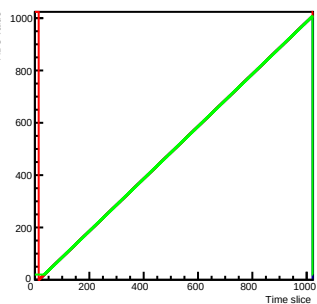
Run 0 Time mcm 0 chip 5 chan 14 debug



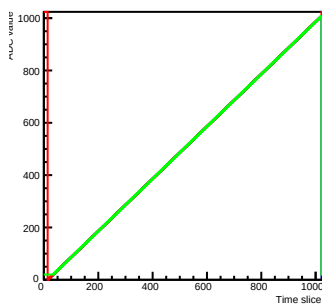
Run 0 Time mcm 0 chip 5 chan 15 debug



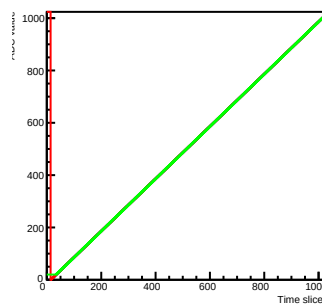
Run 0 Time mcm 0 chip 6 chan 0 debug



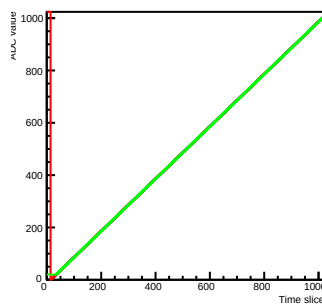
Run 0 Time mcm 0 chip 6 chan 1 debug



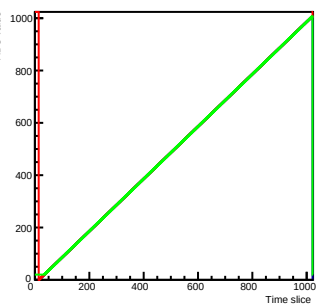
Run 0 Time mcm 0 chip 6 chan 2 debug



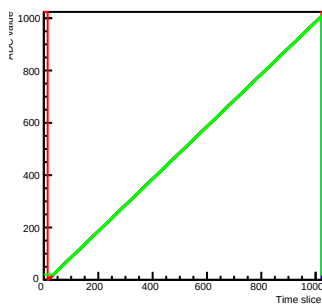
Run 0 Time mcm 0 chip 6 chan 3 debug



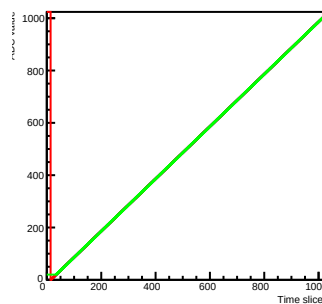
Run 0 Time mcm 0 chip 6 chan 4 debug



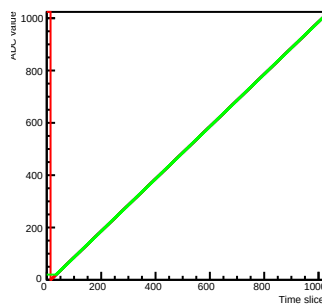
Run 0 Time mcm 0 chip 6 chan 5 debug



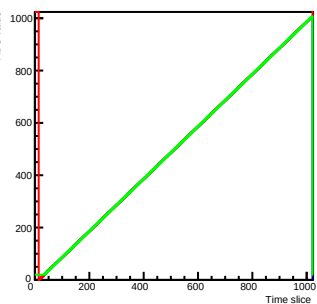
Run 0 Time mcm 0 chip 6 chan 6 debug



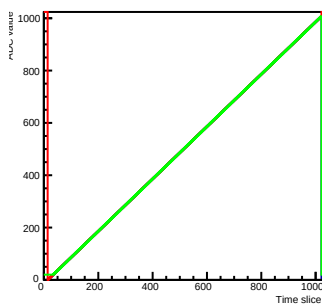
Run 0 Time mcm 0 chip 6 chan 7 debug



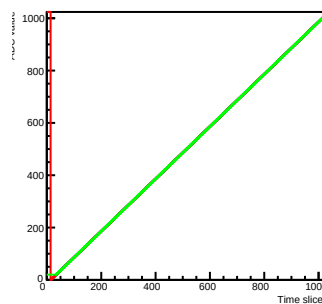
Run 0 Time mcm 0 chip 6 chan 8 debug



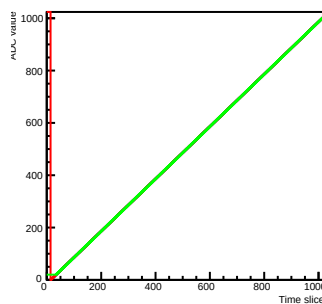
Run 0 Time mcm 0 chip 6 chan 9 debug



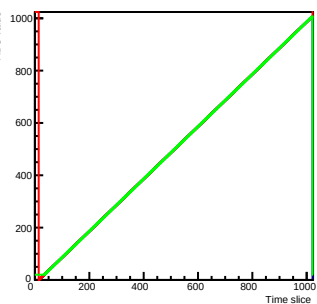
Run 0 Time mcm 0 chip 6 chan 10 debug



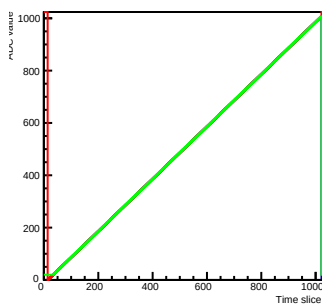
Run 0 Time mcm 0 chip 6 chan 11 debug



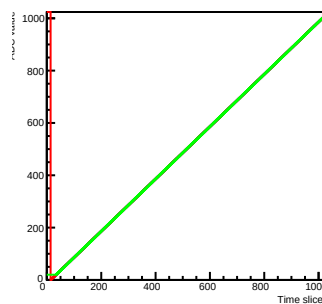
Run 0 Time mcm 0 chip 6 chan 12 debug



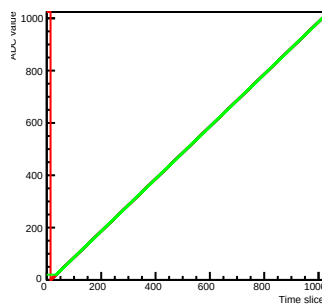
Run 0 Time mcm 0 chip 6 chan 13 debug



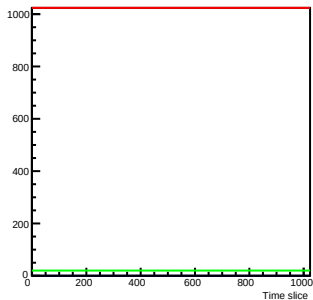
Run 0 Time mcm 0 chip 6 chan 14 debug



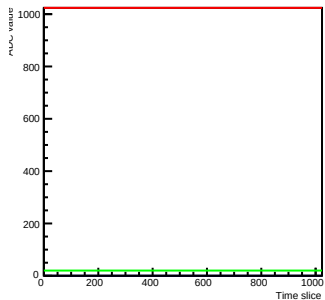
Run 0 Time mcm 0 chip 6 chan 15 debug



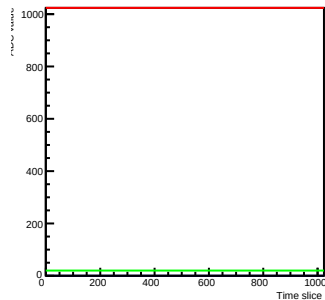
Run 0 Time mcm 0 chip 7 chan 0 debug



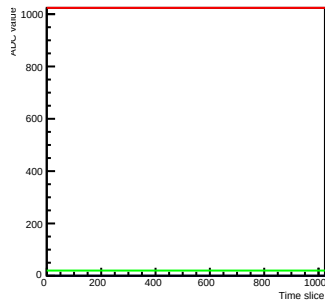
Run 0 Time mcm 0 chip 7 chan 1 debug



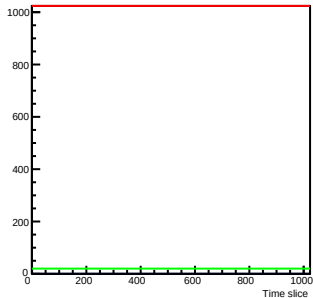
Run 0 Time mcm 0 chip 7 chan 2 debug



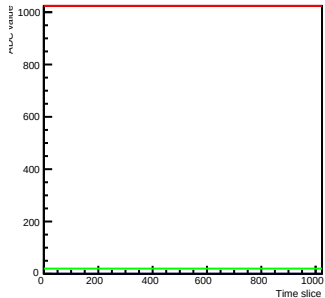
Run 0 Time mcm 0 chip 7 chan 3 debug



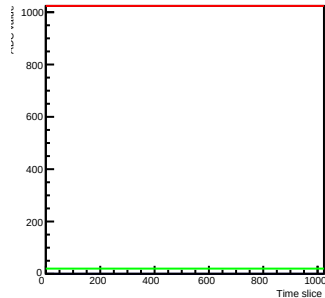
Run 0 Time mcm 0 chip 7 chan 4 debug



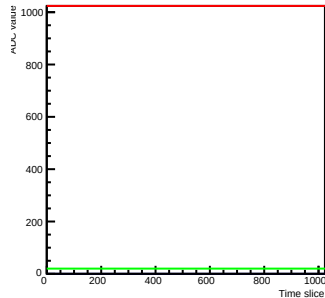
Run 0 Time mcm 0 chip 7 chan 5 debug



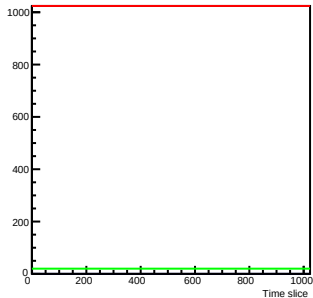
Run 0 Time mcm 0 chip 7 chan 6 debug



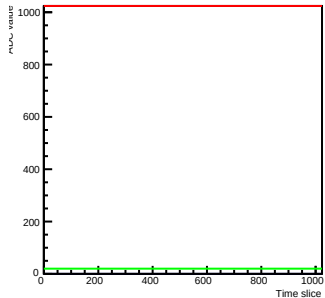
Run 0 Time mcm 0 chip 7 chan 7 debug



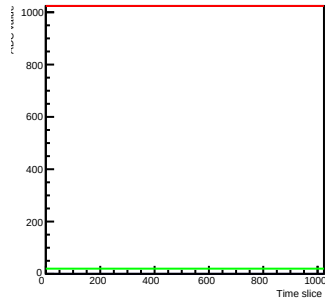
Run 0 Time mcm 0 chip 7 chan 8 debug



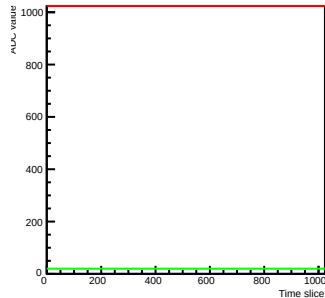
Run 0 Time mcm 0 chip 7 chan 9 debug



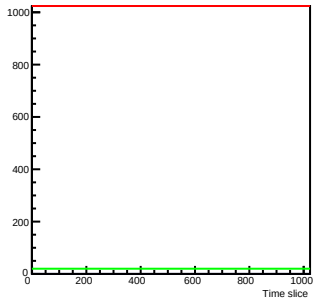
Run 0 Time mcm 0 chip 7 chan 10 debug



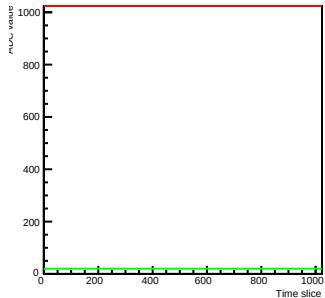
Run 0 Time mcm 0 chip 7 chan 11 debug



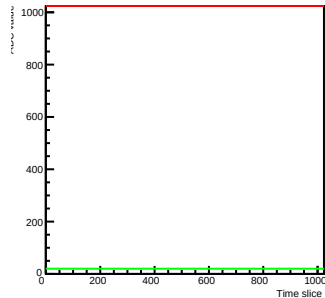
Run 0 Time mcm 0 chip 7 chan 12 debug



Run 0 Time mcm 0 chip 7 chan 13 debug



Run 0 Time mcm 0 chip 7 chan 14 debug



Run 0 Time mcm 0 chip 7 chan 15 debug

