

Batch 3017

Chip	2018	cern testboard	Retest 20221219
65	ch2 high-low	ch2 high low	ch2 high low
63	ch9 high-low	ch9 high-low	ch9 high low g1s0p1
62	ch1 high-low	ch1 high-low	ch1 high-low g0s0p1, g1s0p1,g2s0p1,g3s0p1
61	OK	OK	
60	ch11 high-low	ch11 high-low	ch11 high-low g0s0p1, g1s0p1,g2s0p1,g3,s0p1
57	ch12 high low (weak)	ch1 bit error(?)	ch1 ramp
55	ch15 bit error, also ramp	ch15 bit error, also ramp	ch15 bit error, also ramp2
52	ch9 high-low	ch9 bit error, high-low weak	ch9 high-low weak g01s0p1
40	ch14 bit error?	Chip not working, readout errors.	OK
38	ch2 bit error, also ramp	ch2 bit error, also ramp. Ch4 suspect.	ch2 bit error, also ramp

Batch 1918

Chip	2018	Retest 20221219
16	Current 0.48A. Do not get any events. Register test OK.	OK
29	preamp1gain0shaper0decay0.5: rms ch1= 7.45, ch5 = 4.44	can not test preamp1. OK
30	Short GND IOPOWER/GND	NOT TESTED
31	No connection on input signals	No current.
38	preamp1gain2shaper0decay0.5: rms ch13=3.877 preamp1gain2shaper0decay0.5: rms ch13=3.466	can not test preamp1. OK can not test preamp1. OK
39	preamp1gain0shaper0decay0.5: rms ch7=1.740 ramp, som bit missing: ch10 (see also chip43)	Ramp, channel 10. ramp2, single error
43	ramp: CH 13 have time=adc, value 504 = 440 ==> diff of 64	NO CURRENT NOTE: looks as chip 31 again??
55	pol0preamp0gain0shaper0decay0: rms ch0=1.687 preamp1gain0shaper0decay0.5: rms ch12=2.754	Bits 7,8,9 missing ch1, no pulses seen in pedsub,zerosup. Ramp1 ch 13. NOTE: looks as previous test of chip 43??
60	pol0preamp0gain0shaper0decay0.5: rms ch3=1.350	g3s0p1: high pedestal channel 10=96 no pedestal run for p0.
63	preamp1gain2shaper0decay0.5: rms ch2=1.698 delay, check gain dependence?	Can not test peamp1. OK.
64	--polarity 0 --preamp 0 --gain 0 --shaper 0 CH 8&9 NOISY --preamp 1 --gain 0 --shaper 0 CH9 has spikes: ch9-64-1-30645.pdf --preamp 1 --gain 0 --shaper 0 CH1 has spikes: ch1-64-1-30659.pdf TESTGAIN PULSER CHIP 64 HIGH GAIN	OK
75	no clock?	OK

NOTE: chip 43 + 55 have wrong chip numbering in test??