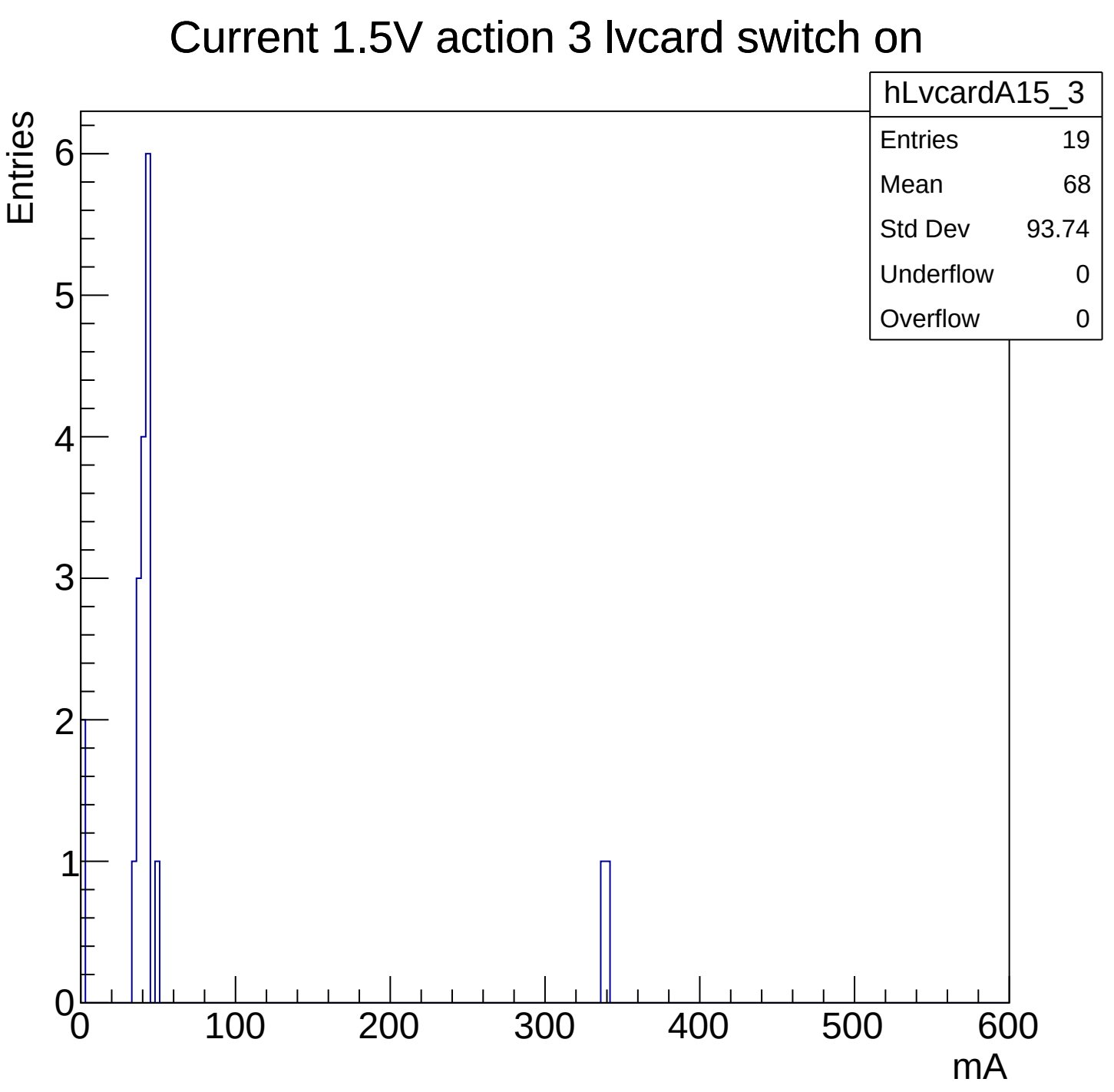
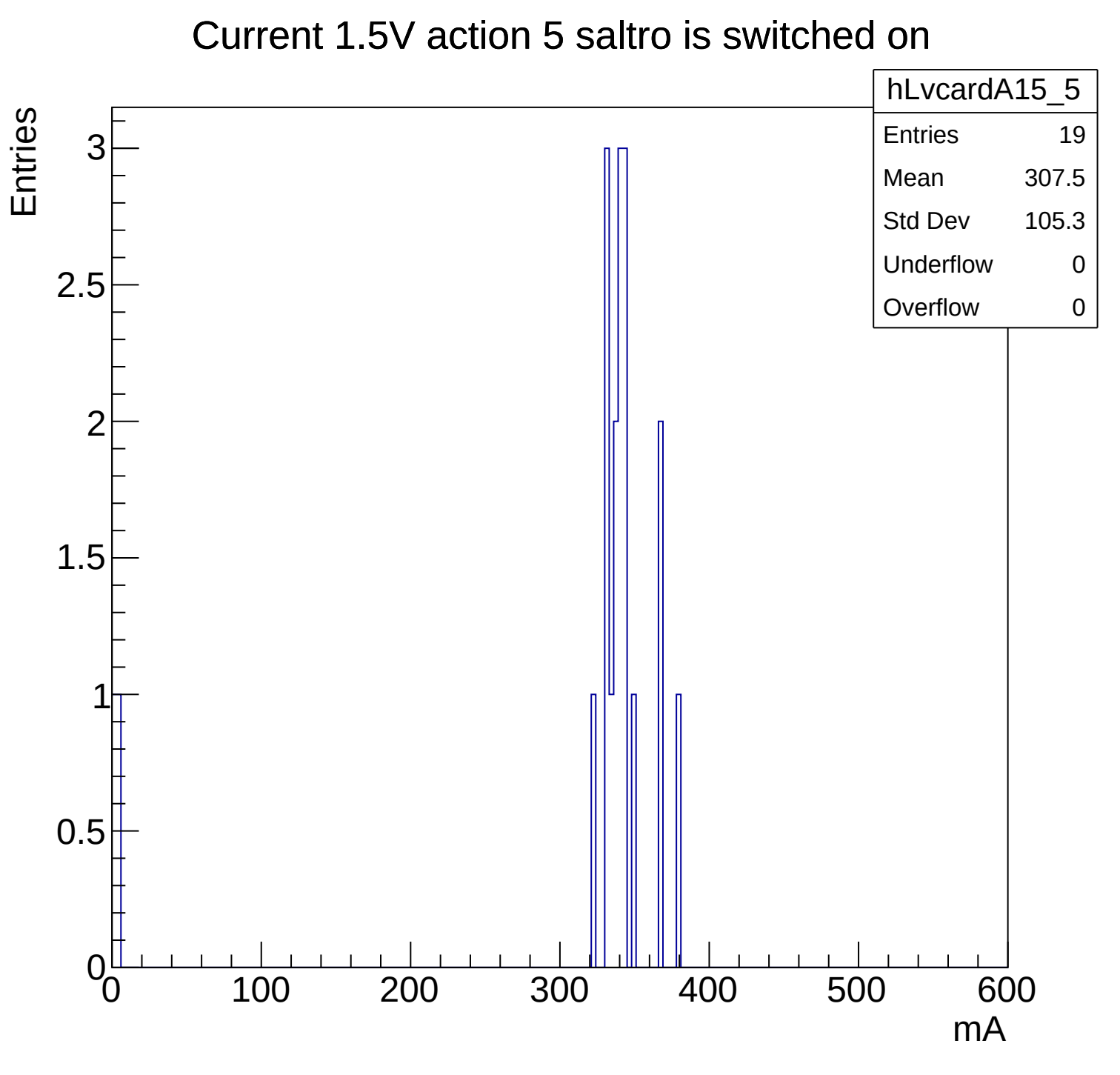
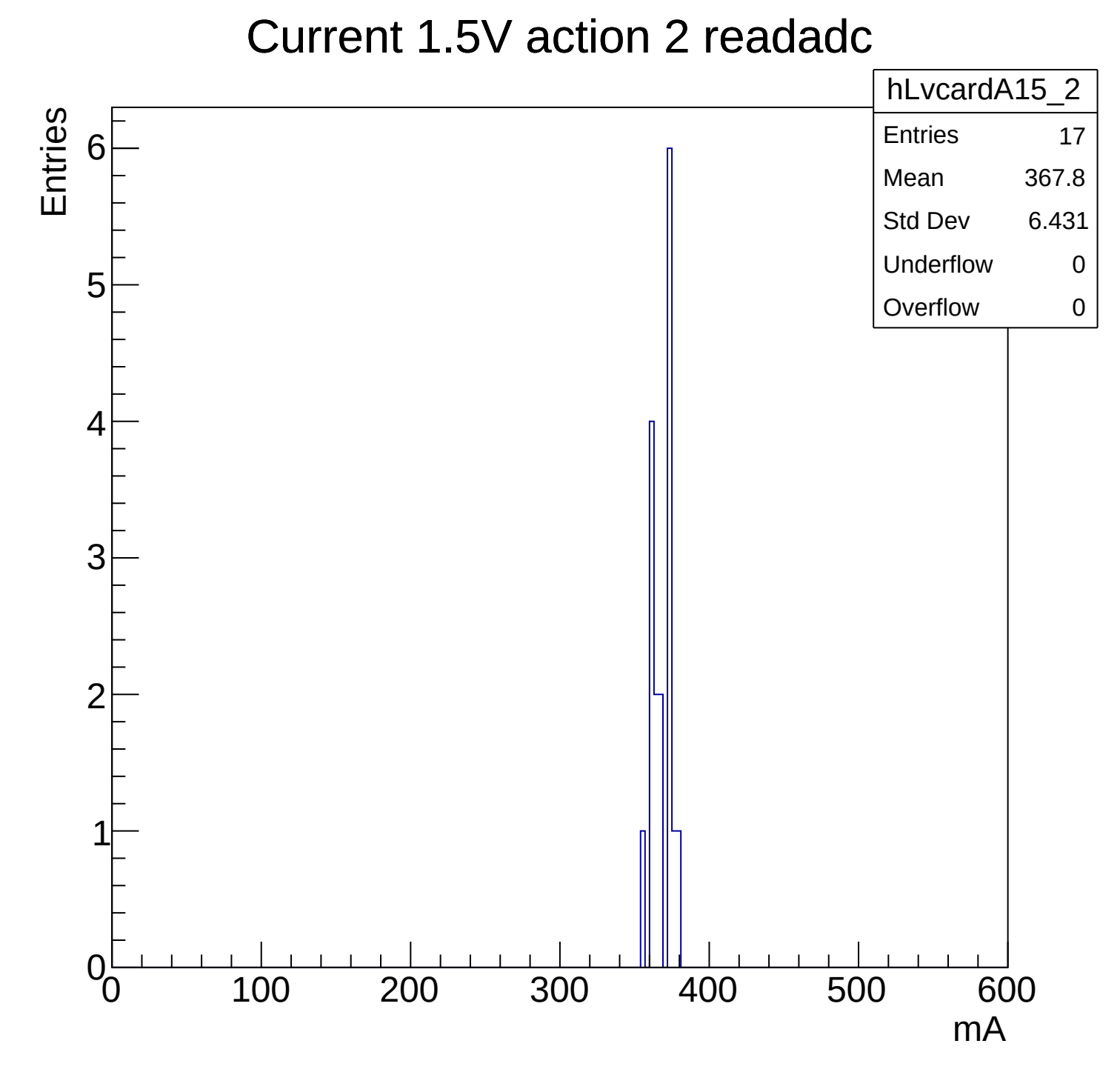
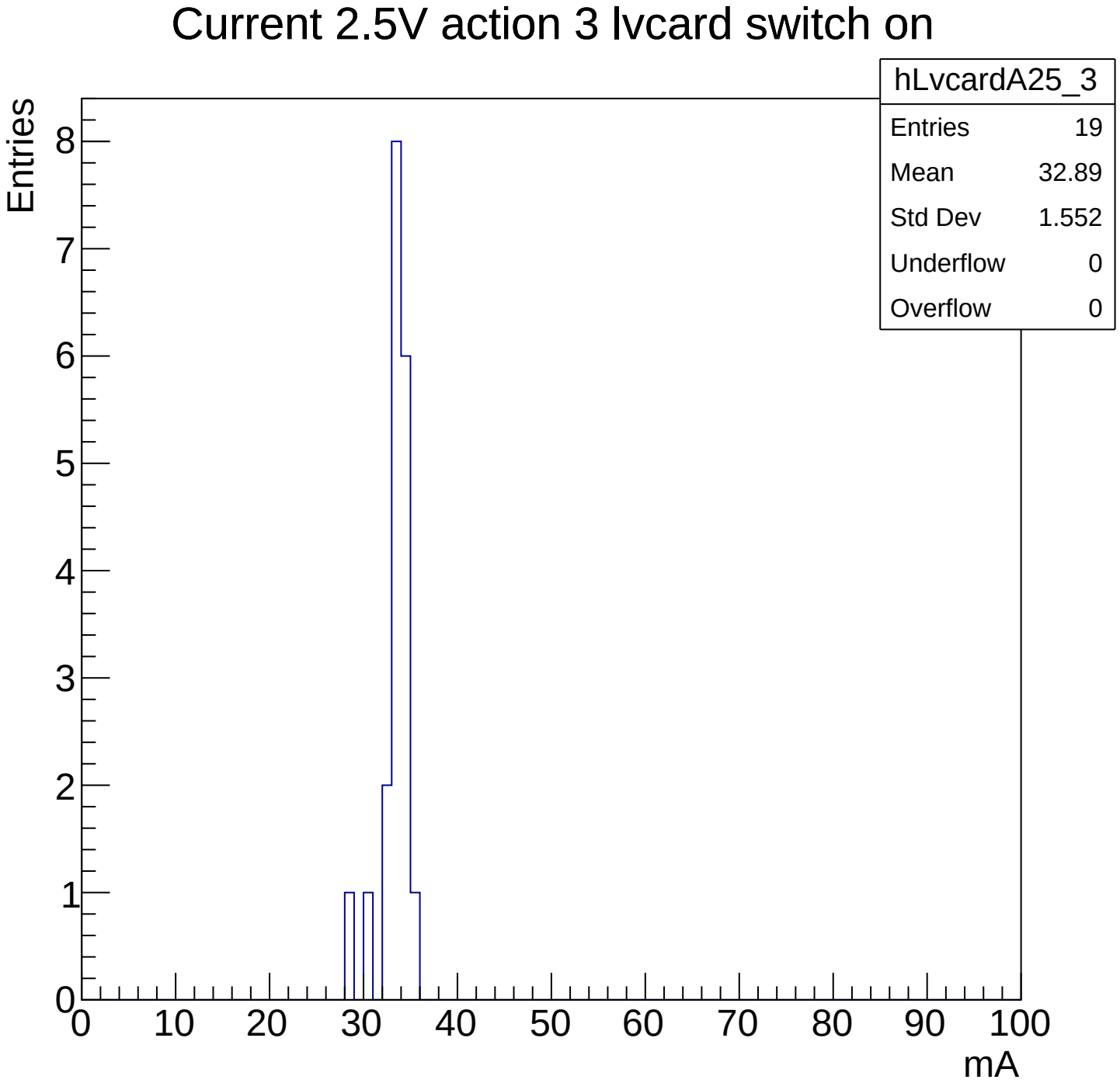
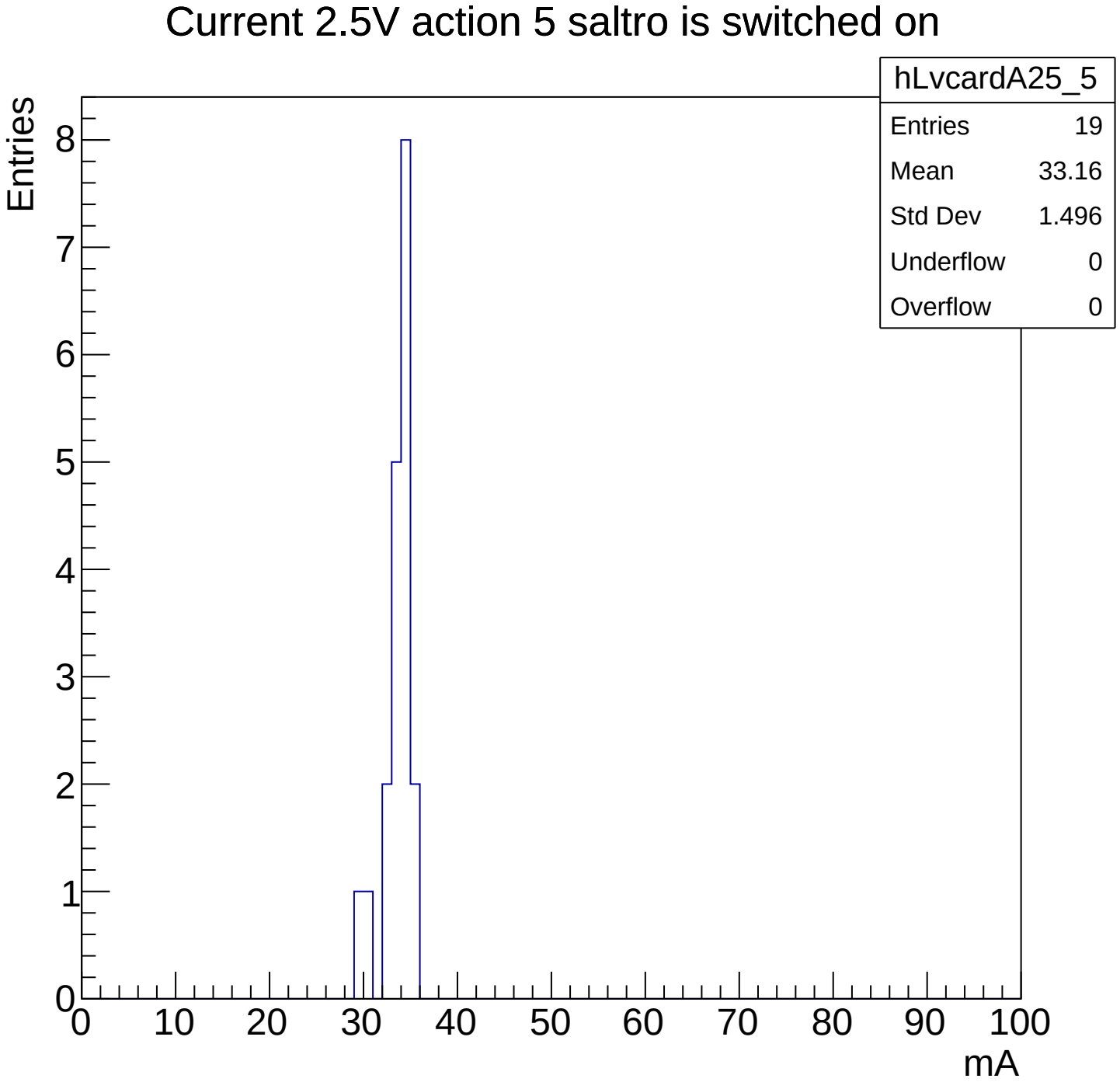
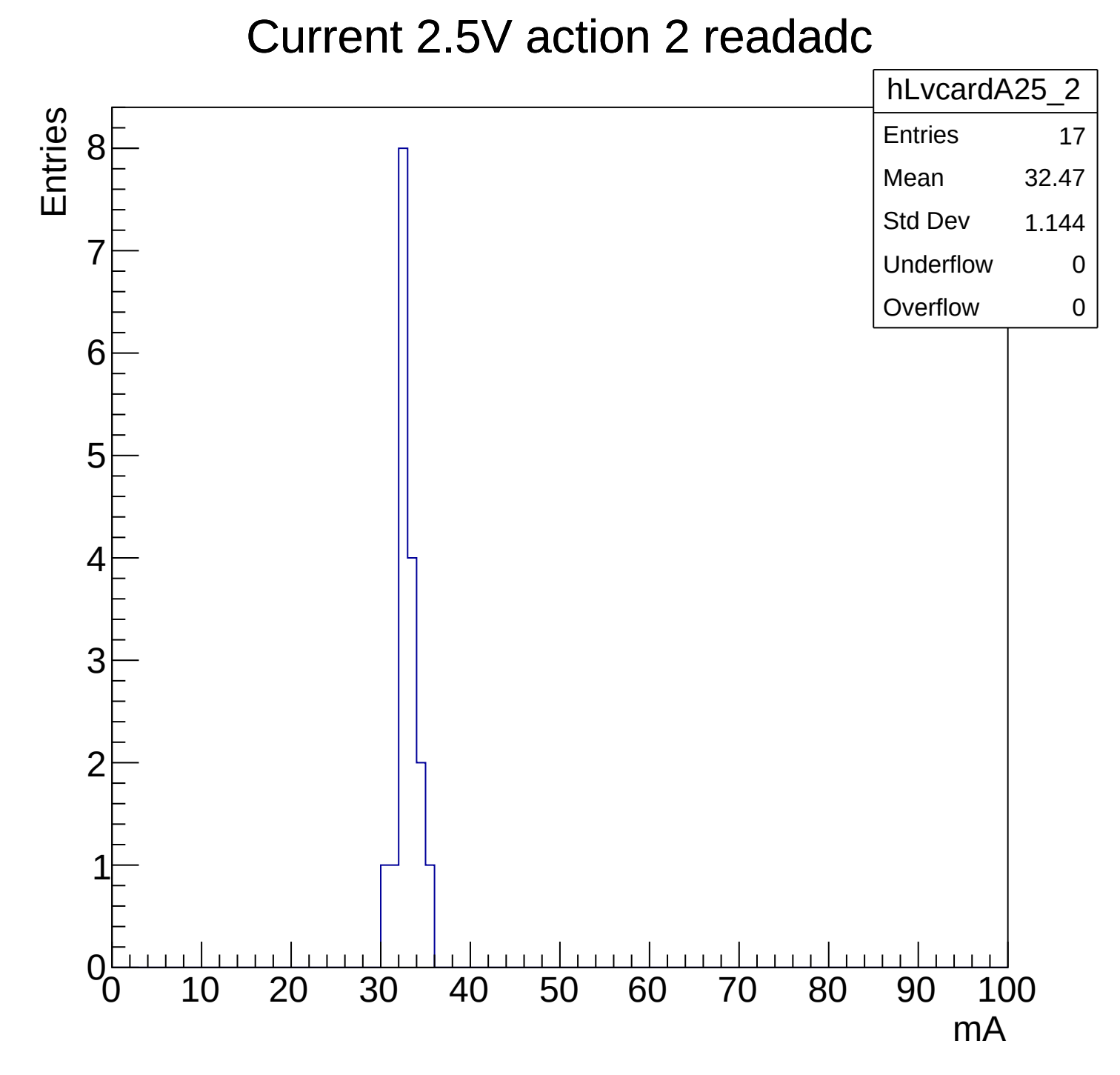

Current 1.5V action 3 lvcard switch on
hLvcardA15_3
Entries 19
Mean 68
Std Dev 93.74
Underflow 0
Overflow 0
Entries
0
1
2
3
4
5
6
0
100
200
300
400
500
600
mA

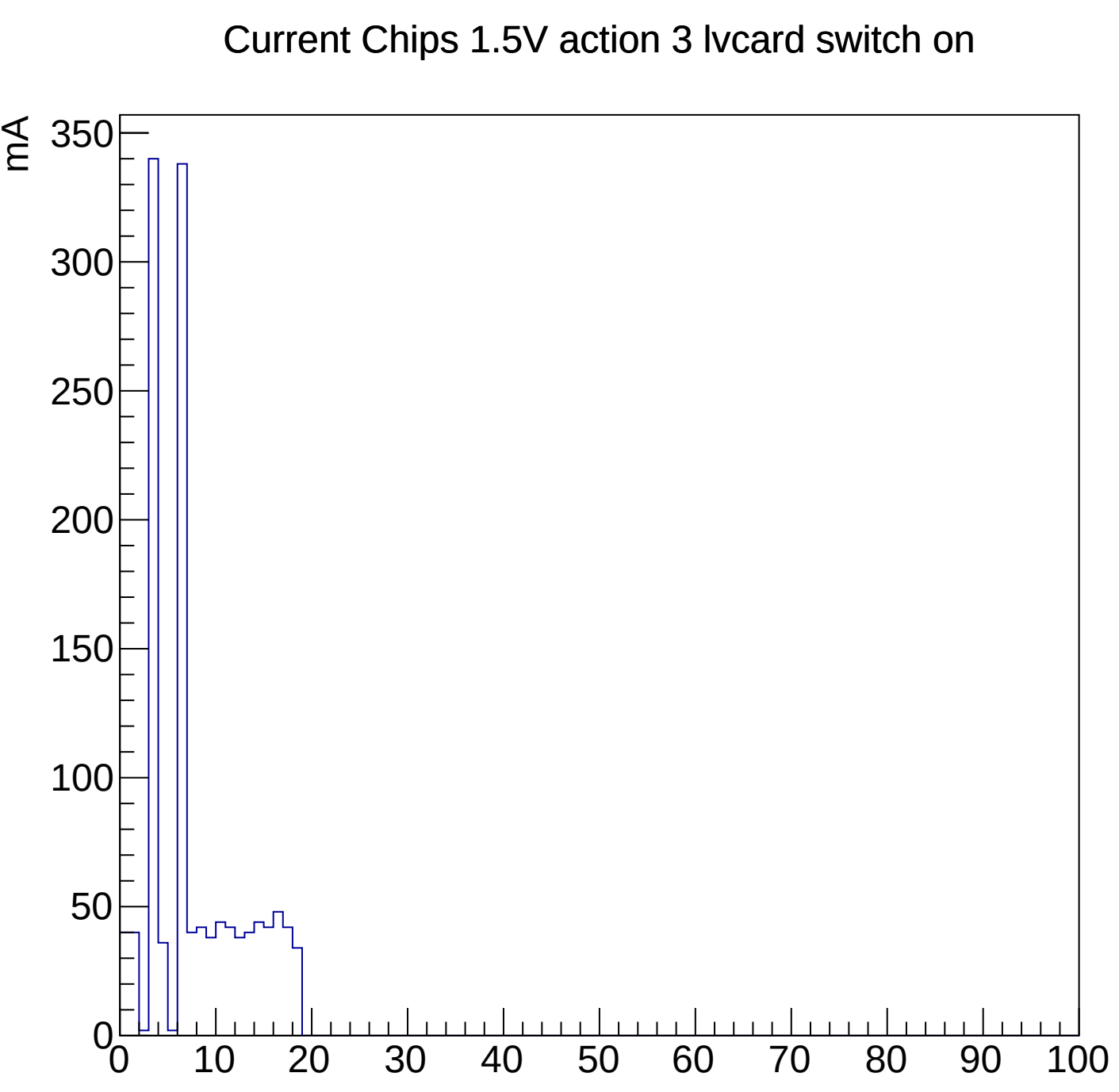

Current 1.5V action 5 saltro is switched on
hLvcardA15_5
Entries 19
Mean 307.5
Std Dev 105.3
Underflow 0
Overflow 0
Entries
0
0.5
1
1.5
2
2.5
3
0
100
200
300
400
500
600
mA

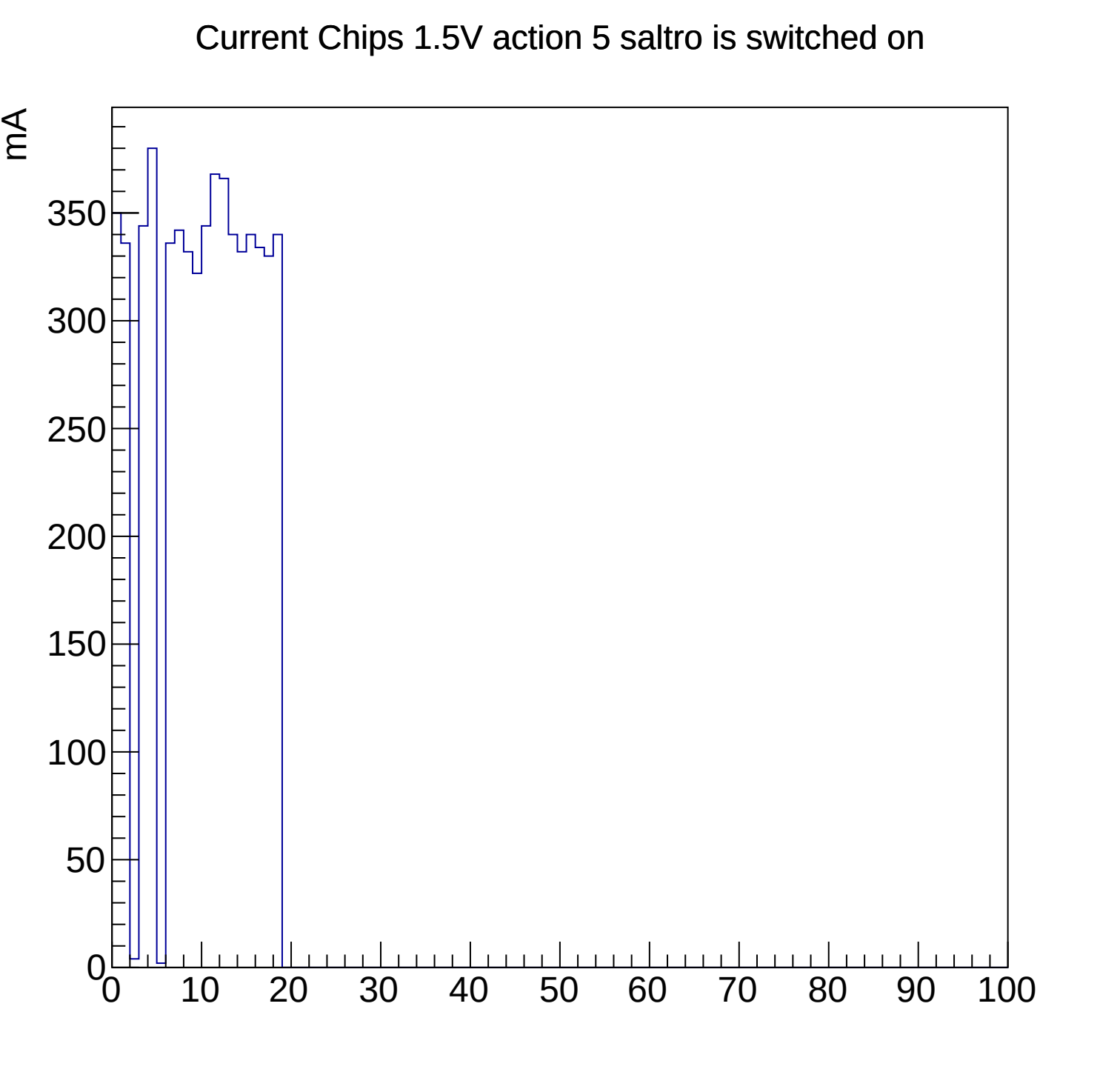

Current 1.5V action 2 readadc
hLvcardA15_2
Entries 17
Mean 367.8
Std Dev 6.431
Underflow 0
Overflow 0
Entries
0
1
2
3
4
5
6
0
100
200
300
400
500
600
mA

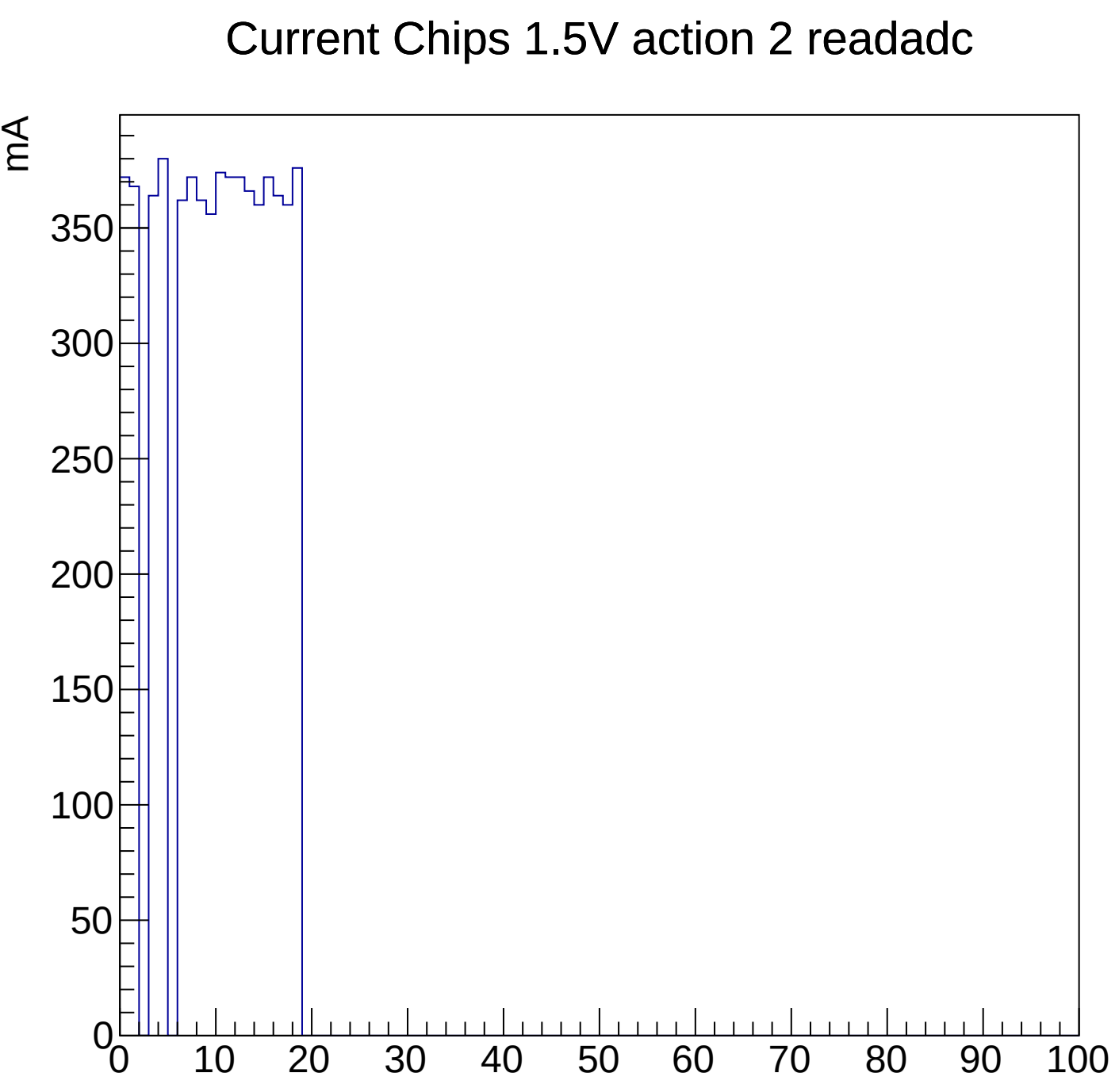

Current 2.5V action 3 lvcard switch on
hLvcardA25_3
Entries 19
Mean 32.89
Std Dev 1.552
Underflow 0
Overflow 0
Entries
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
80
90
100
mA

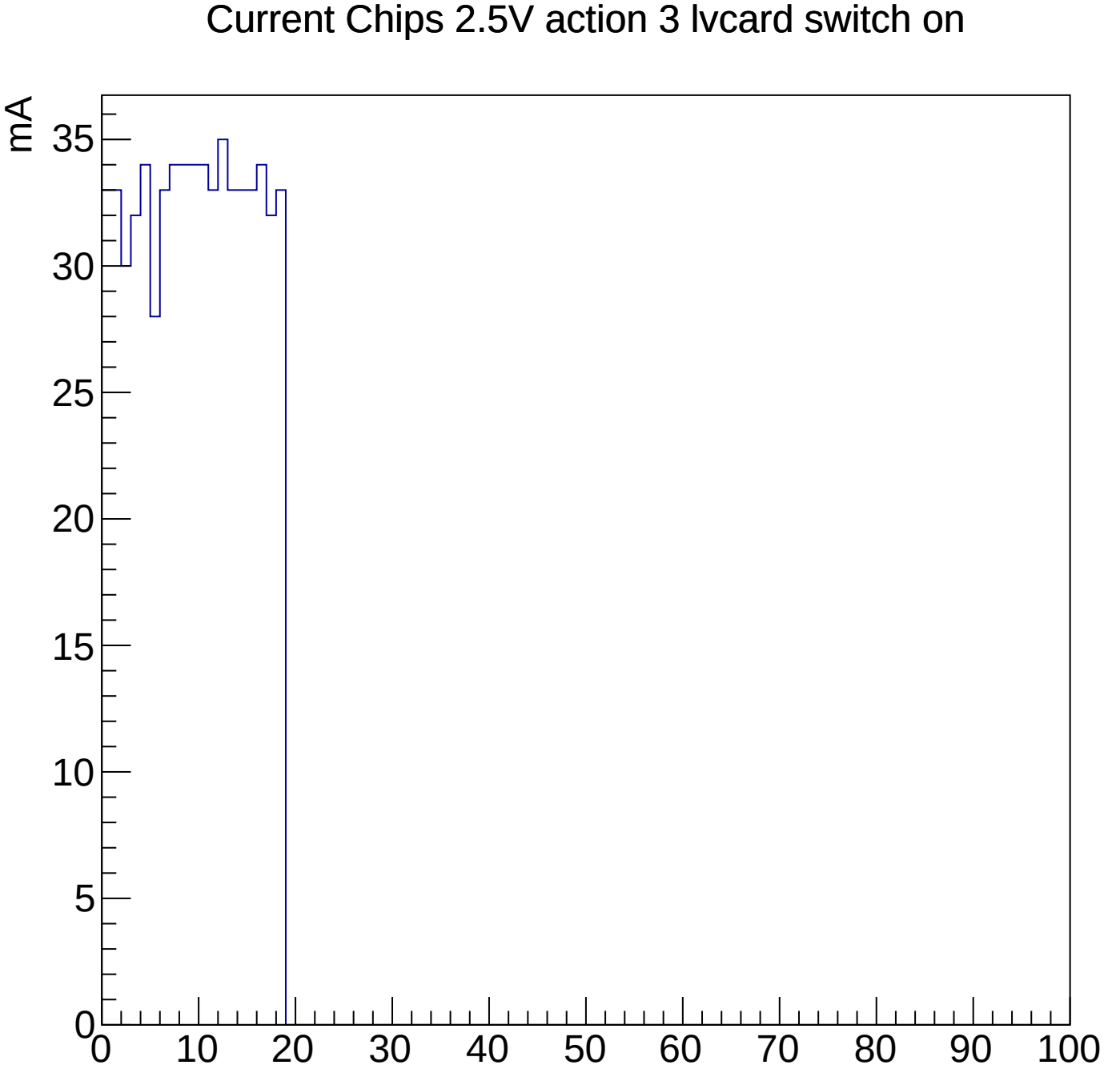

Current 2.5V action 5 saltro is switched on
hLvcardA25_5
Entries 19
Mean 33.16
Std Dev 1.496
Underflow 0
Overflow 0
Entries
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
80
90
100
mA

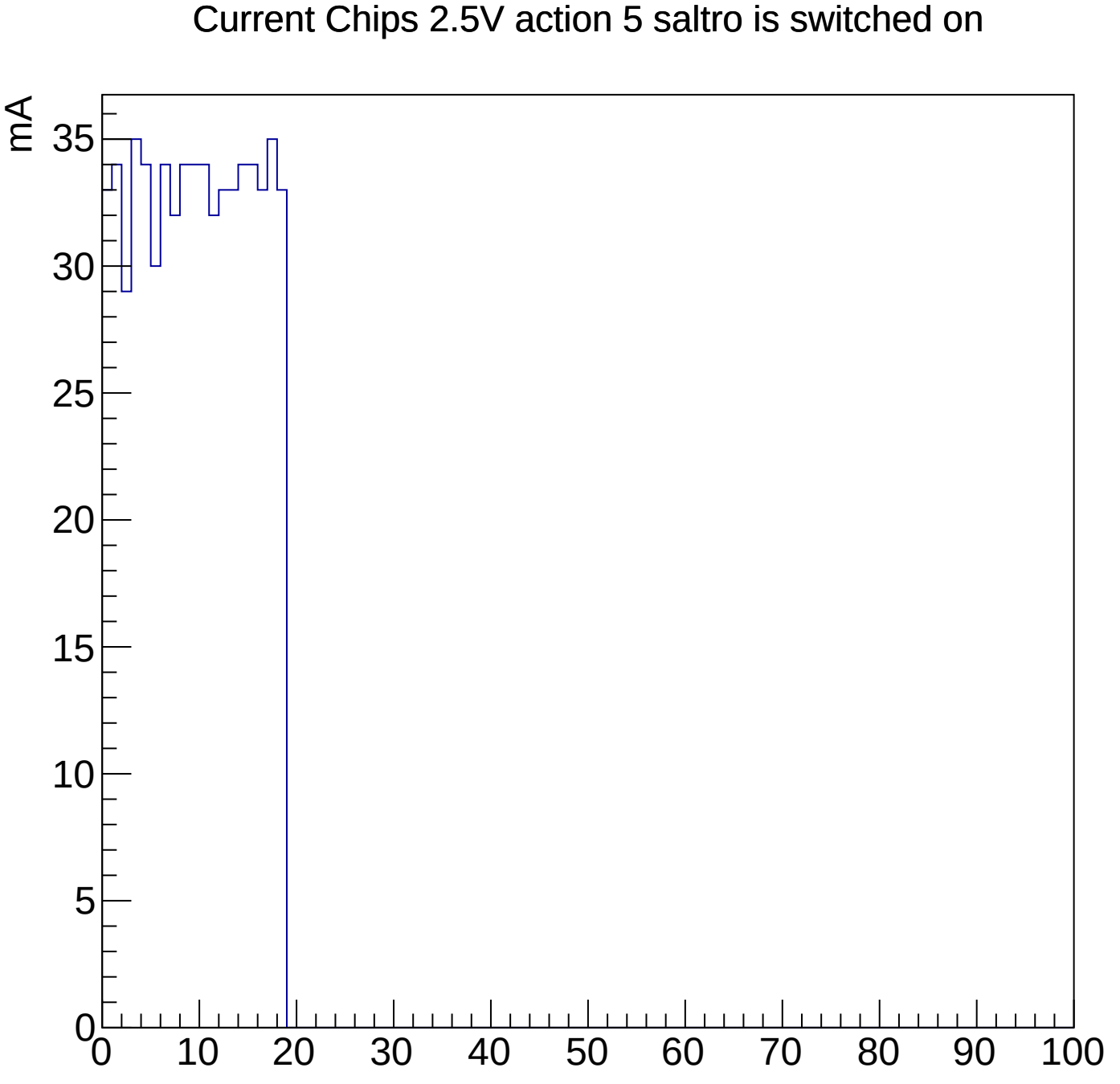

Current 2.5V action 2 readadc
hLvcardA25_2
Entries 17
Mean 32.47
Std Dev 1.144
Underflow 0
Overflow 0
Entries
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
80
90
100
mA

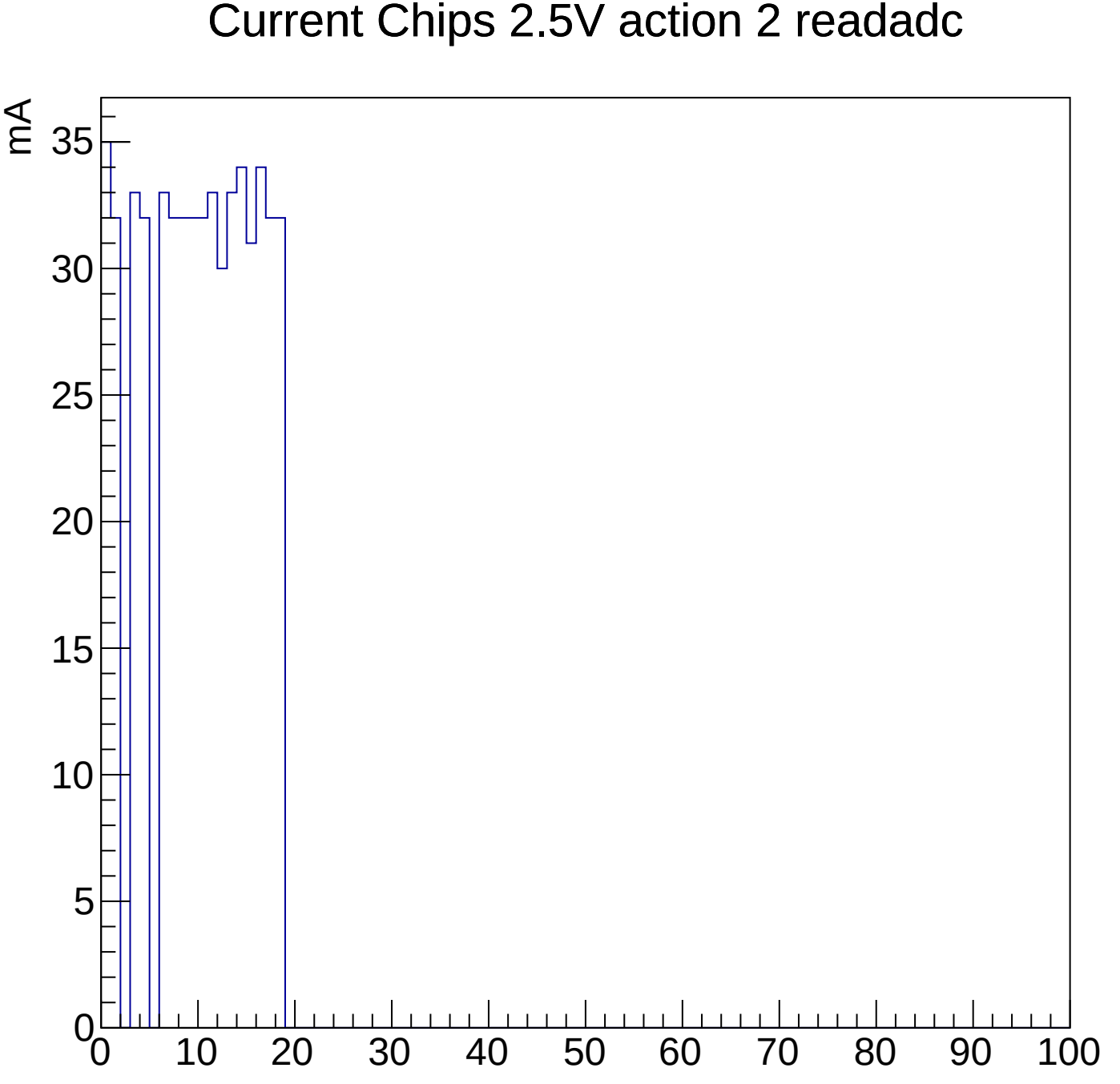

Current Chips 1.5V action 3 lvcard switch on
mA
350
300
250
200
150
100
50
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 1.5V action 5 saltro is switched on
mA
350
300
250
200
150
100
50
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 1.5V action 2 readadc
mA
350
300
250
200
150
100
50
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 2.5V action 3 lvcard switch on
mA
35
30
25
20
15
10
5
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 2.5V action 5 saltro is switched on
mA
35
30
25
20
15
10
5
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 2.5V action 2 readadc
mA
35
30
25
20
15
10
5
0
0
10
20
30
40
50
60
70
80
90
100