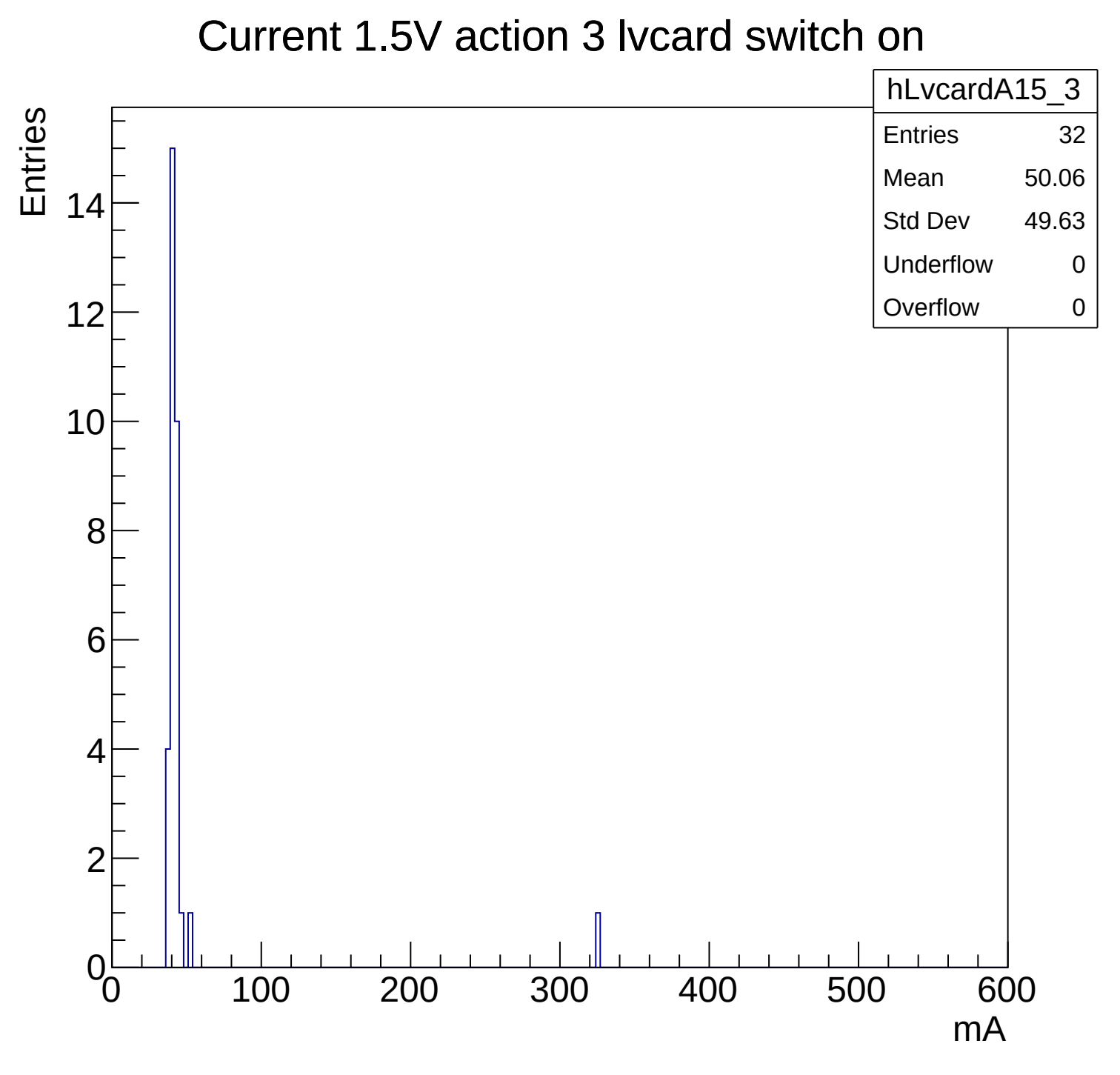
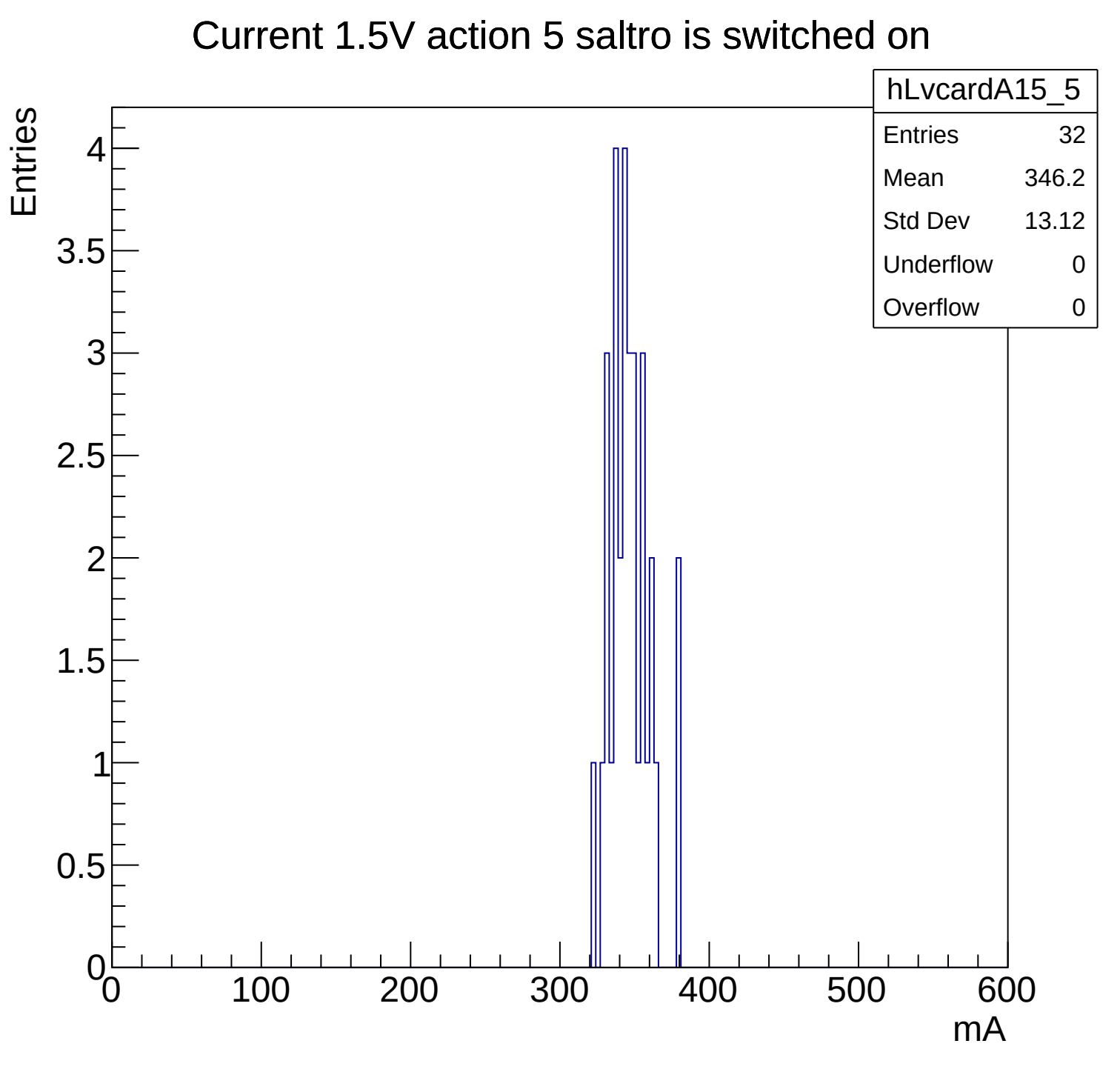
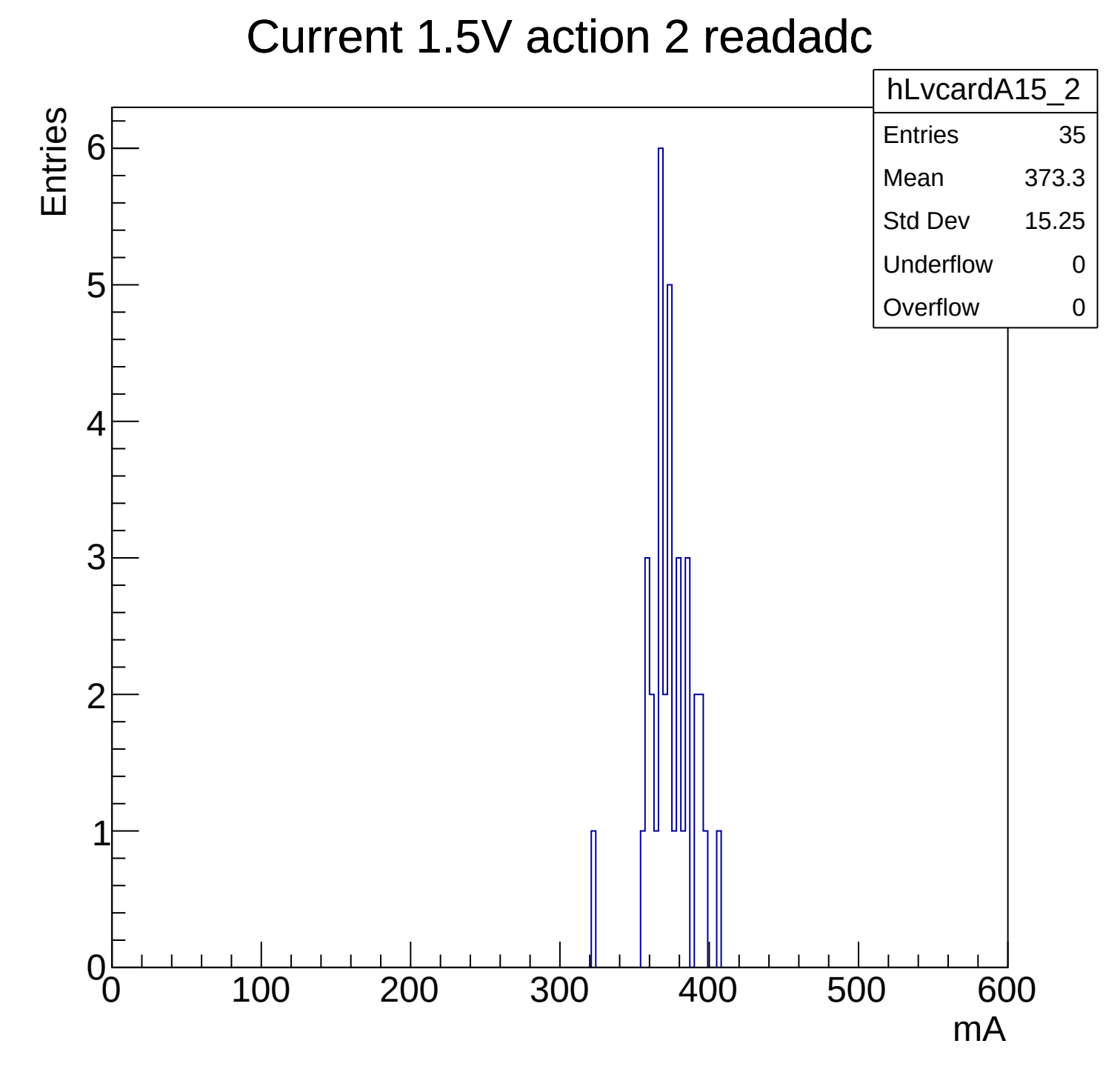
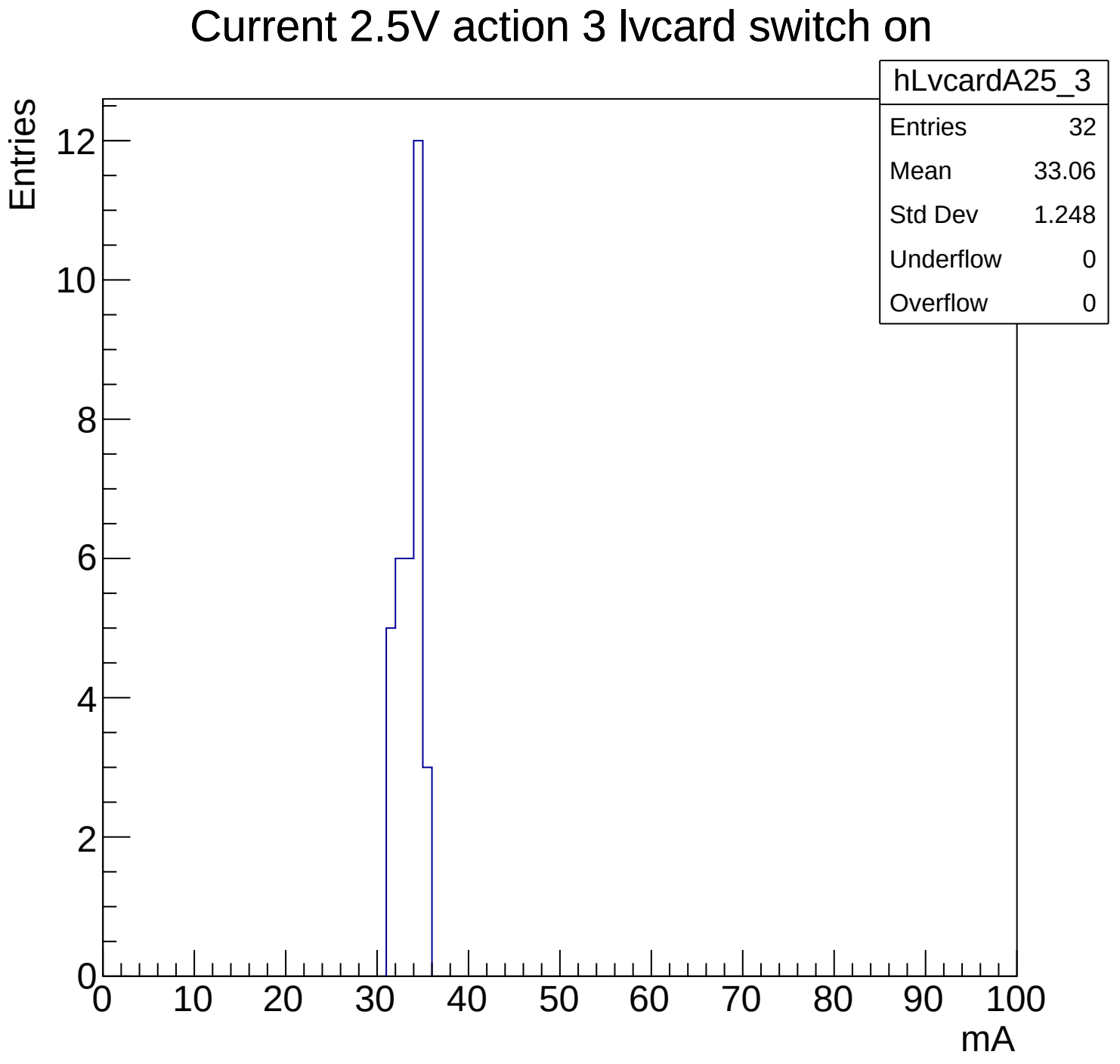
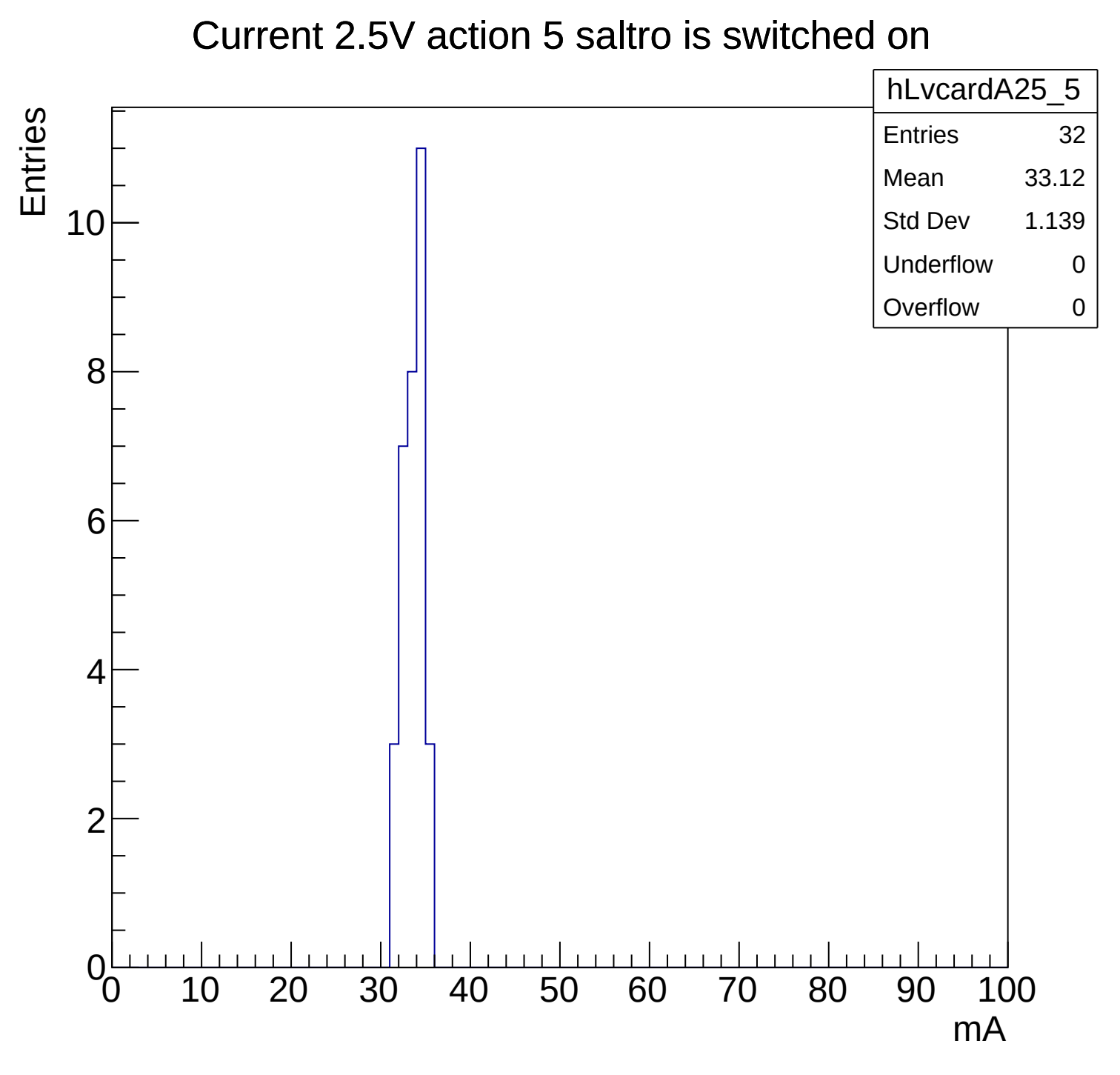
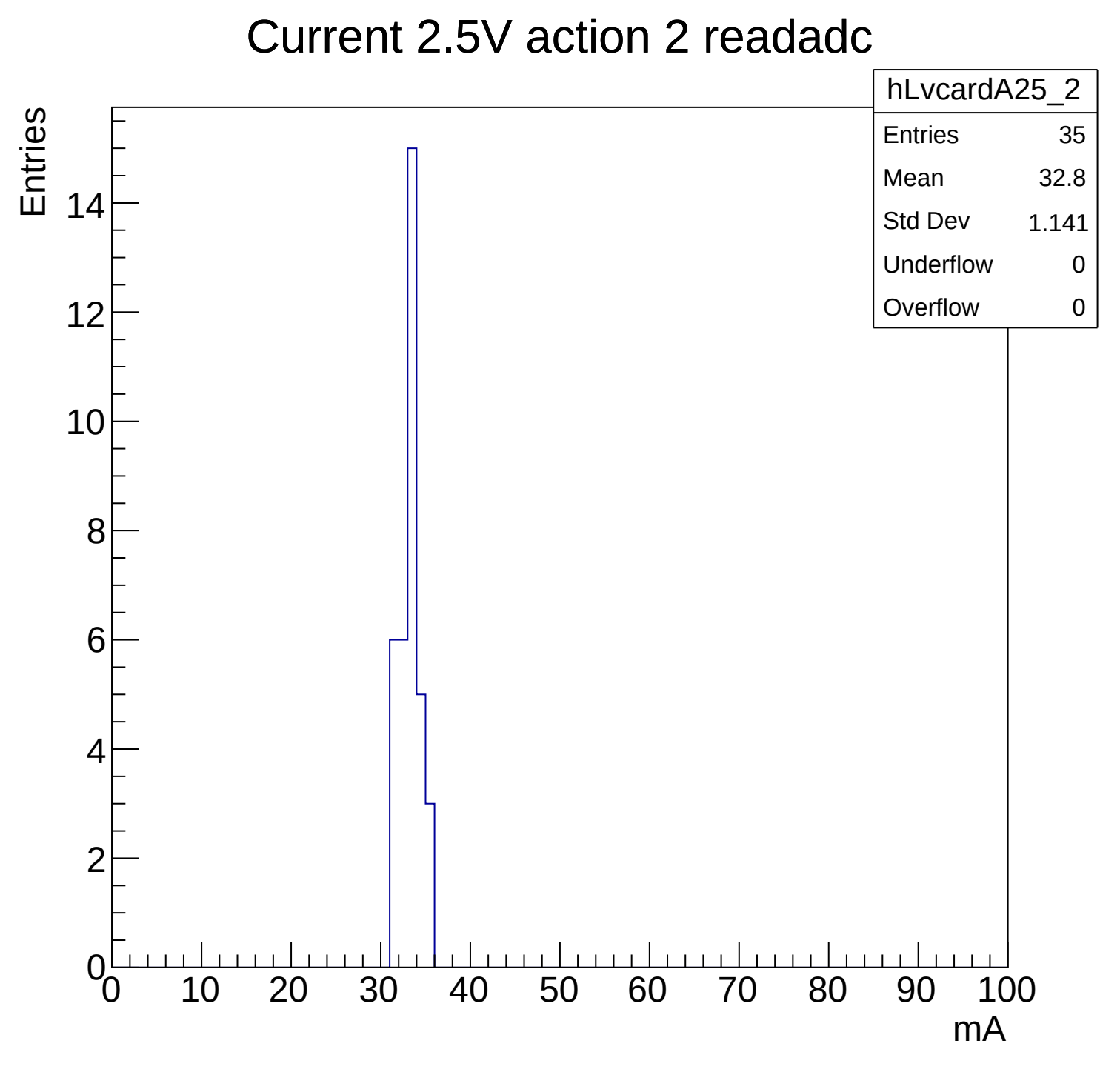

Current 1.5V action 3 lvcard switch on
hLvcardA15_3
Entries 32
Mean 50.06
Std Dev 49.63
Underflow 0
Overflow 0
Entries
0
2
4
6
8
10
12
14
0
100
200
300
400
500
600
mA

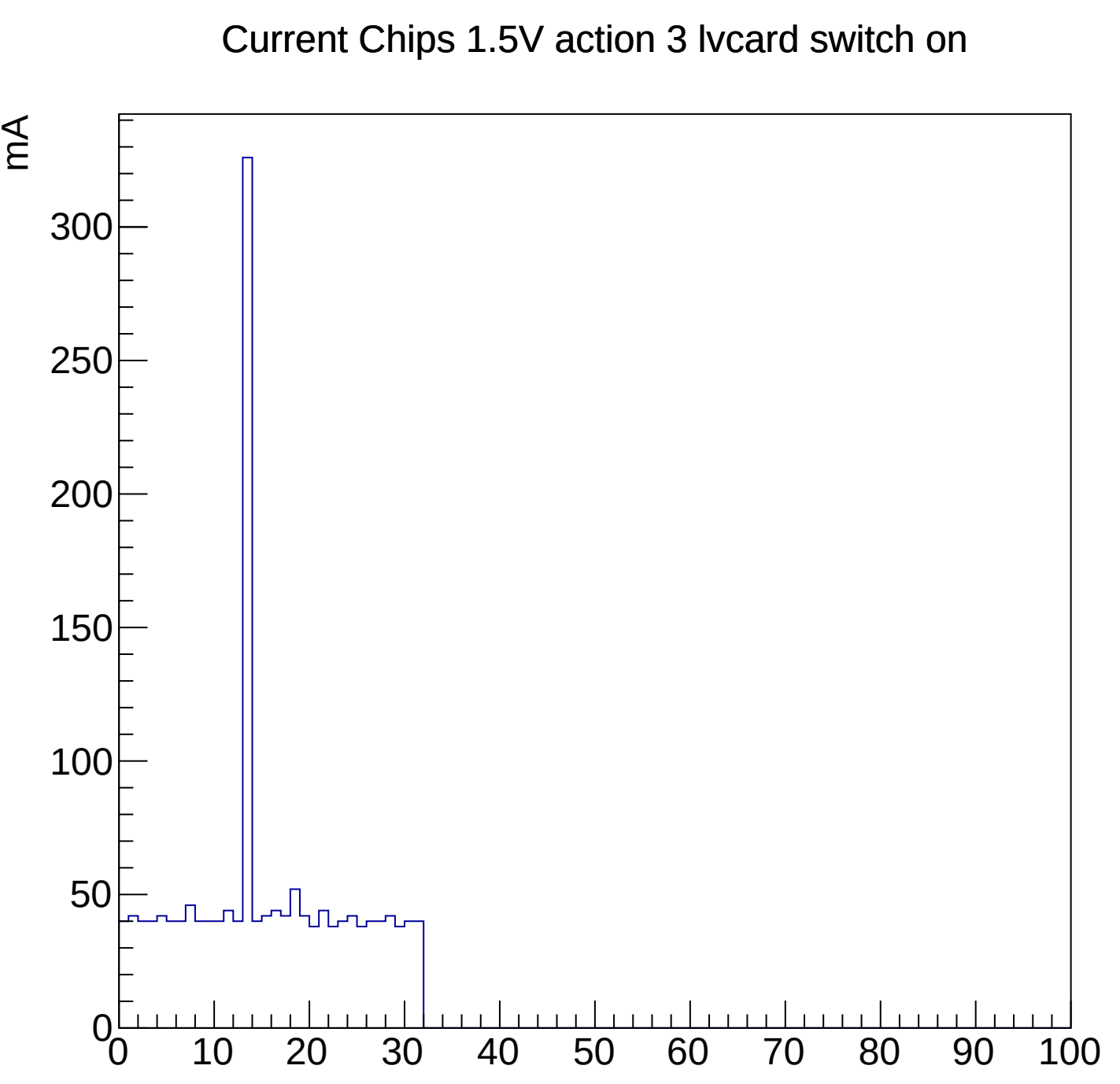

Current 1.5V action 5 saltro is switched on
hLvcardA15_5
Entries 32
Mean 346.2
Std Dev 13.12
Underflow 0
Overflow 0
Entries
0
0.5
1
1.5
2
2.5
3
3.5
4
0
100
200
300
400
500
600
mA

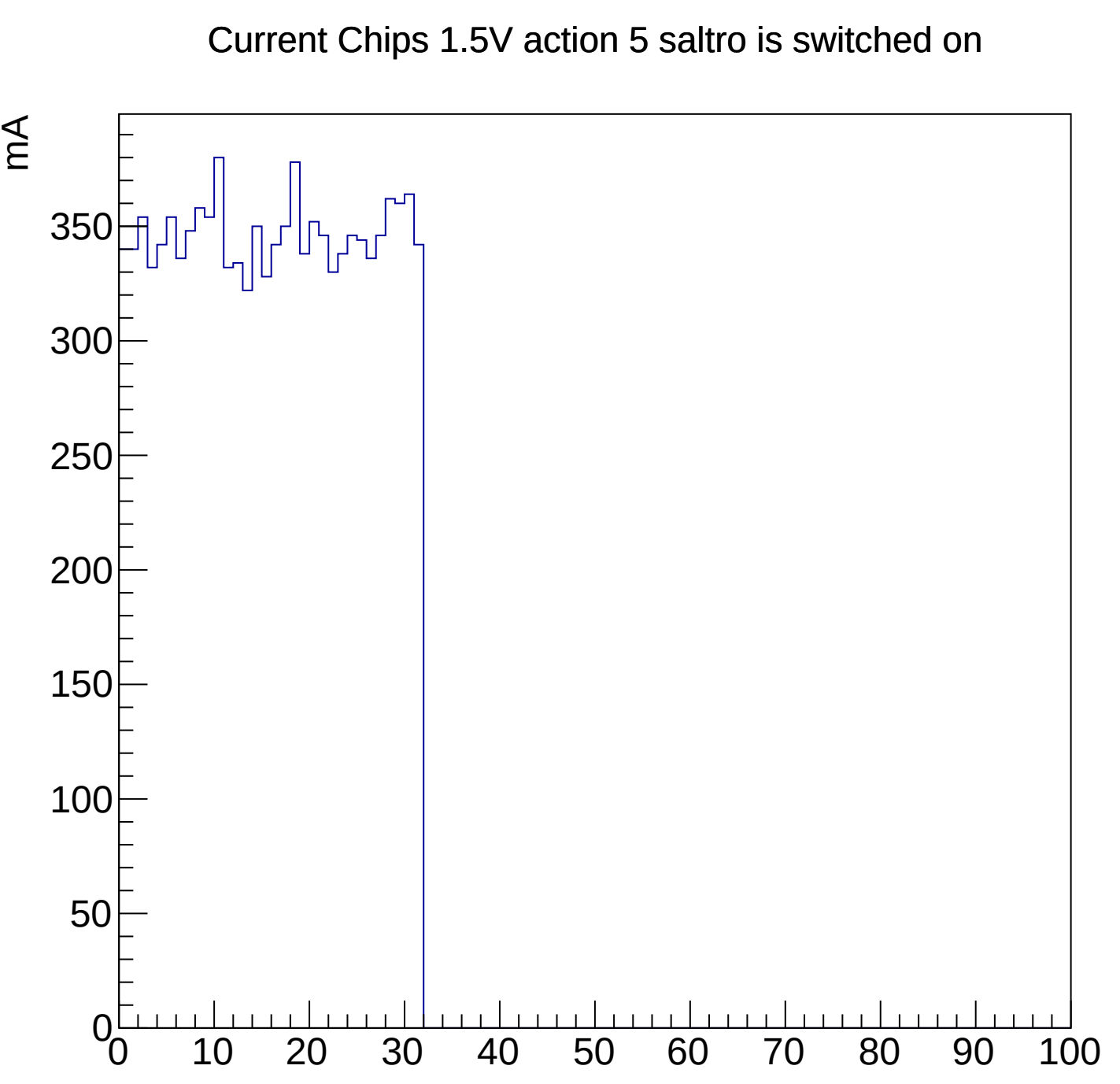

Current 1.5V action 2 readadc
hLvcardA15_2
Entries 35
Mean 373.3
Std Dev 15.25
Underflow 0
Overflow 0
Entries
0
1
2
3
4
5
6
0
100
200
300
400
500
600
mA

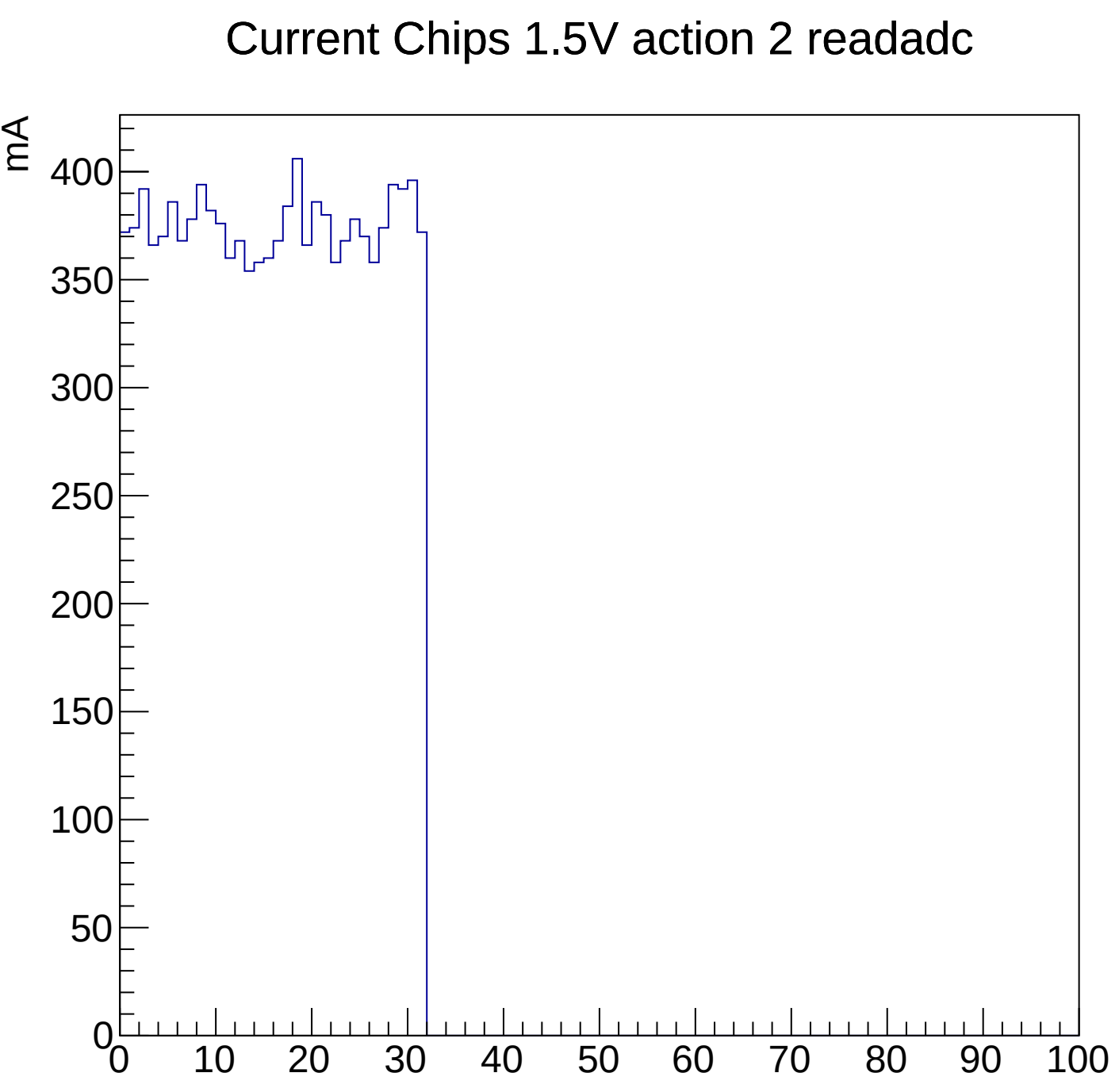

Current 2.5V action 3 lvcard switch on
hLvcardA25_3
Entries 32
Mean 33.06
Std Dev 1.248
Underflow 0
Overflow 0
Entries
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
80
90
100
mA

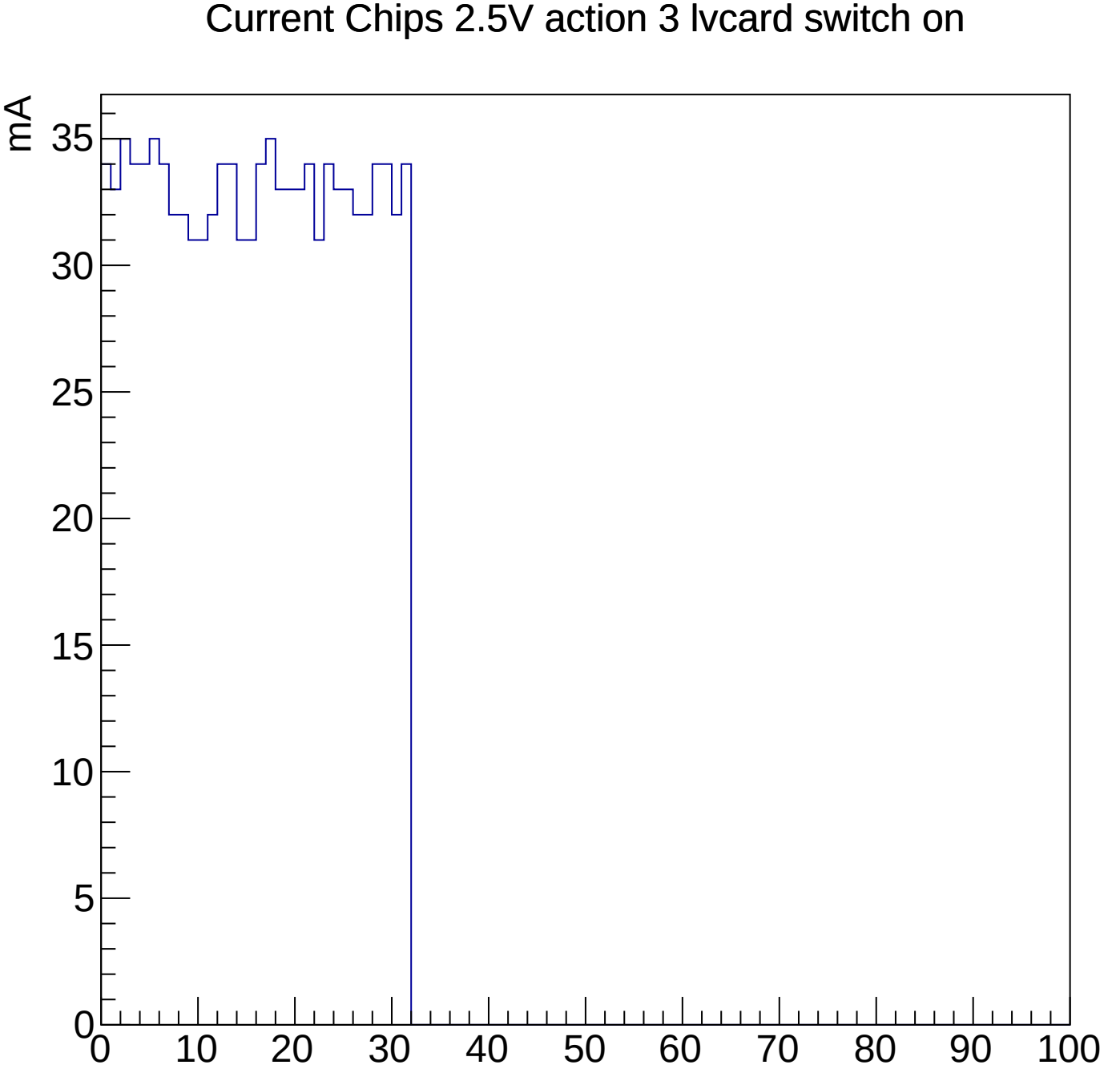

Current 2.5V action 5 saltro is switched on
hLvcardA25_5
Entries 32
Mean 33.12
Std Dev 1.139
Underflow 0
Overflow 0
Entries
0
2
4
6
8
10
0
10
20
30
40
50
60
70
80
90
100
mA

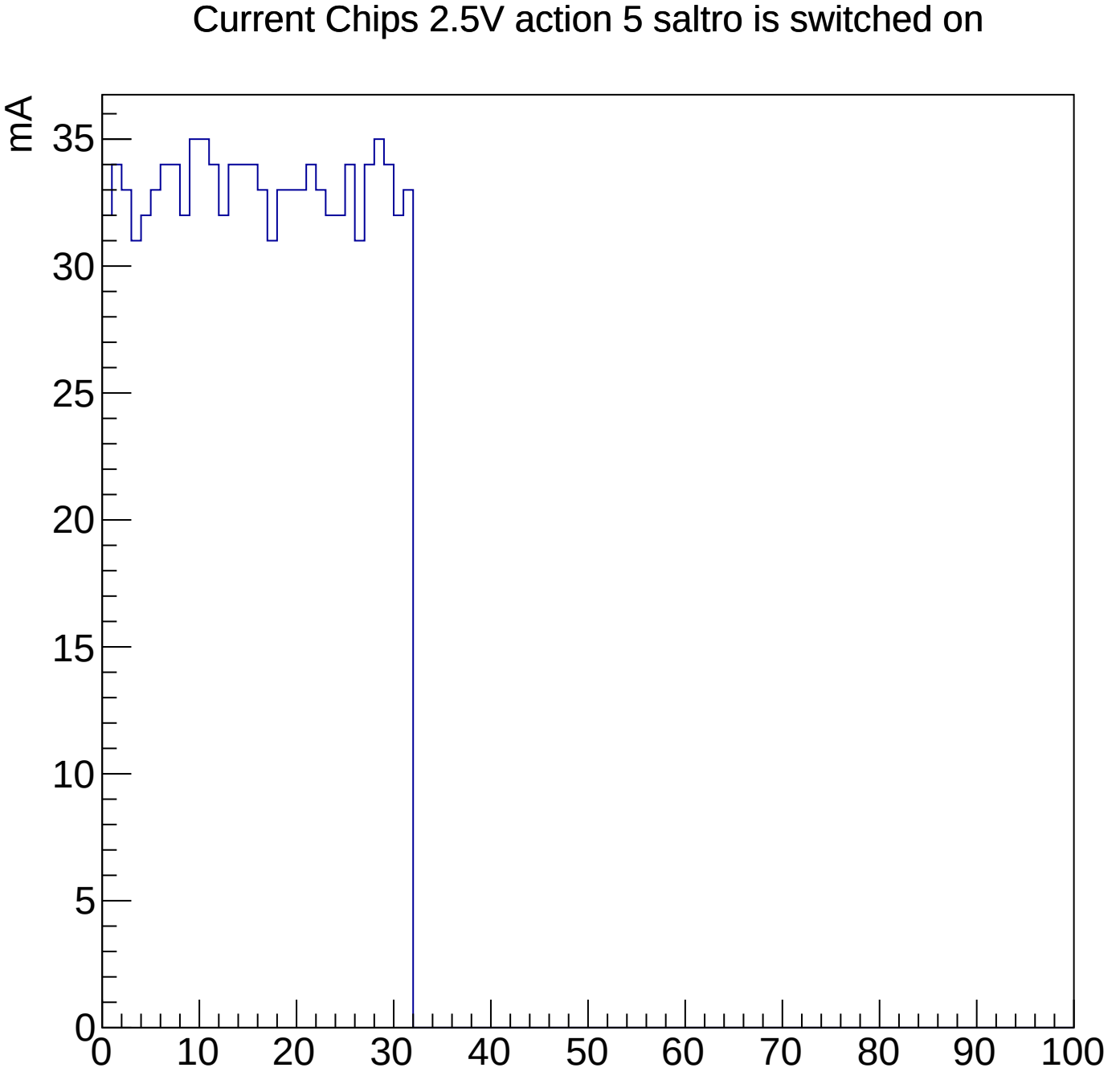

Current 2.5V action 2 readadc
hLvcardA25_2
Entries 35
Mean 32.8
Std Dev 1.141
Underflow 0
Overflow 0
Entries
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
80
90
100
mA

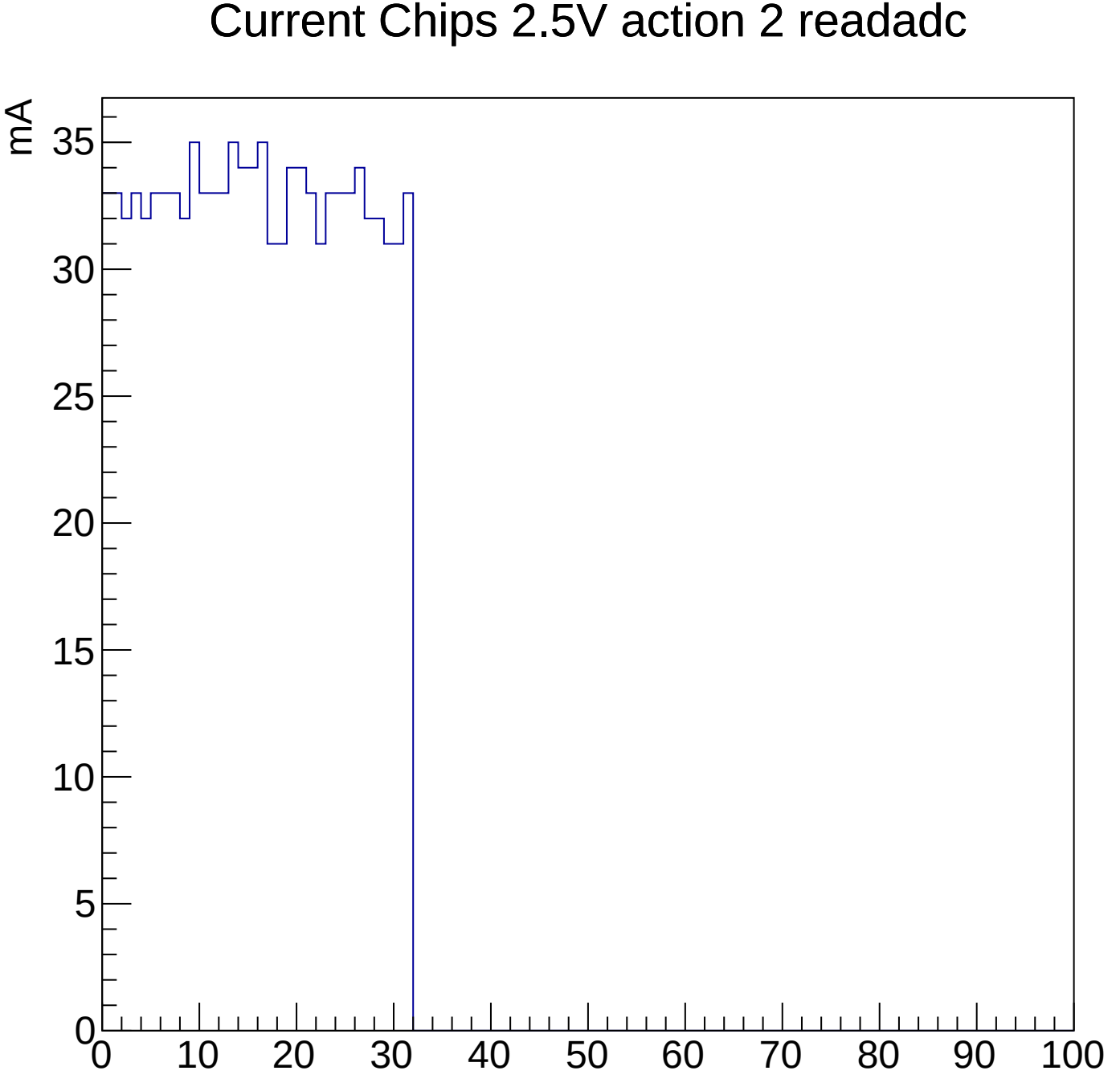

Current Chips 1.5V action 3 lvcard switch on
mA
300
250
200
150
100
50
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 1.5V action 5 saltro is switched on
mA
350
300
250
200
150
100
50
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 1.5V action 2 readadc
mA
400
350
300
250
200
150
100
50
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 2.5V action 3 lvcard switch on
mA
35
30
25
20
15
10
5
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 2.5V action 5 saltro is switched on
mA
35
30
25
20
15
10
5
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 2.5V action 2 readadc
mA
35
30
25
20
15
10
5
0
0
10
20
30
40
50
60
70
80
90
100