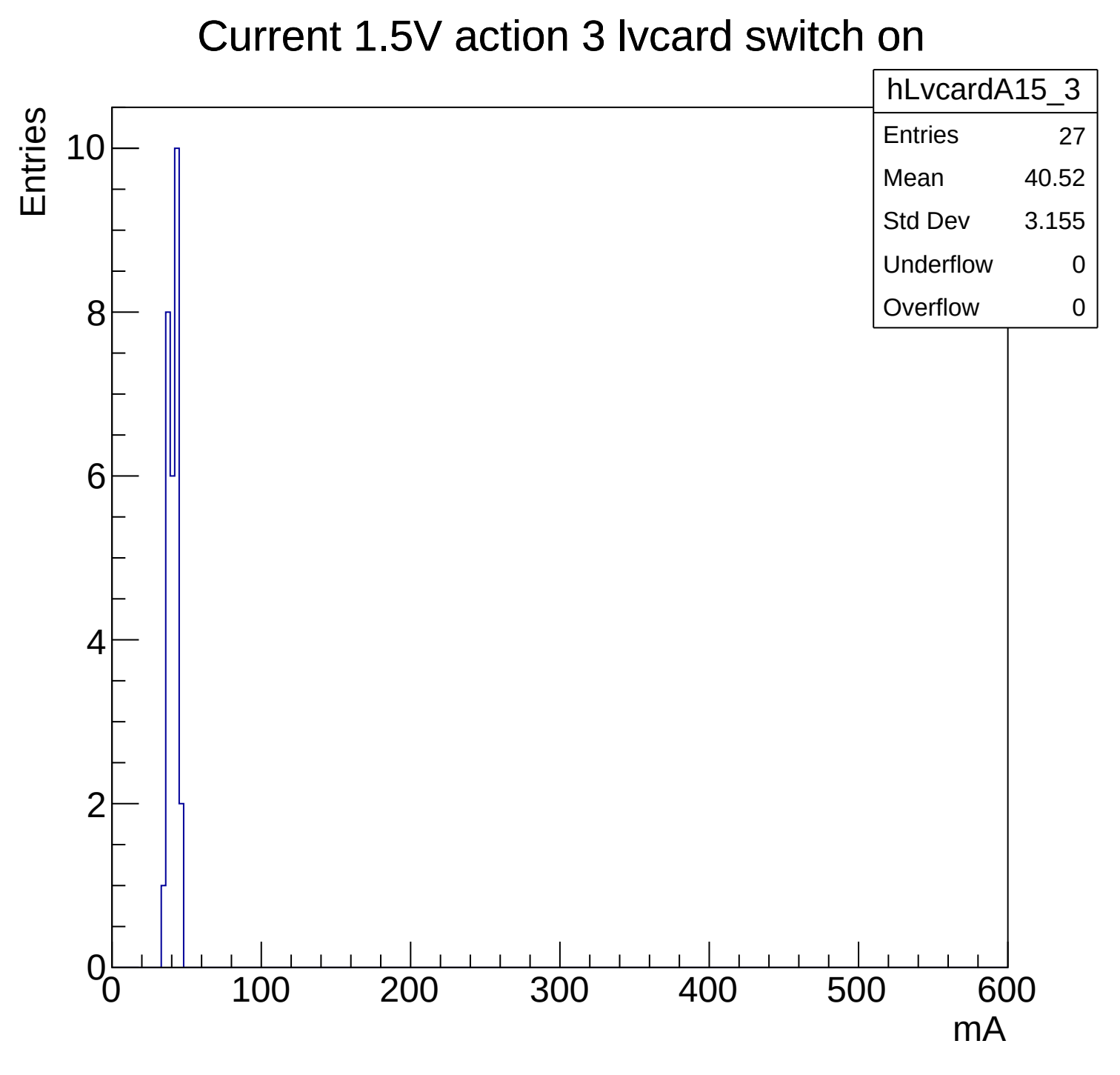
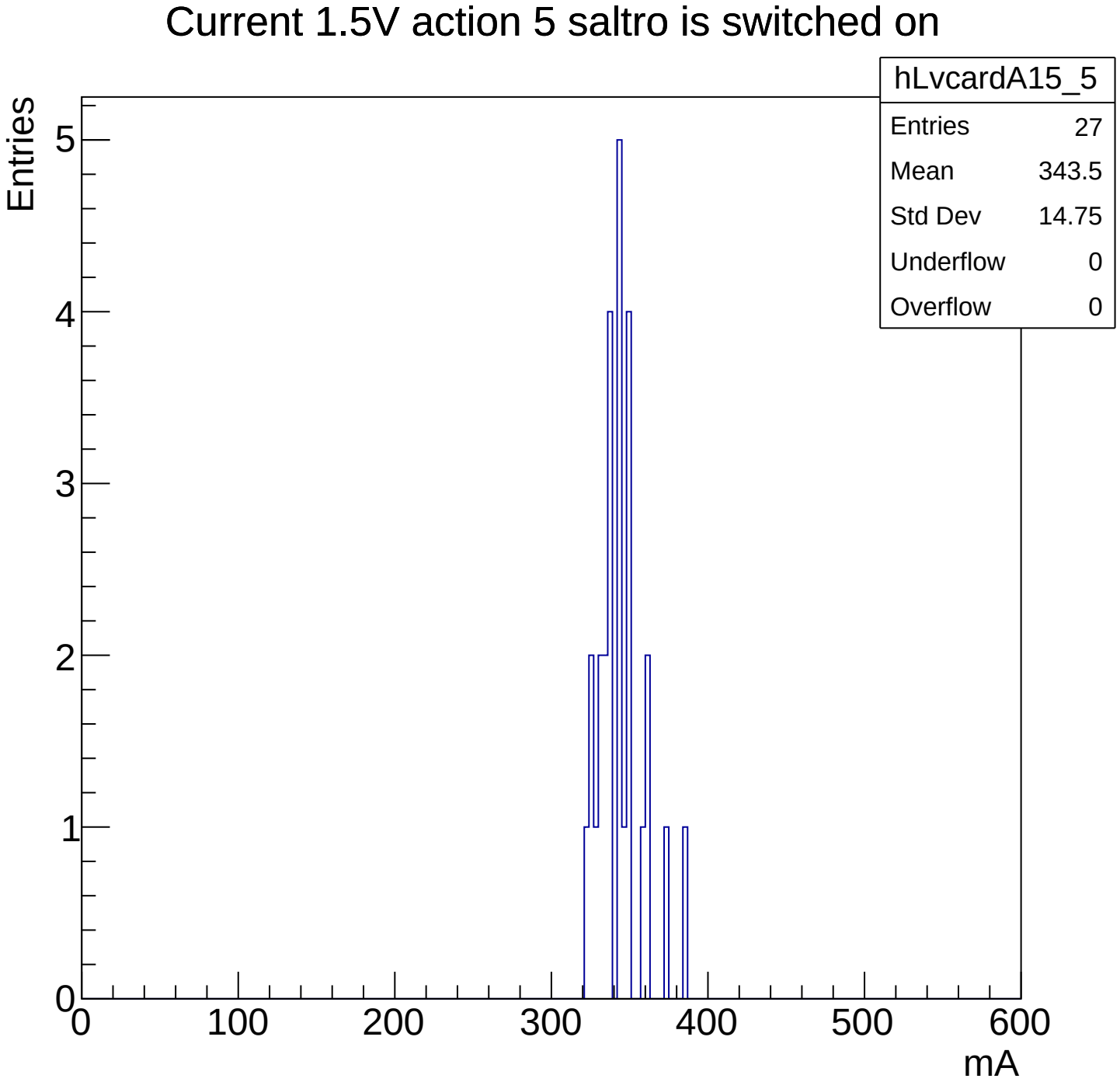
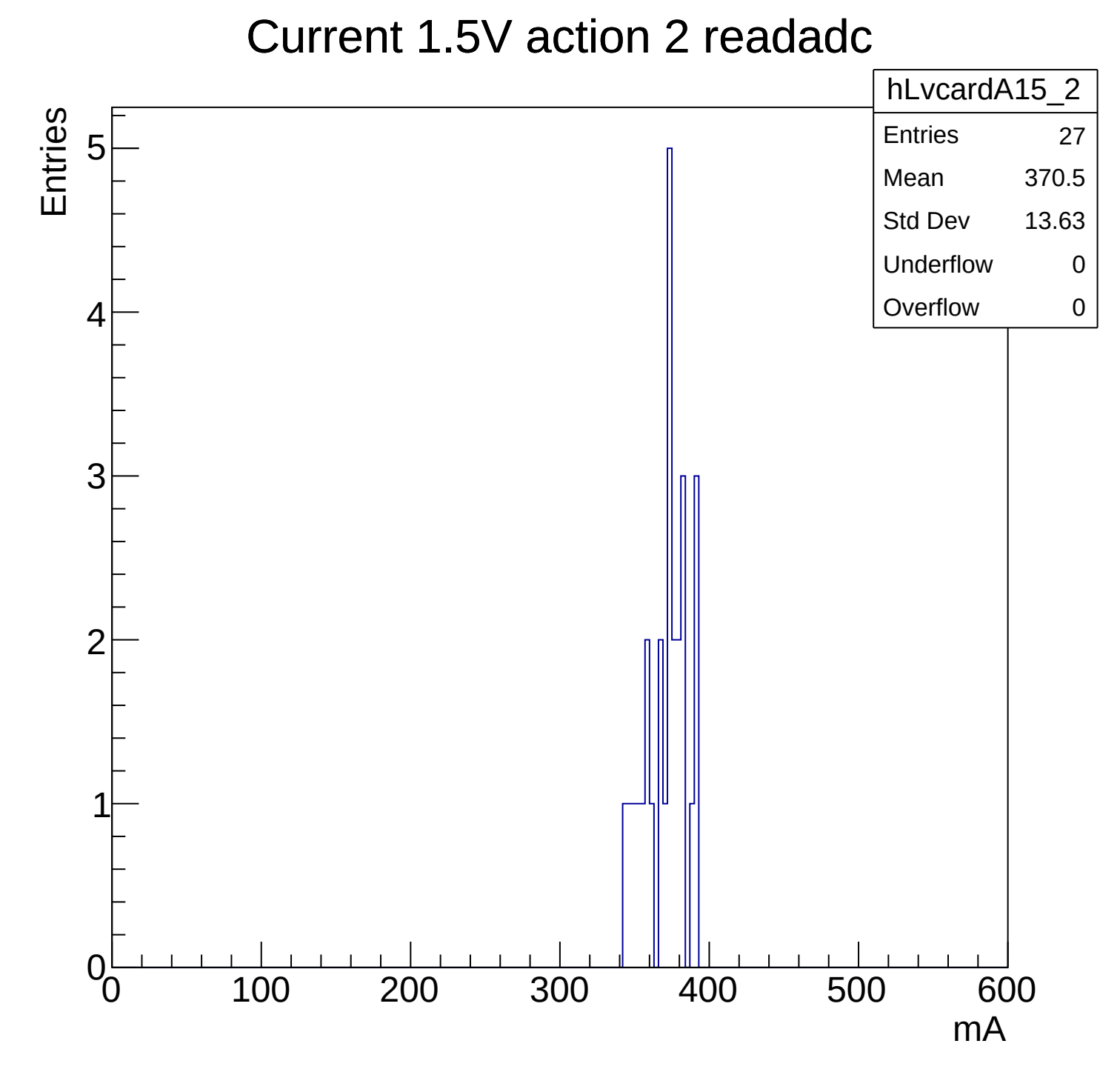
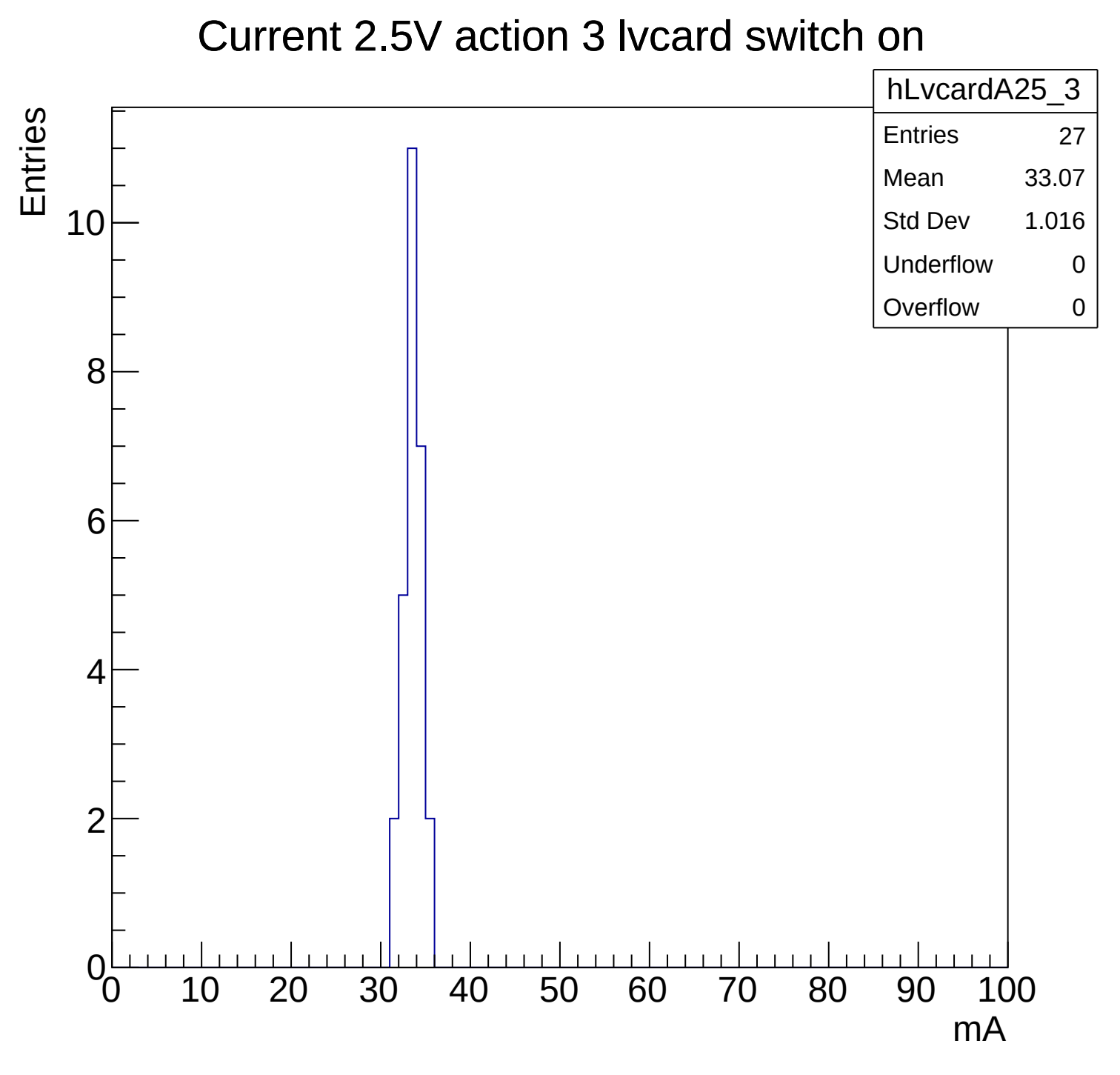
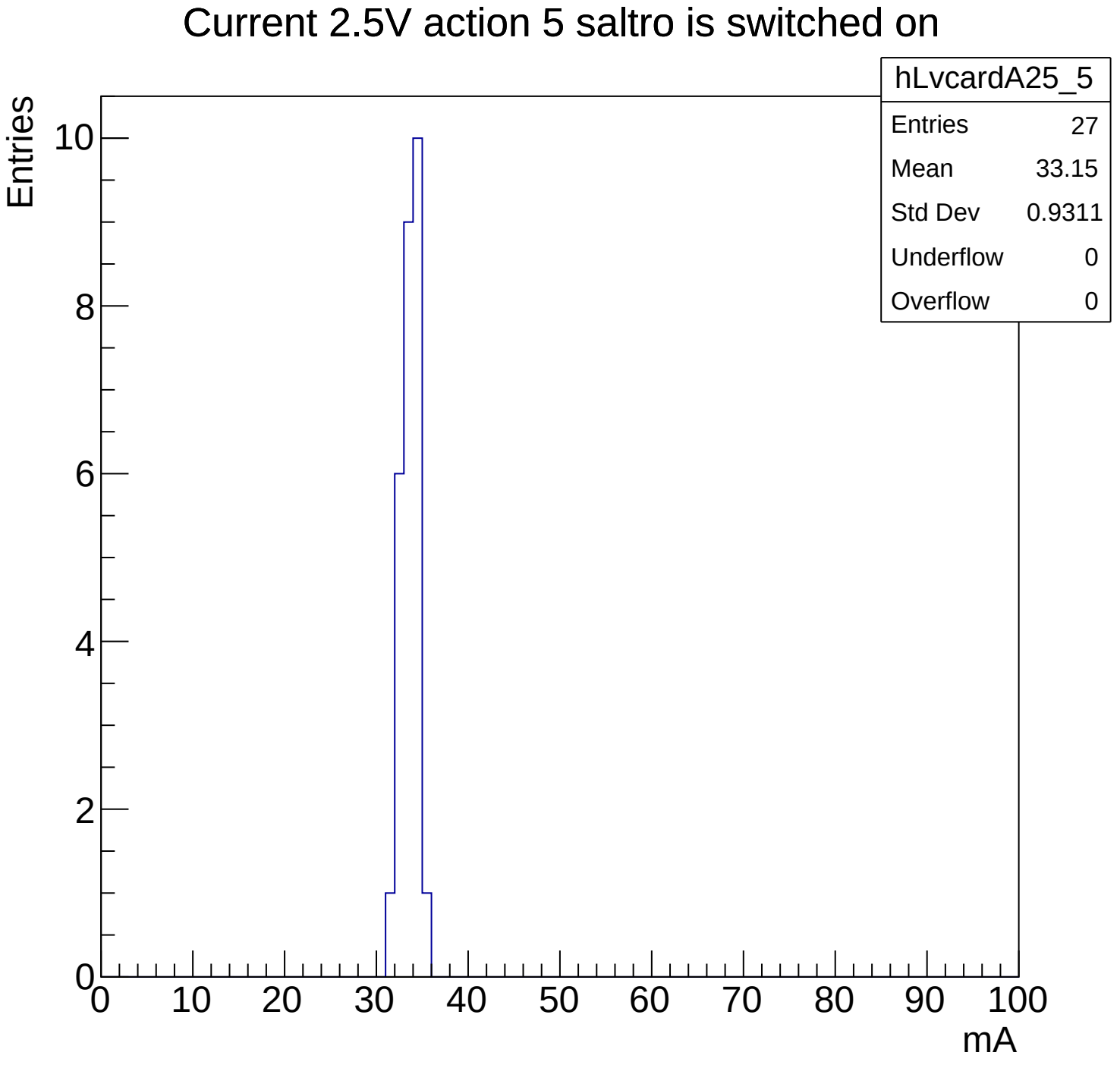
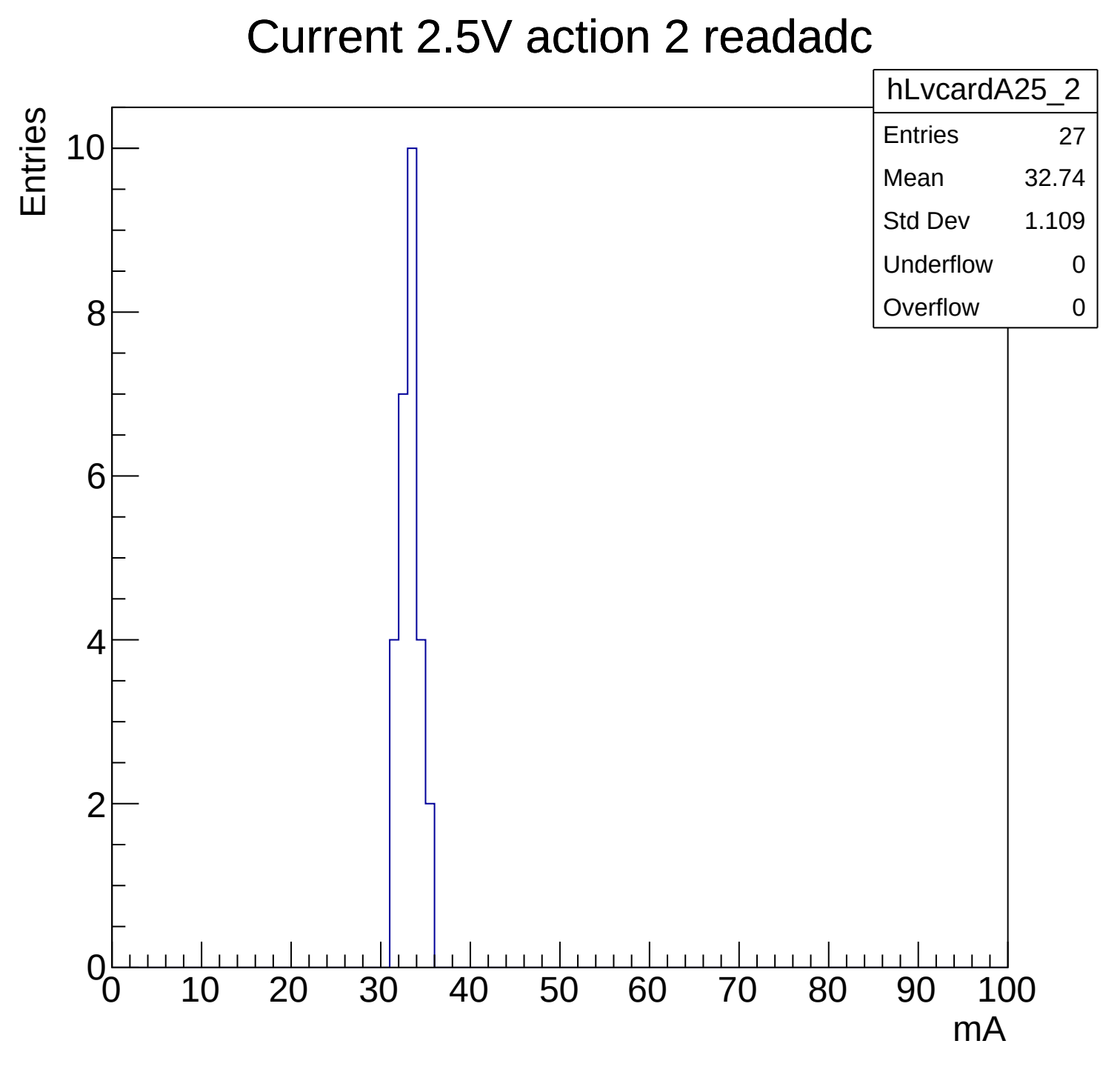

Current 1.5V action 3 lvcard switch on
hLvcardA15_3
Entries 27
Mean 40.52
Std Dev 3.155
Underflow 0
Overflow 0
Entries
0
2
4
6
8
10
0
100
200
300
400
500
600
mA

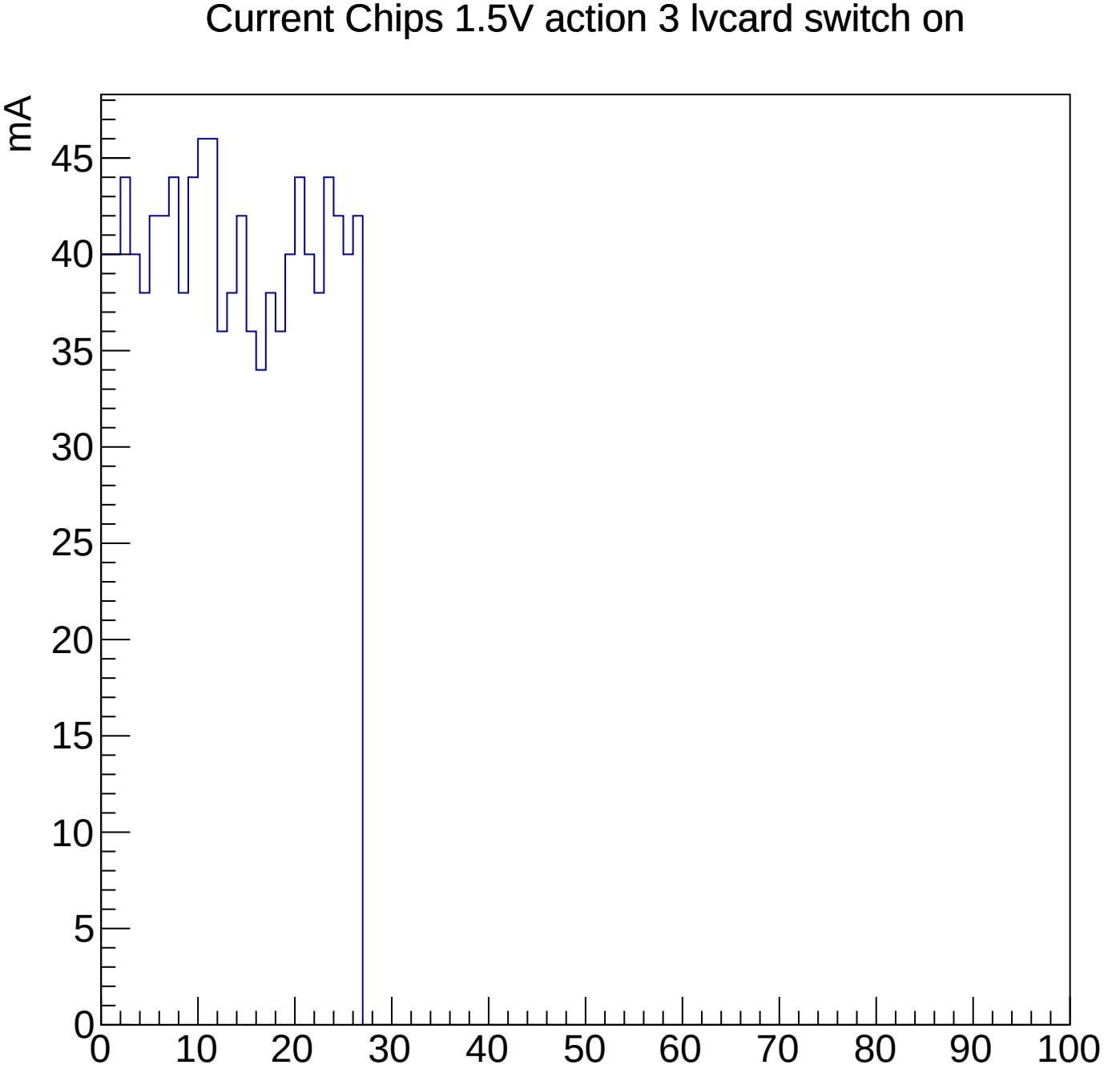

Current 1.5V action 5 saltro is switched on
hLvcardA15_5
Entries 27
Mean 343.5
Std Dev 14.75
Underflow 0
Overflow 0
Entries
0
1
2
3
4
5
0
100
200
300
400
500
600
mA

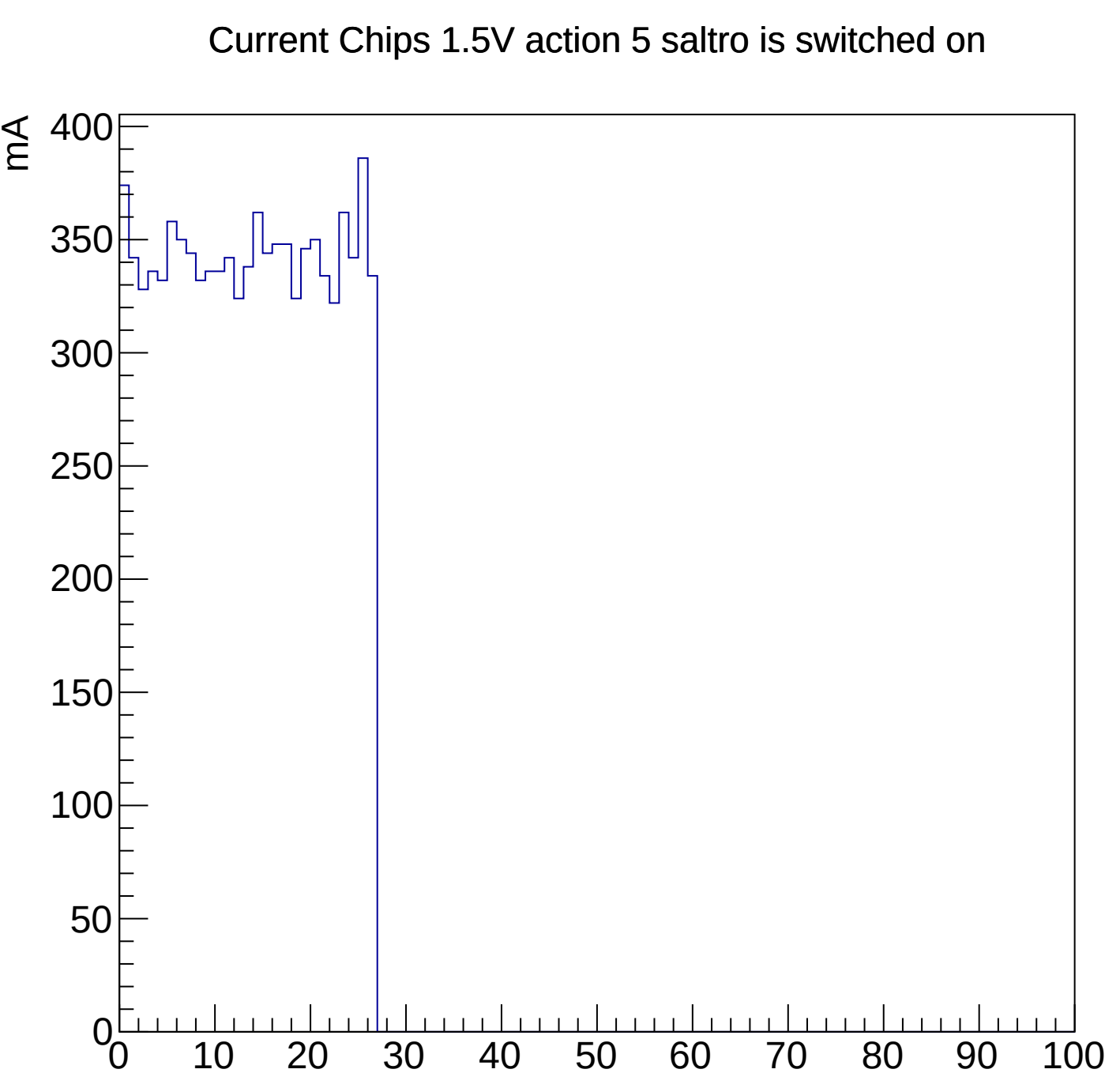

Current 1.5V action 2 readadc
hLvcardA15_2
Entries 27
Mean 370.5
Std Dev 13.63
Underflow 0
Overflow 0
Entries
0
1
2
3
4
5
0
100
200
300
400
500
600
mA

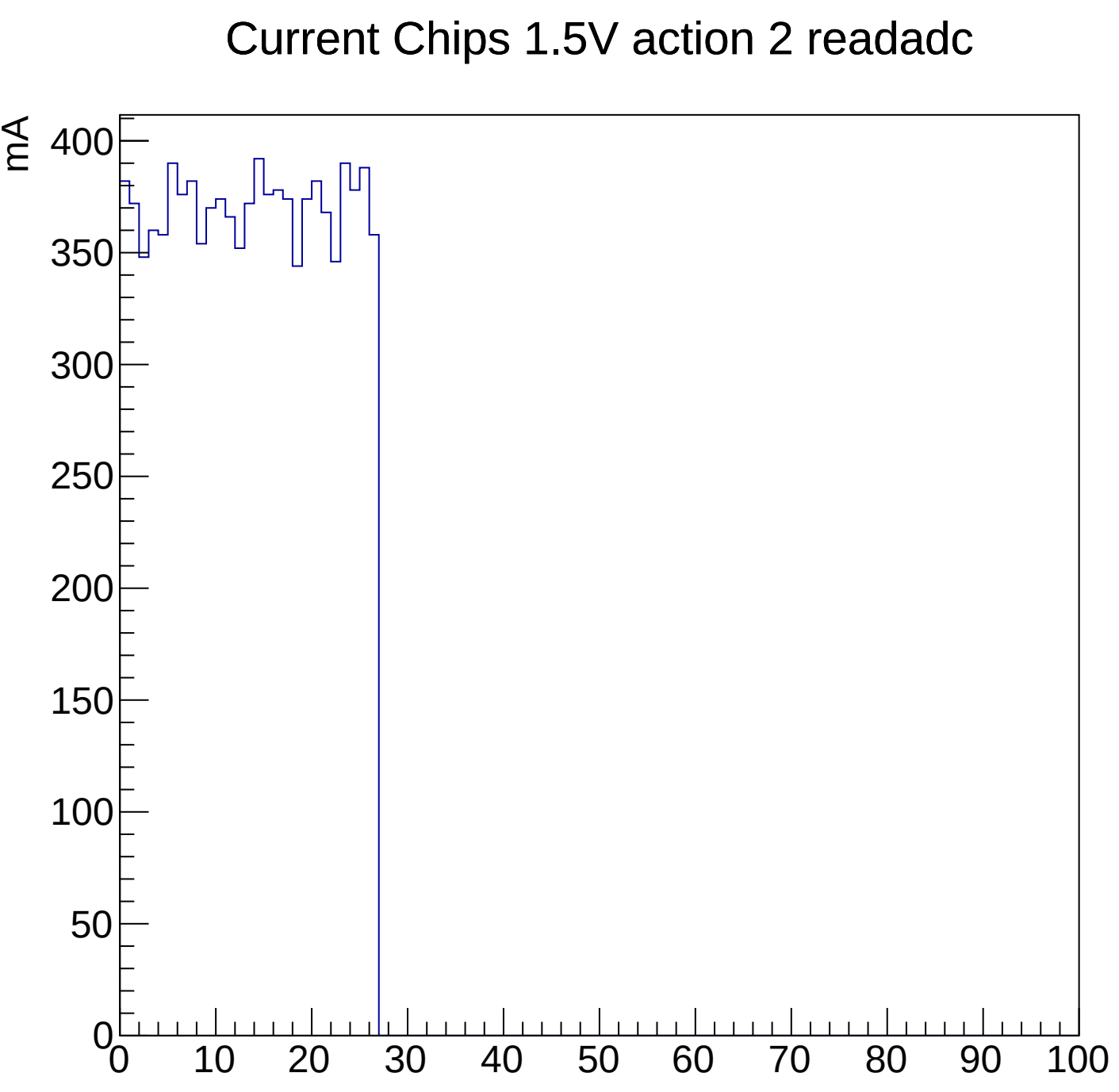

Current 2.5V action 3 lvcard switch on
hLvcardA25_3
Entries 27
Mean 33.07
Std Dev 1.016
Underflow 0
Overflow 0
Entries
0
2
4
6
8
10
0
10
20
30
40
50
60
70
80
90
100
mA

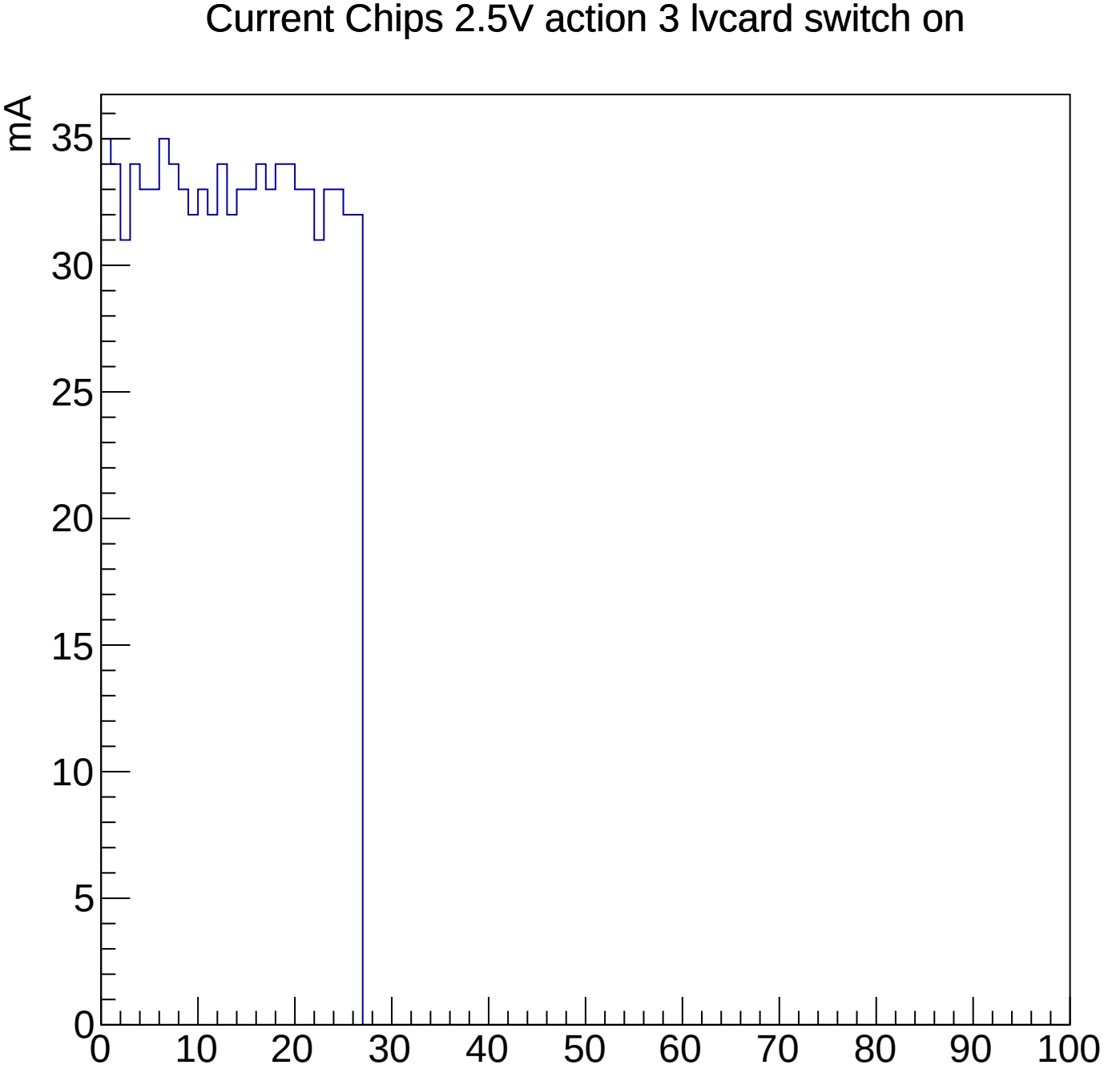

Current 2.5V action 5 saltro is switched on
hLvcardA25_5
Entries 27
Mean 33.15
Std Dev 0.9311
Underflow 0
Overflow 0
Entries
0
2
4
6
8
10
0
10
20
30
40
50
60
70
80
90
100
mA

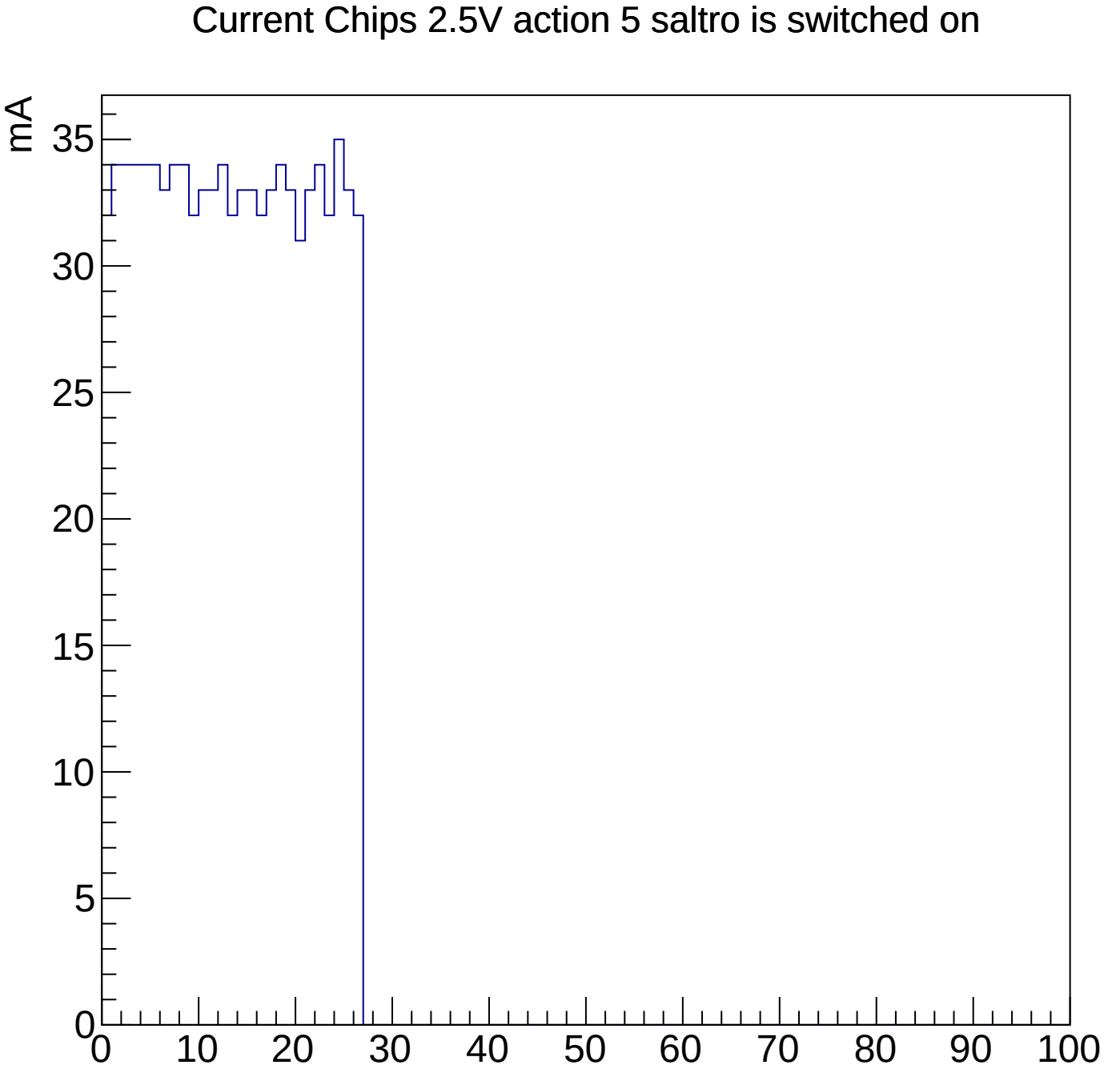

Current 2.5V action 2 readadc
hLvcardA25_2
Entries 27
Mean 32.74
Std Dev 1.109
Underflow 0
Overflow 0
Entries
0
2
4
6
8
10
0
10
20
30
40
50
60
70
80
90
100
mA

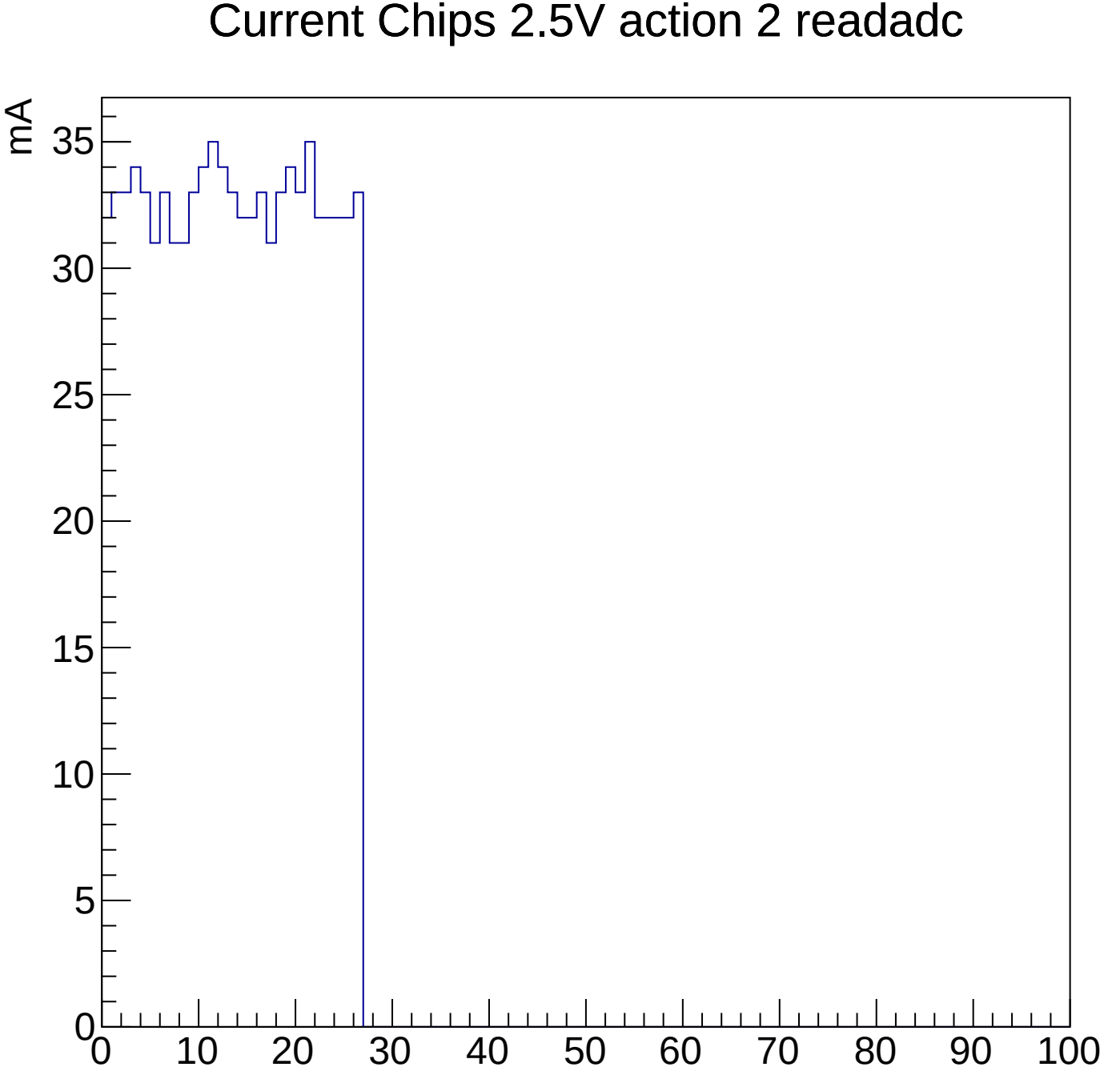

Current Chips 1.5V action 3 lvcard switch on
mA
0
5
10
15
20
25
30
35
40
45
0
10
20
30
40
50
60
70
80
90
100


Current Chips 1.5V action 5 saltro is switched on
mA
400
350
300
250
200
150
100
50
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 1.5V action 2 readadc
mA
400
350
300
250
200
150
100
50
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 2.5V action 3 lvcard switch on
mA
35
30
25
20
15
10
5
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 2.5V action 5 saltro is switched on
mA
35
30
25
20
15
10
5
0
0
10
20
30
40
50
60
70
80
90
100


Current Chips 2.5V action 2 readadc
mA
35
30
25
20
15
10
5
0
0
10
20
30
40
50
60
70
80
90
100