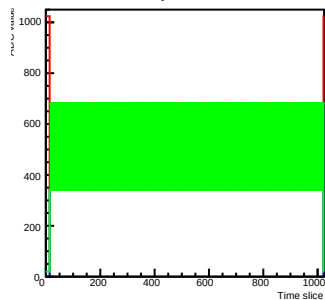


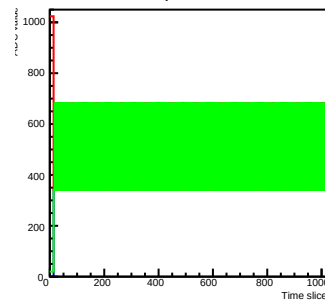
mcm 0 chip 0 chan 0 test1



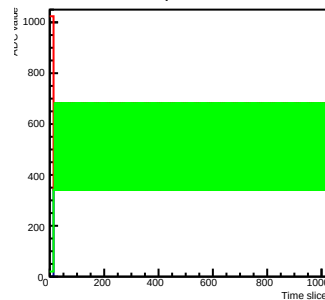
mcm 0 chip 0 chan 1 test1



mcm 0 chip 0 chan 2 test1



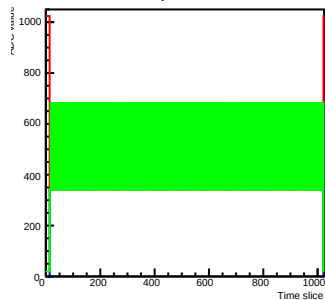
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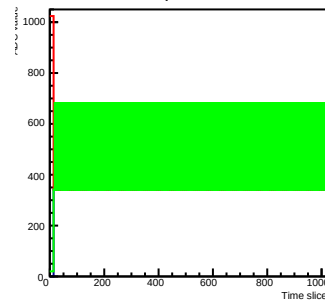
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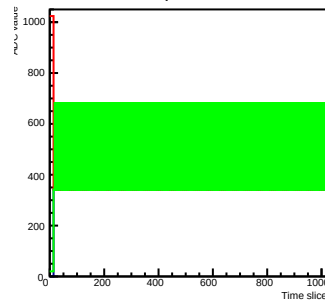
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mcm 0 chip 0 chan 6 test1



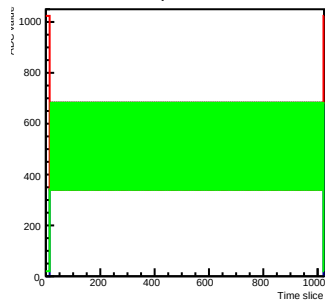
mcm 0 chip 0 chan 7 test1



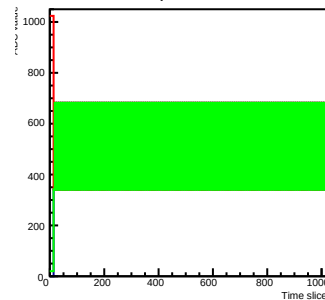
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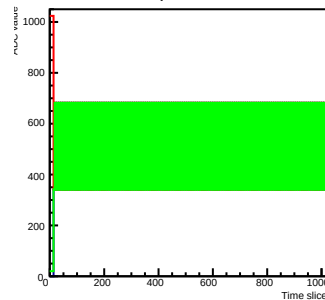
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mcm 0 chip 0 chan 10 test1



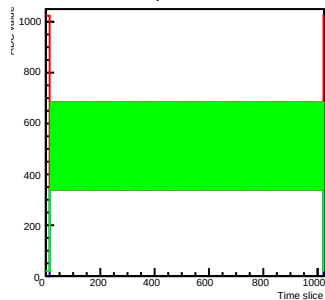
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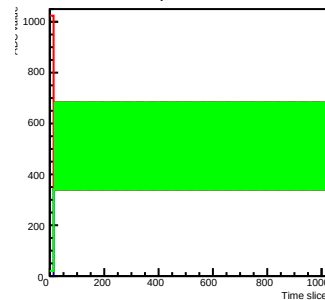
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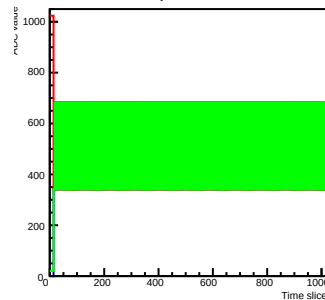
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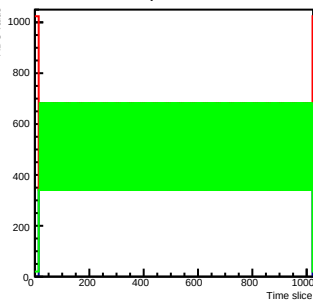
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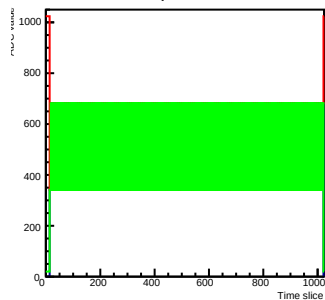
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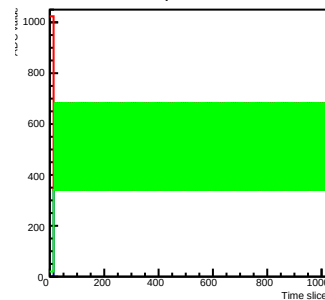
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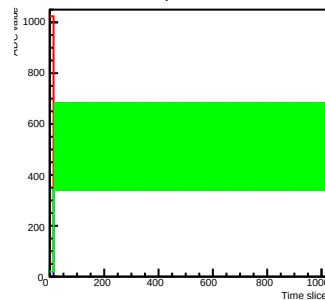
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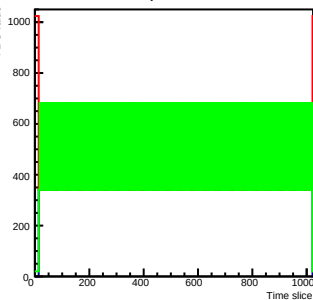
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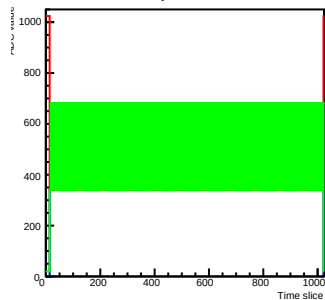
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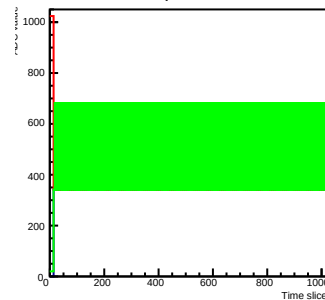
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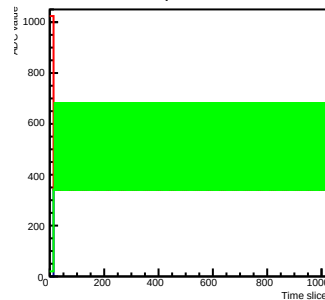
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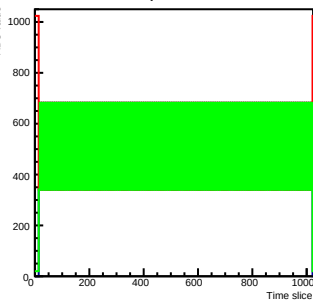
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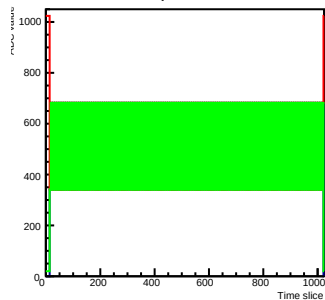
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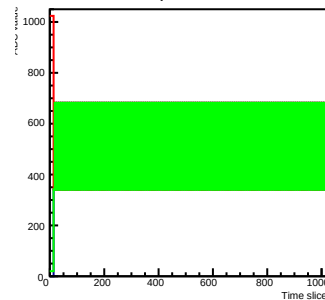
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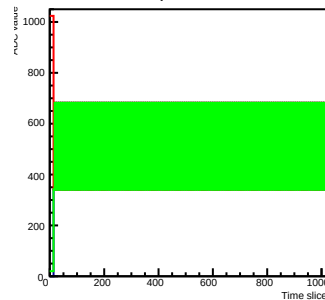
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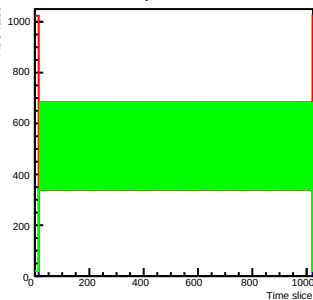
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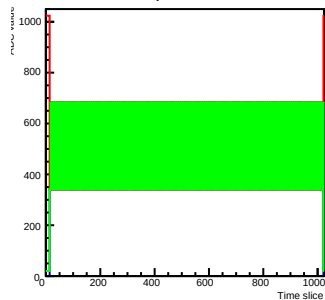
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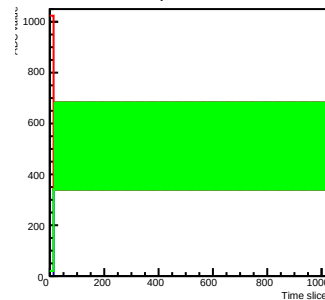
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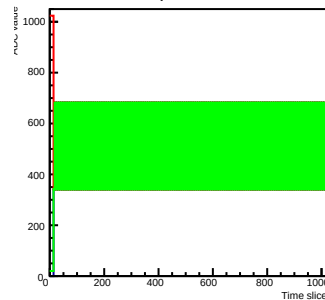
mcm 0 chip 1 chan 13 test1



mcm 0 chip 1 chan 14 test1



mcm 0 chip 1 chan 15 test1



mcm 0 chip 2 chan 0 test1



mcm 0 chip 2 chan 1 test1



mcm 0 chip 2 chan 2 test1



mcm 0 chip 2 chan 3 test1



mcm 0 chip 2 chan 4 test1



mcm 0 chip 2 chan 5 test1



mcm 0 chip 2 chan 6 test1



mcm 0 chip 2 chan 7 test1



mcm 0 chip 2 chan 8 test1



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mcm 0 chip 2 chan 11 test1



mcm 0 chip 2 chan 12 test1



mcm 0 chip 2 chan 13 test1



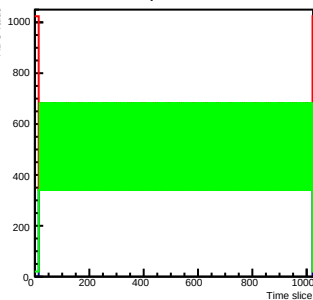
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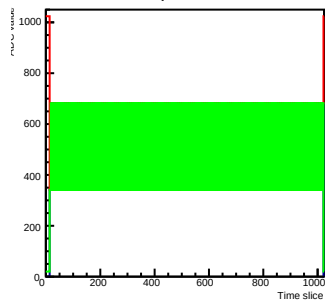
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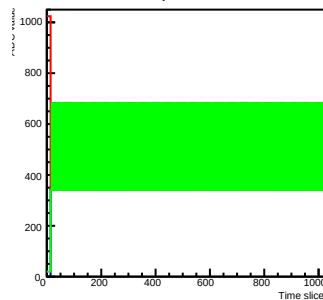
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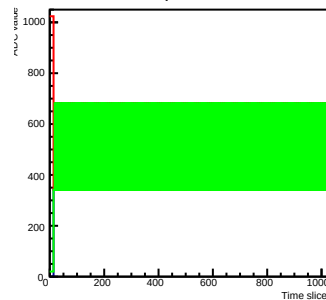
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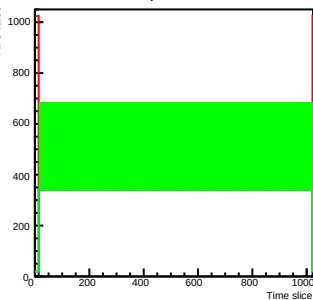
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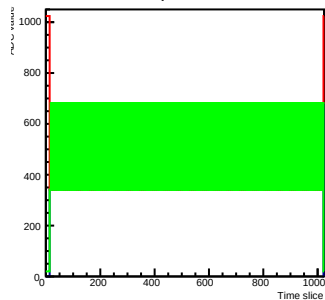
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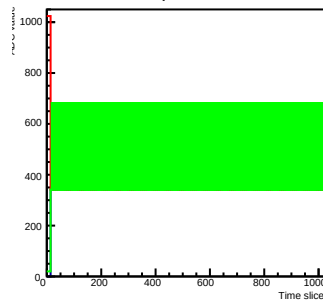
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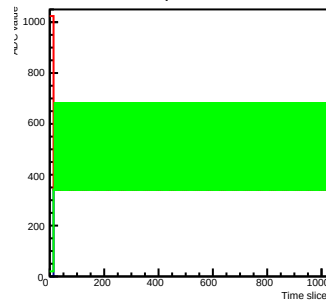
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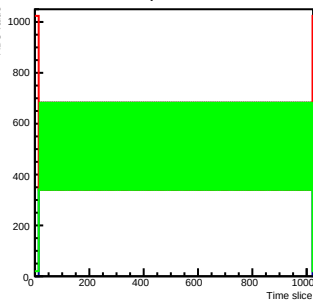
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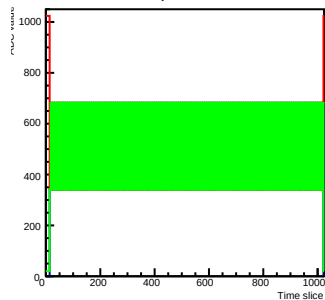
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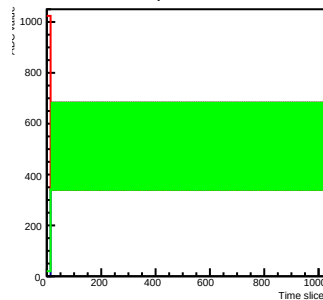
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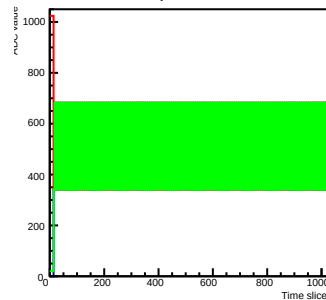
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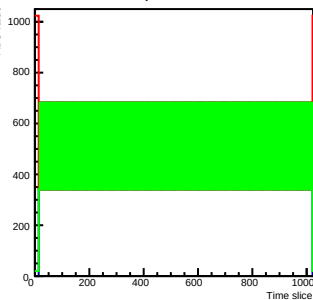
mcm 0 chip 3 chan 10 test1



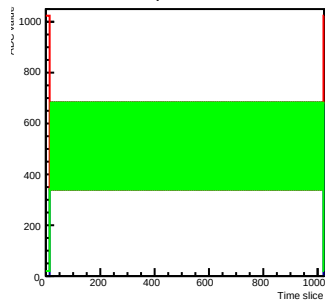
mcm 0 chip 3 chan 11 test1



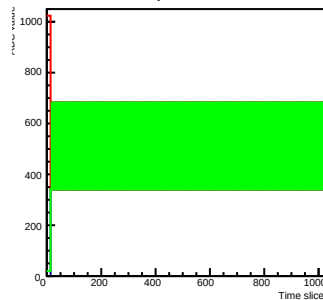
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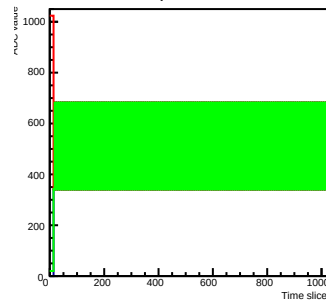
mcm 0 chip 3 chan 13 test1



mcm 0 chip 3 chan 14 test1



mcm 0 chip 3 chan 15 test1



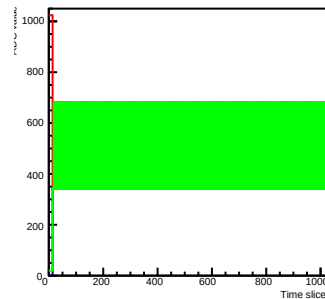
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mcm 0 chip 4 chan 1 test1



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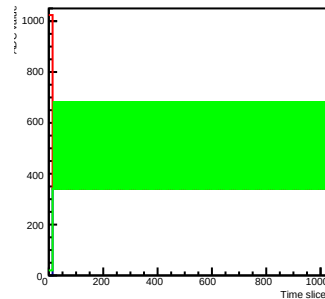
mcm 0 chip 4 chan 4 test1



mcm 0 chip 4 chan 5 test1



mcm 0 chip 4 chan 6 test1



mcm 0 chip 4 chan 7 test1



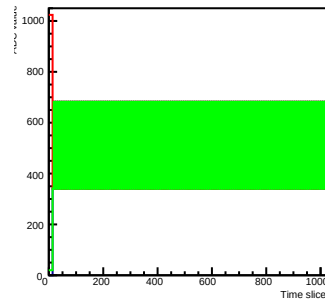
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mcm 0 chip 4 chan 9 test1



mcm 0 chip 4 chan 10 test1



mcm 0 chip 4 chan 11 test1



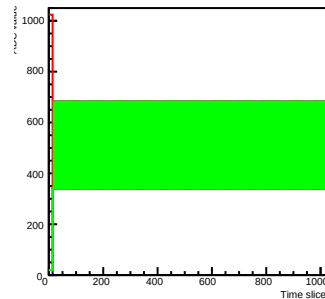
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mcm 0 chip 4 chan 13 test1



mcm 0 chip 4 chan 14 test1



mcm 0 chip 4 chan 15 test1



mcm 0 chip 5 chan 0 test1



mcm 0 chip 5 chan 1 test1



mcm 0 chip 5 chan 2 test1



mcm 0 chip 5 chan 3 test1



mcm 0 chip 5 chan 4 test1



mcm 0 chip 5 chan 5 test1



mcm 0 chip 5 chan 6 test1



mcm 0 chip 5 chan 7 test1



mcm 0 chip 5 chan 8 test1



mcm 0 chip 5 chan 9 test1



mcm 0 chip 5 chan 10 test1



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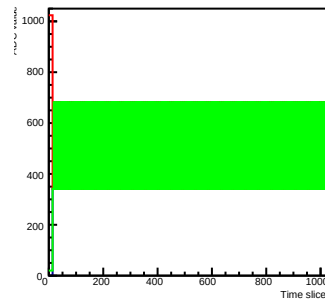
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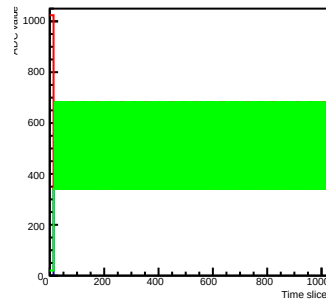
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mcm 0 chip 6 chan 3 test1



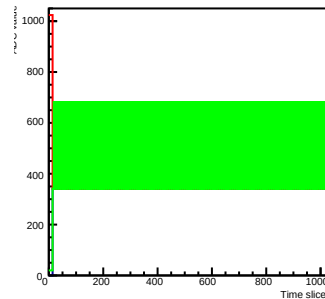
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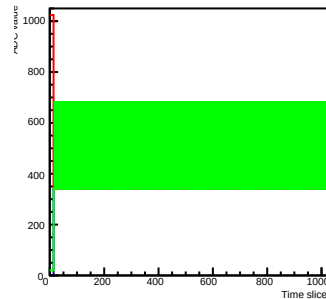
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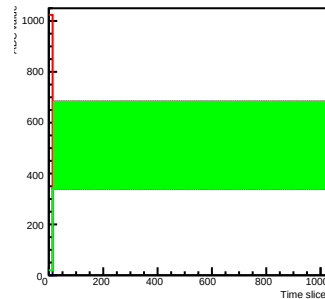
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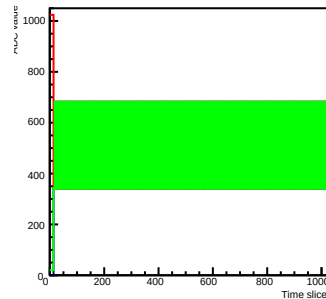
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mcm 0 chip 6 chan 10 test1



mcm 0 chip 6 chan 11 test1



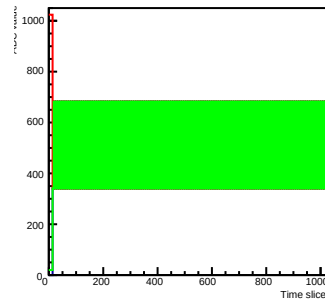
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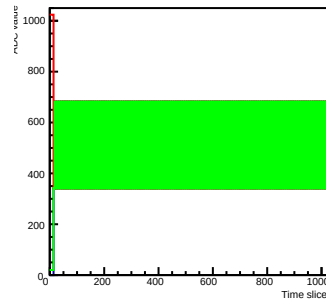
mcm 0 chip 6 chan 13 test1



mcm 0 chip 6 chan 14 test1



mcm 0 chip 6 chan 15 test1



mcm 0 chip 7 chan 0 test1



mcm 0 chip 7 chan 1 test1



mcm 0 chip 7 chan 2 test1



mcm 0 chip 7 chan 3 test1



mcm 0 chip 7 chan 4 test1



mcm 0 chip 7 chan 5 test1



mcm 0 chip 7 chan 6 test1



mcm 0 chip 7 chan 7 test1



mcm 0 chip 7 chan 8 test1



mcm 0 chip 7 chan 9 test1



mcm 0 chip 7 chan 10 test1



mcm 0 chip 7 chan 11 test1



mcm 0 chip 7 chan 12 test1



mcm 0 chip 7 chan 13 test1



mcm 0 chip 7 chan 14 test1



mcm 0 chip 7 chan 15 test1



mcm 0 chip 0 chan 0 test1



mcm 0 chip 0 chan 1 test1



mcm 0 chip 0 chan 2 test1



mcm 0 chip 0 chan 3 test1



mcm 0 chip 0 chan 4 test1



mcm 0 chip 0 chan 5 test1



mcm 0 chip 0 chan 6 test1



mcm 0 chip 0 chan 7 test1



mcm 0 chip 0 chan 8 test1



mcm 0 chip 0 chan 9 test1



mcm 0 chip 0 chan 10 test1



mcm 0 chip 0 chan 11 test1



mcm 0 chip 0 chan 12 test1



mcm 0 chip 0 chan 13 test1



mcm 0 chip 0 chan 14 test1



mcm 0 chip 0 chan 15 test1



mcm 0 chip 1 chan 0 test1



mcm 0 chip 1 chan 1 test1



mcm 0 chip 1 chan 2 test1



mcm 0 chip 1 chan 3 test1



mcm 0 chip 1 chan 4 test1



mcm 0 chip 1 chan 5 test1



mcm 0 chip 1 chan 6 test1



mcm 0 chip 1 chan 7 test1



mcm 0 chip 1 chan 8 test1



mcm 0 chip 1 chan 9 test1



mcm 0 chip 1 chan 10 test1



mcm 0 chip 1 chan 11 test1



mcm 0 chip 1 chan 12 test1



mcm 0 chip 1 chan 13 test1



mcm 0 chip 1 chan 14 test1



mcm 0 chip 1 chan 15 test1



A bar chart comparing ADC values between two groups. The y-axis is labeled 'ADC value' and ranges from 0 to 50,000 in increments of 10,000. The x-axis has two categories. The first group (left) has a single blue bar reaching approximately 50,000. The second group (right) also has a single blue bar reaching approximately 50,000.

The ADC histogram displays two sharp, narrow vertical peaks. The first peak is located at approximately 350 ADC units, and the second peak is at approximately 700 ADC units. The x-axis is labeled 'ADC value' and ranges from 200 to 1000 with major tick marks every 200 units.

[illegible]

mcm 0 chip 3 chan 0 test1



mcm 0 chip 3 chan 1 test1



mcm 0 chip 3 chan 2 test1



mcm 0 chip 3 chan 3 test1



mcm 0 chip 3 chan 4 test1



mcm 0 chip 3 chan 5 test1



mcm 0 chip 3 chan 6 test1



mcm 0 chip 3 chan 7 test1



mcm 0 chip 3 chan 8 test1



mcm 0 chip 3 chan 9 test1



mcm 0 chip 3 chan 10 test1



mcm 0 chip 3 chan 11 test1



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mcm 0 chip 3 chan 14 test1



mcm 0 chip 3 chan 15 test1



mcm 0 chip 4 chan 0 test1



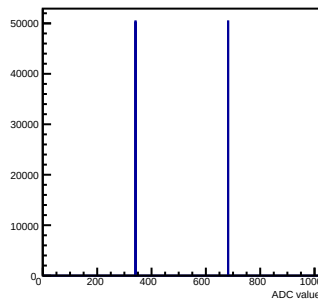
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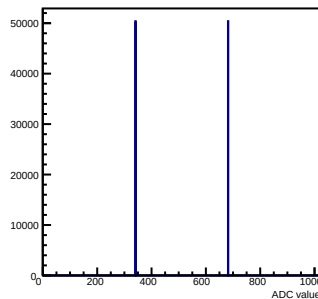
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mcm 0 chip 4 chan 7 test1



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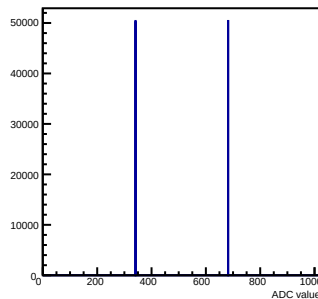
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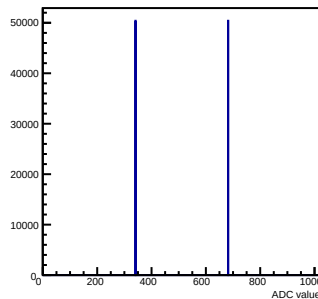
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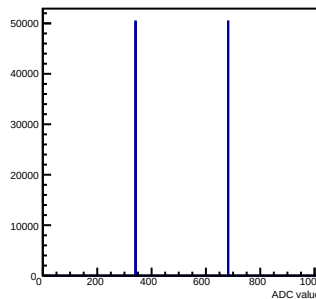
mcm 0 chip 5 chan 0 test1



mcm 0 chip 5 chan 1 test1



mcm 0 chip 5 chan 2 test1



mcm 0 chip 5 chan 3 test1



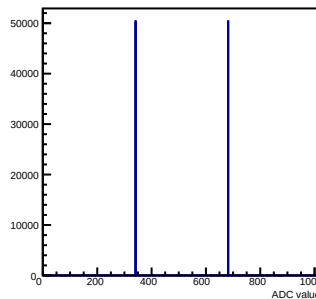
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mcm 0 chip 5 chan 5 test1



mcm 0 chip 5 chan 6 test1



mcm 0 chip 5 chan 7 test1



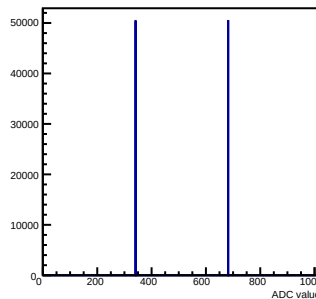
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mcm 0 chip 5 chan 9 test1



mcm 0 chip 5 chan 10 test1



mcm 0 chip 5 chan 11 test1



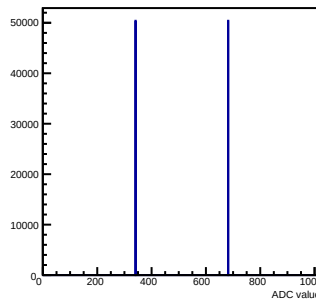
mcm 0 chip 5 chan 12 test1



mcm 0 chip 5 chan 13 test1



mcm 0 chip 5 chan 14 test1



mcm 0 chip 5 chan 15 test1



mcm 0 chip 6 chan 0 test1



mcm 0 chip 6 chan 1 test1



mcm 0 chip 6 chan 2 test1



mcm 0 chip 6 chan 3 test1



mcm 0 chip 6 chan 4 test1



mcm 0 chip 6 chan 5 test1



mcm 0 chip 6 chan 6 test1



mcm 0 chip 6 chan 7 test1



mcm 0 chip 6 chan 8 test1



mcm 0 chip 6 chan 9 test1



mcm 0 chip 6 chan 10 test1



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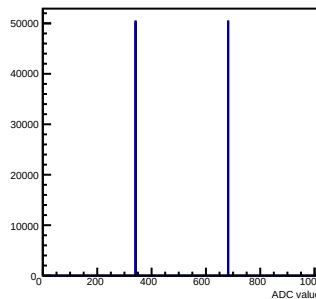
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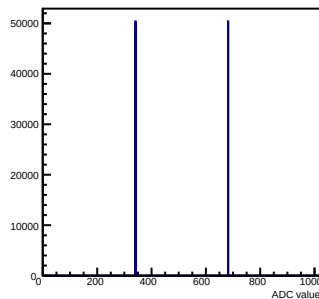
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mcm 0 chip 7 chan 2 test1



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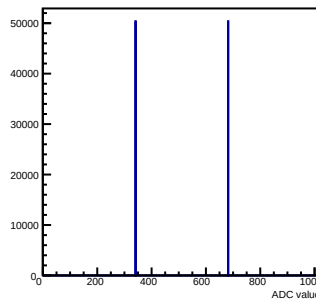
mcm 0 chip 7 chan 4 test1



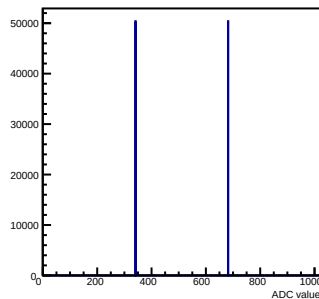
mcm 0 chip 7 chan 5 test1



mcm 0 chip 7 chan 6 test1



mcm 0 chip 7 chan 7 test1



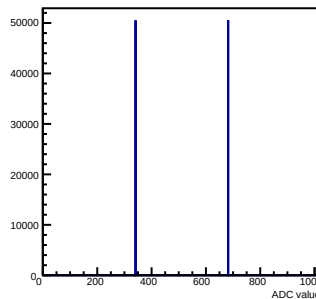
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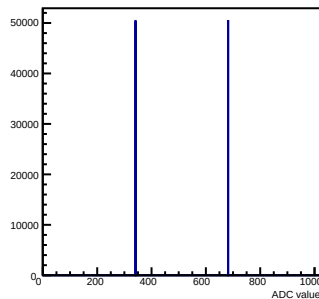
mcm 0 chip 7 chan 9 test1



mcm 0 chip 7 chan 10 test1



mcm 0 chip 7 chan 11 test1



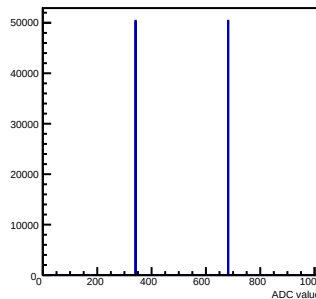
mcm 0 chip 7 chan 12 test1



mcm 0 chip 7 chan 13 test1



mcm 0 chip 7 chan 14 test1



mcm 0 chip 7 chan 15 test1

