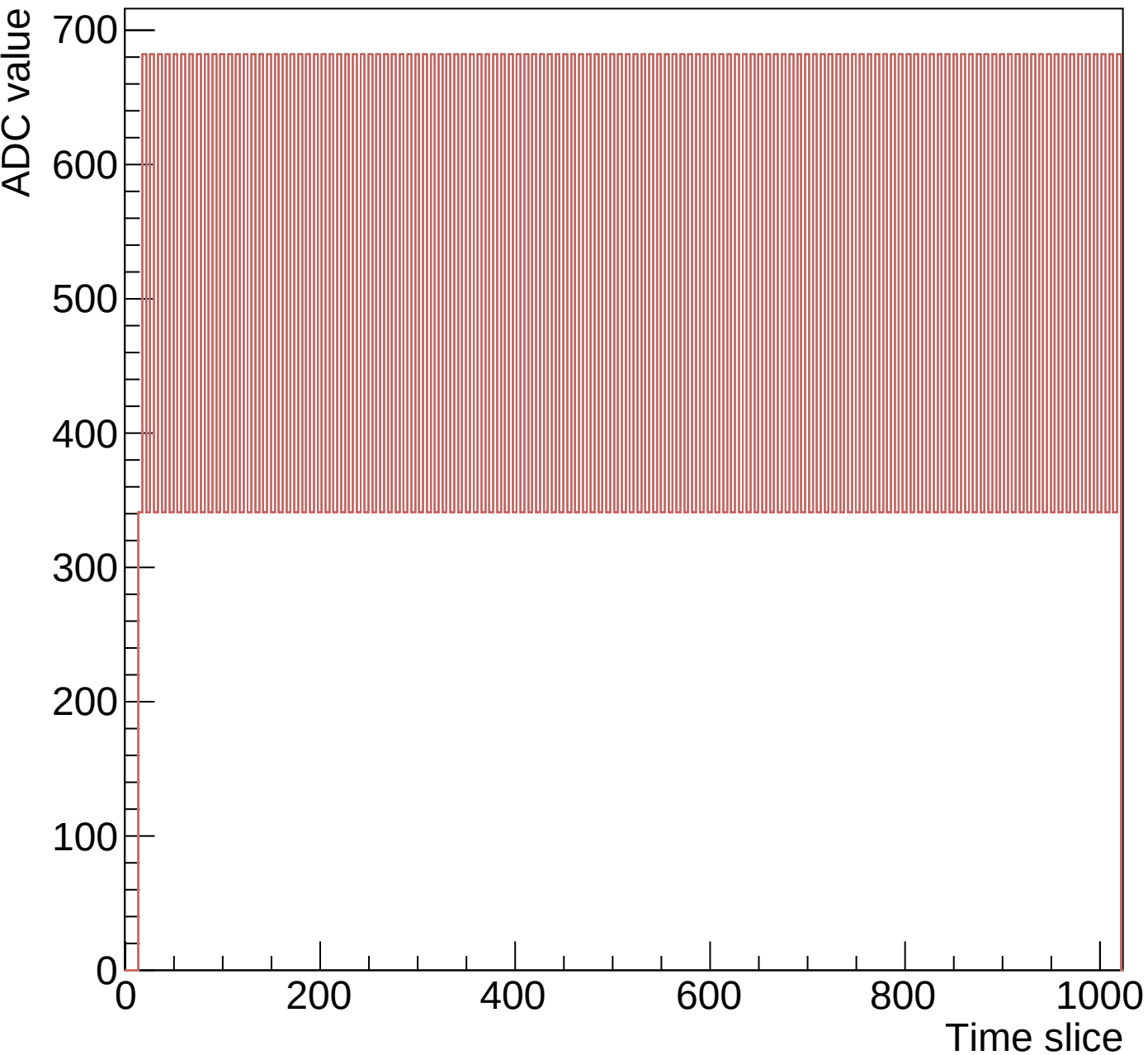
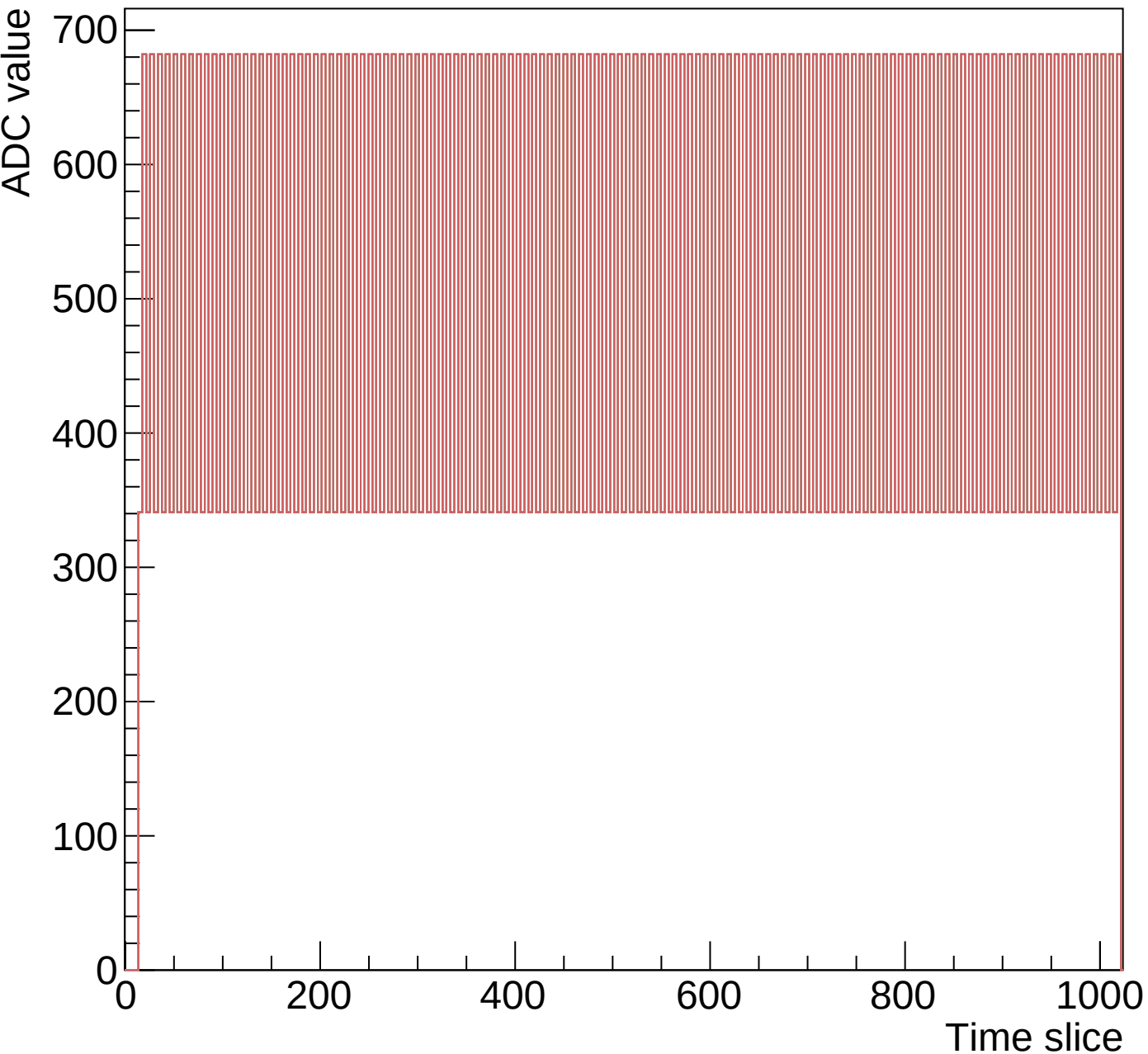


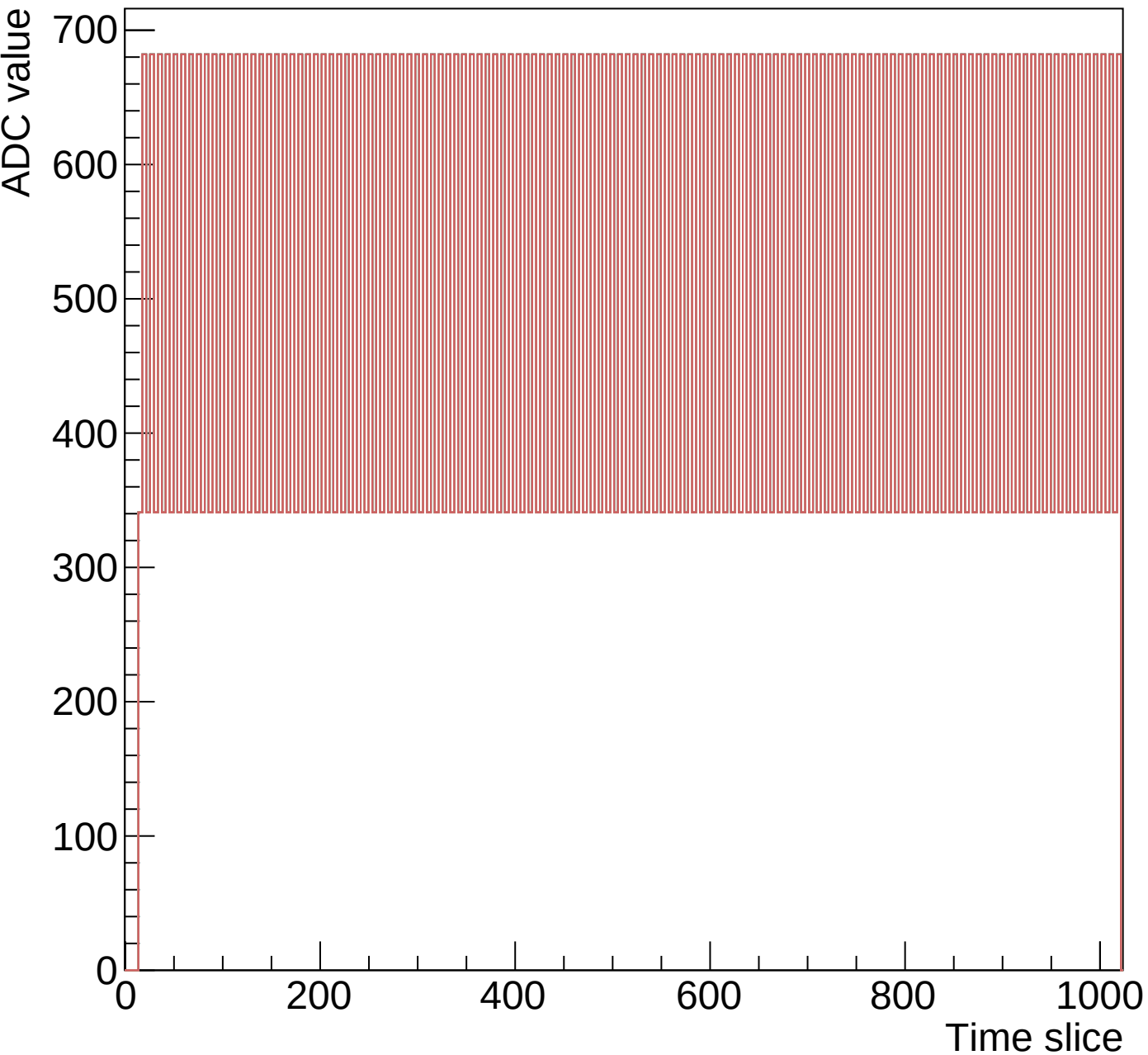
Run 0 event 2 mcm 0 chip 0 t



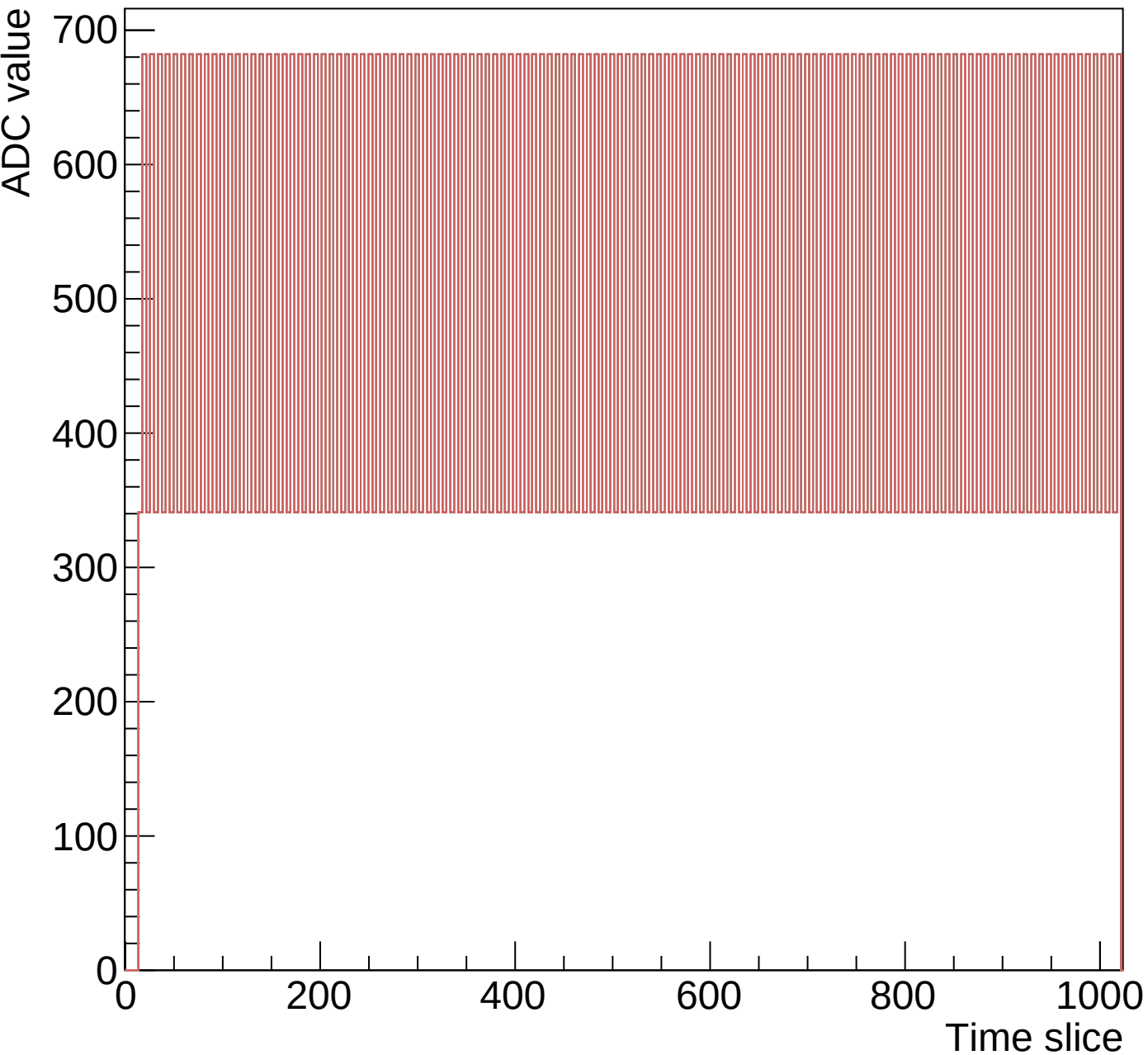
Run 0 event 2 mcm 0 chip 1 t



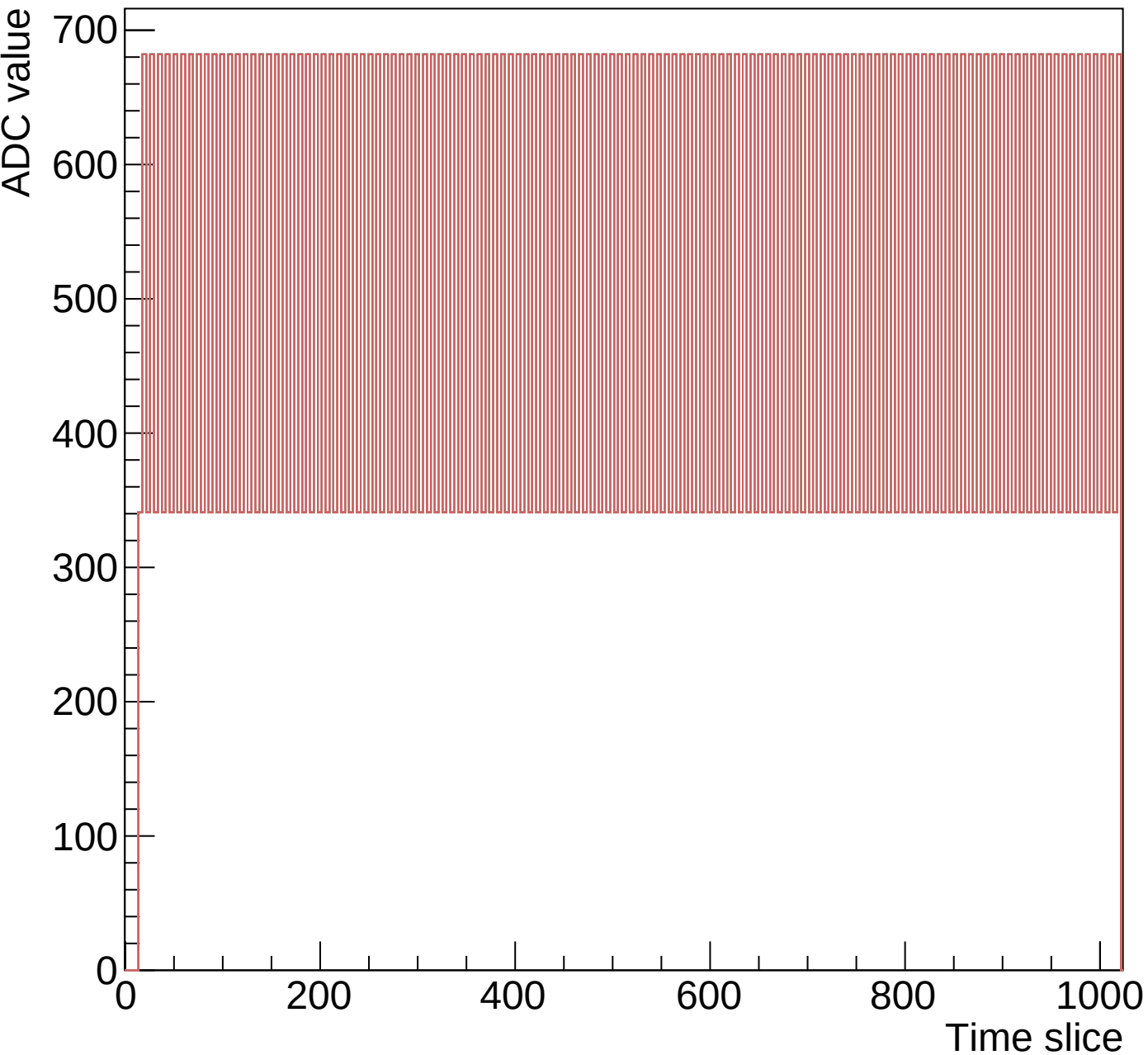
Run 0 event 2 mcm 0 chip 2 t



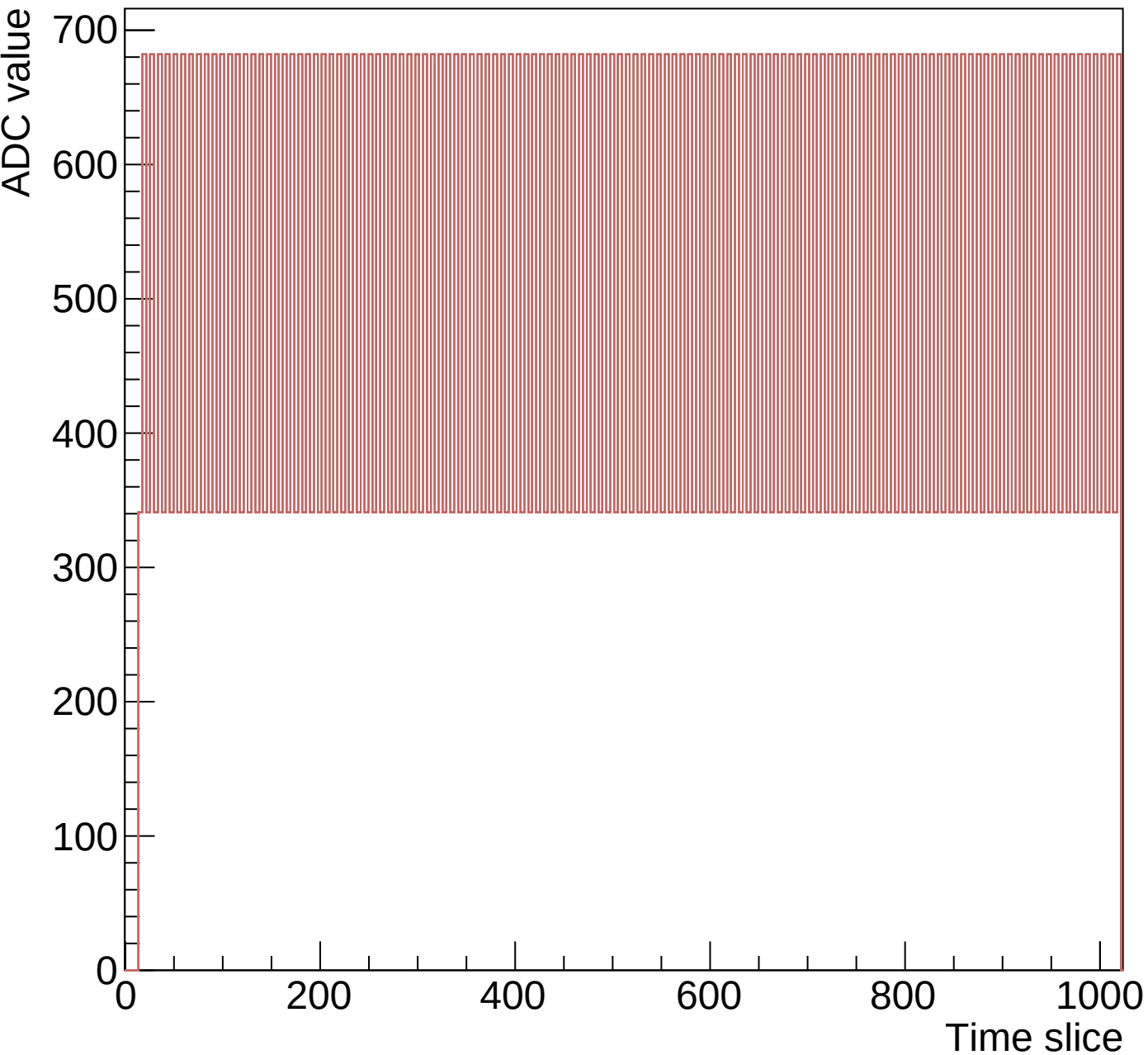
Run 0 event 2 mcm 0 chip 3 t



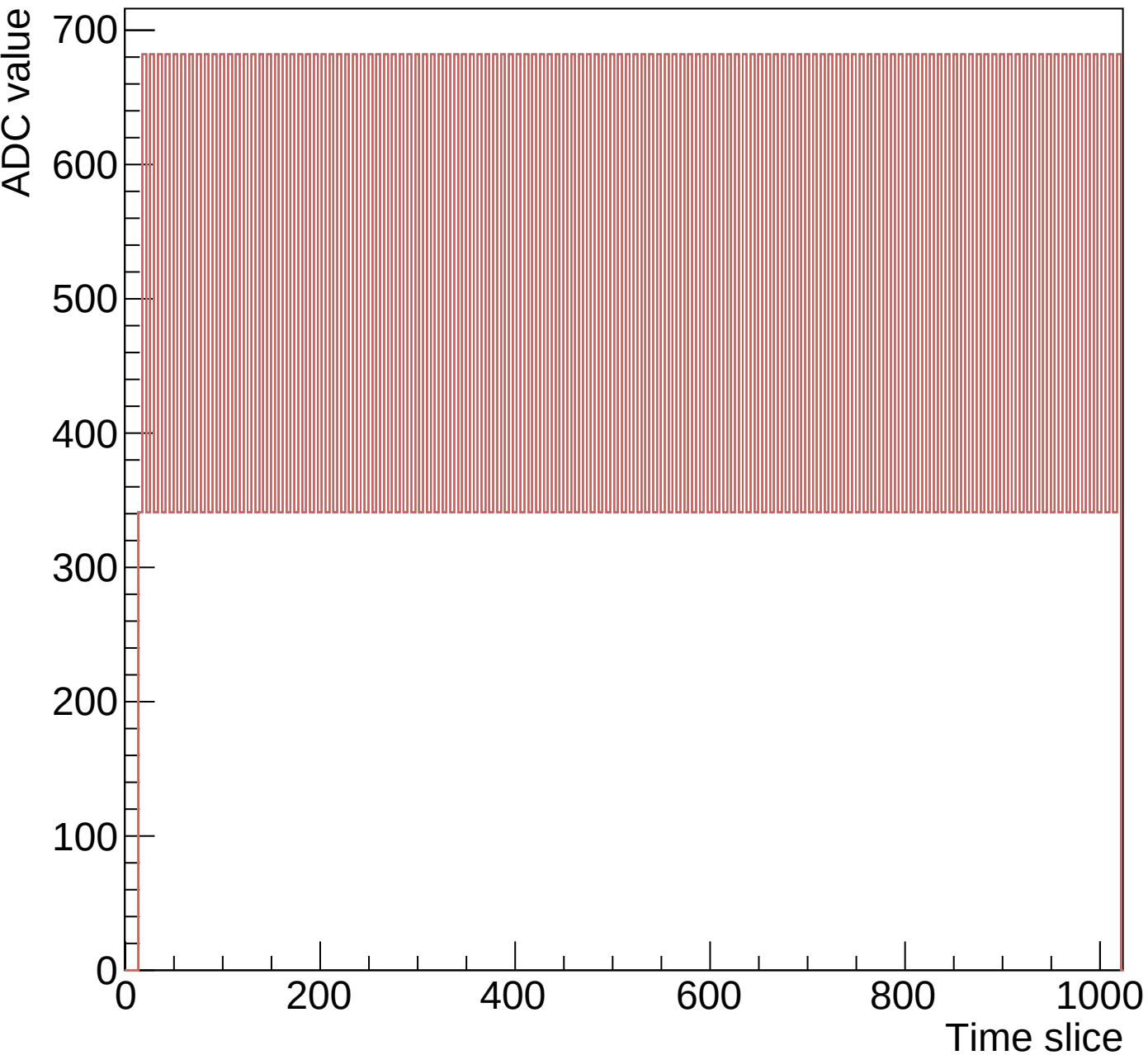
Run 0 event 2 mcm 0 chip 4 t



Run 0 event 2 mcm 0 chip 5 t



Run 0 event 2 mcm 0 chip 6 t



Run 0 event 2 mcm 0 chip 7 t

