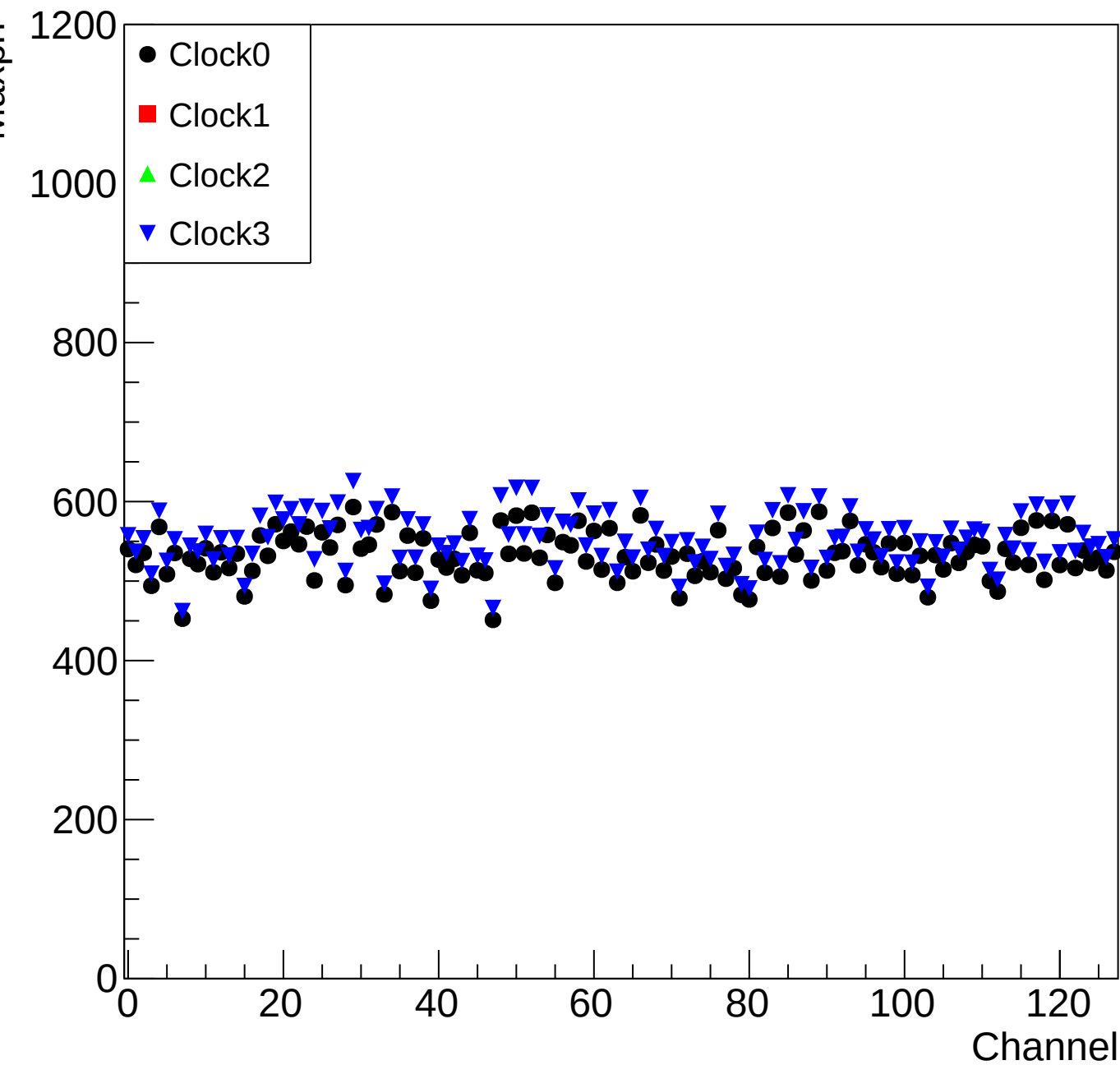


Maxph clocks 0-3



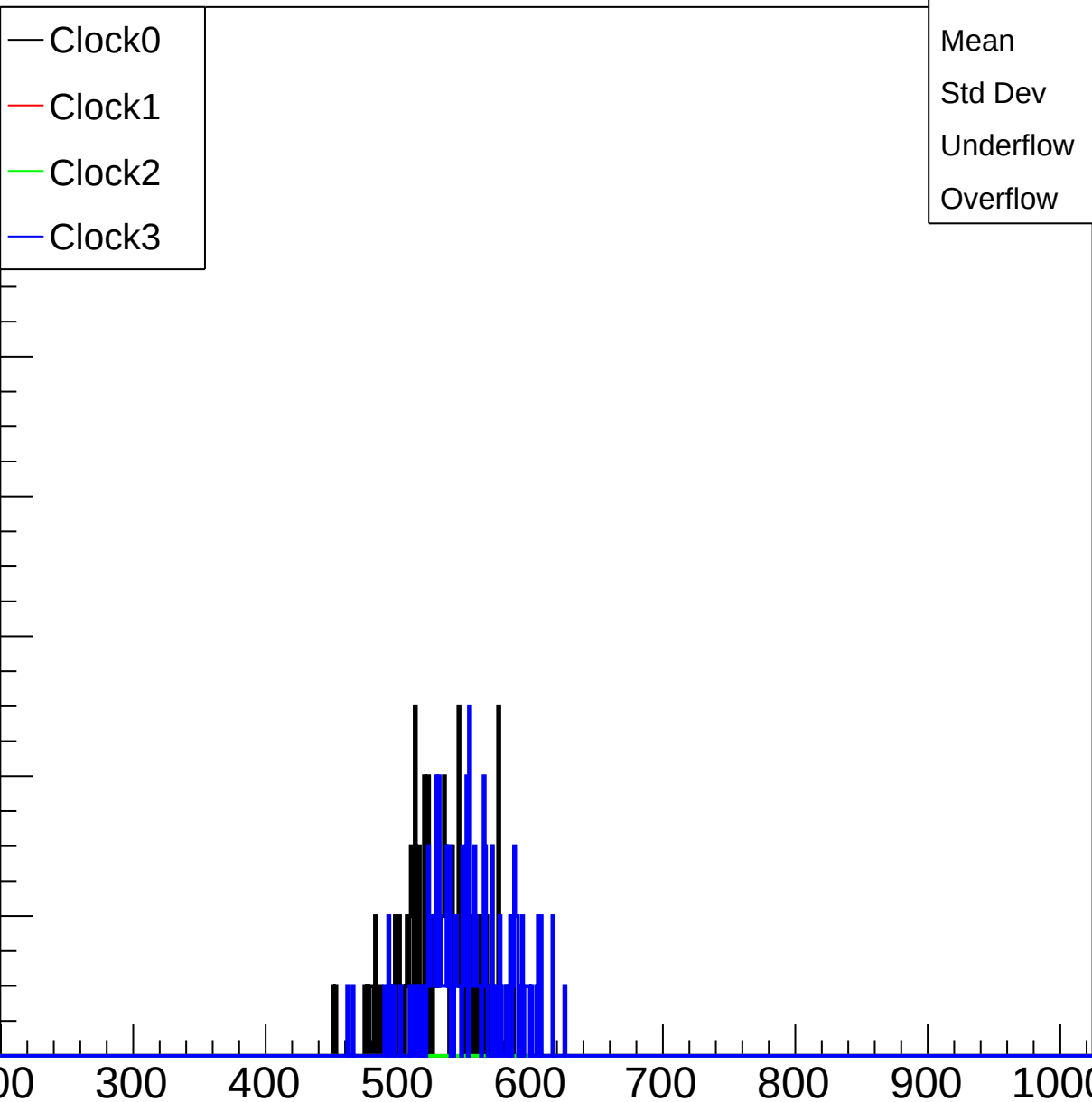
Maxph all clocks 0-3

Entries

- Clock0
- Clock1
- Clock2
- Clock3

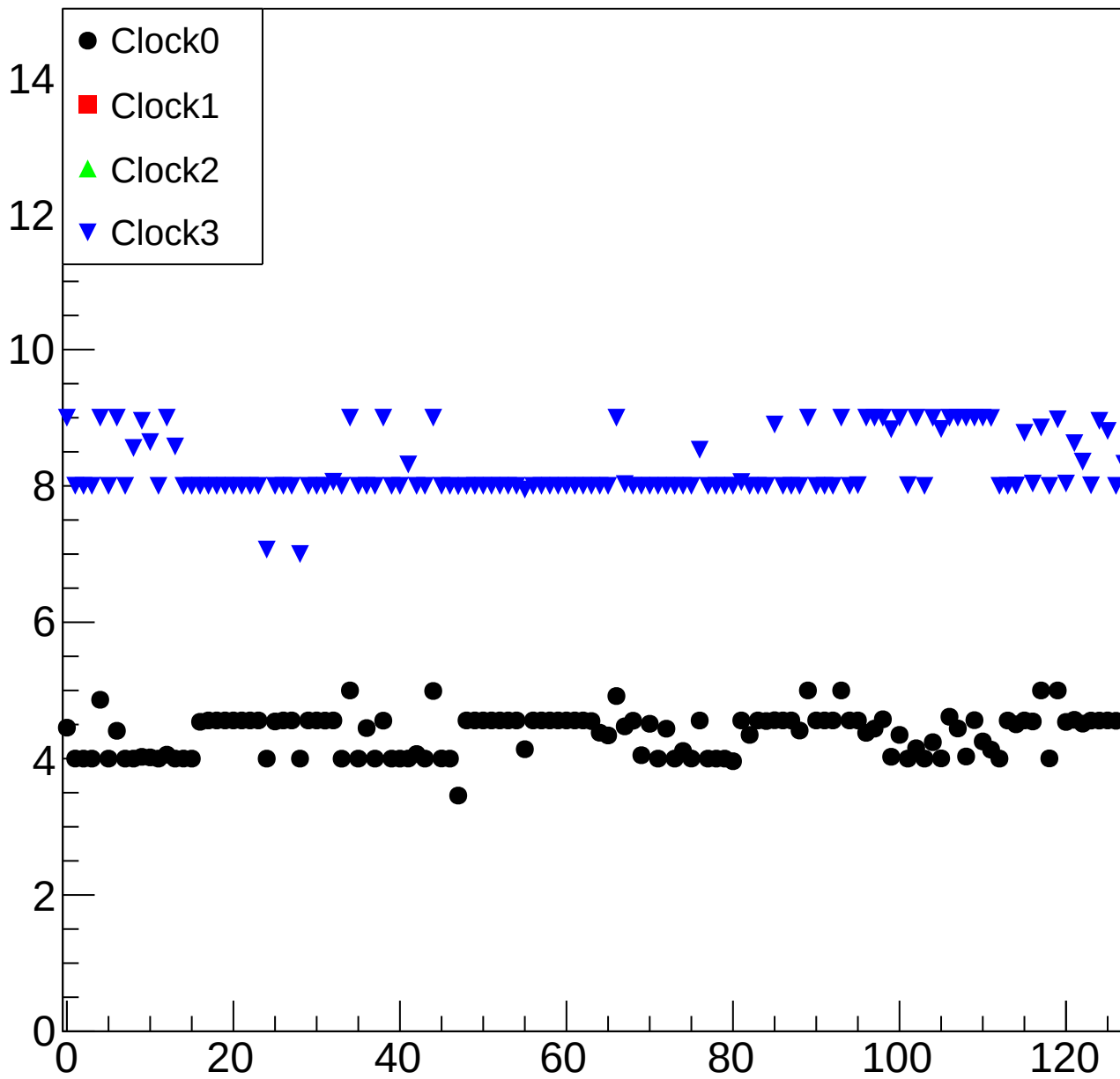
Entries	128
Mean	532
Std Dev	29.24
Underflow	0
Overflow	0

14
12
10
8
6
4
2
0



ADC

Clocks width 0-3



Clocks width all 0-3

