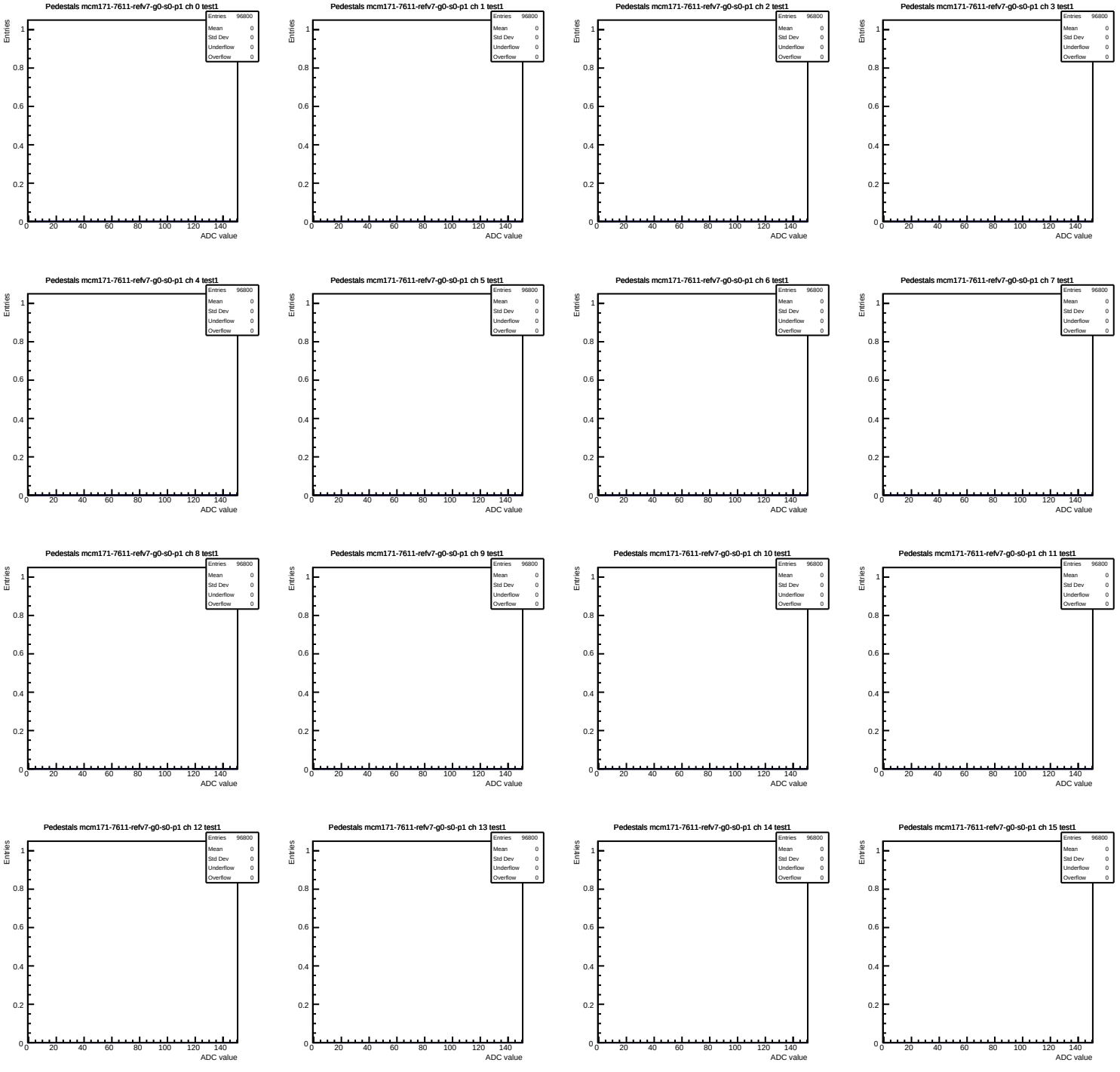
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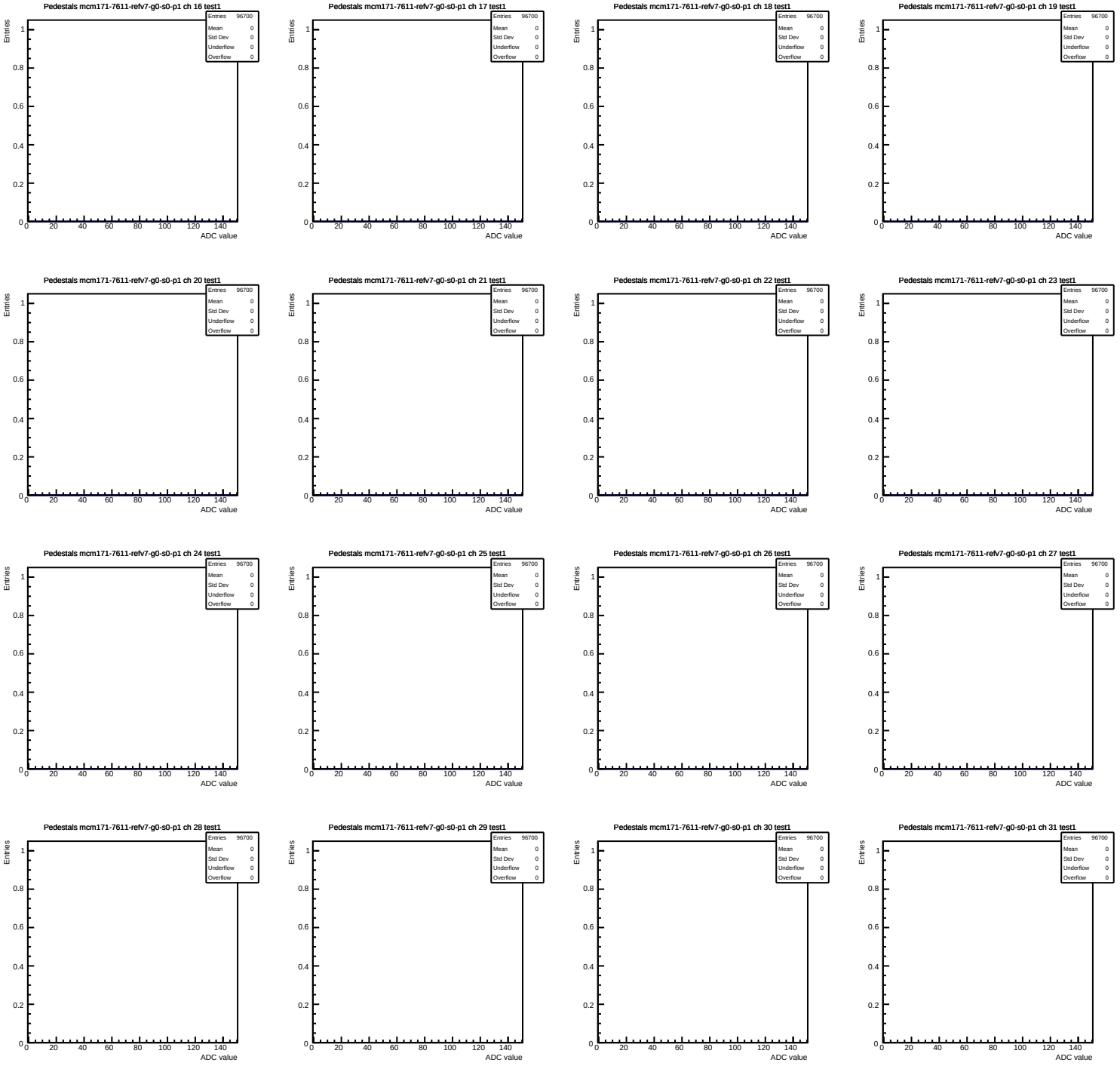
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 1 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 2 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 3 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 4 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 5 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 6 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 7 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 8 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 9 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 10 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 11 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 12 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 13 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 14 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 15 test1
Entries 96800
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 16 test1
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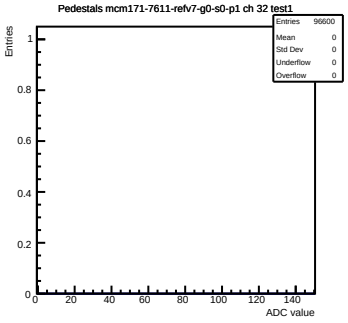
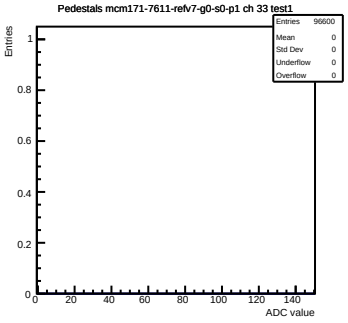
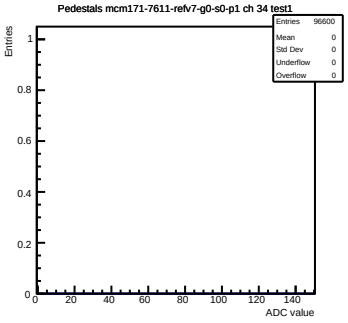
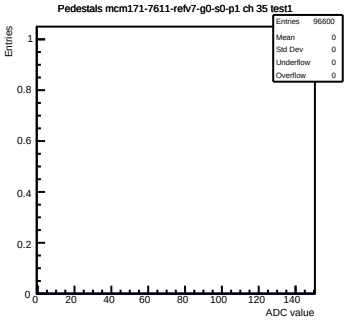
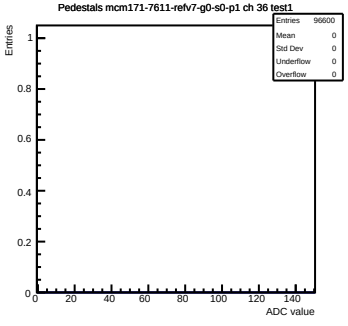
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 18 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 19 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 20 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 21 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 22 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 23 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 24 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 25 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 26 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 27 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 28 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 29 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 30 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 31 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

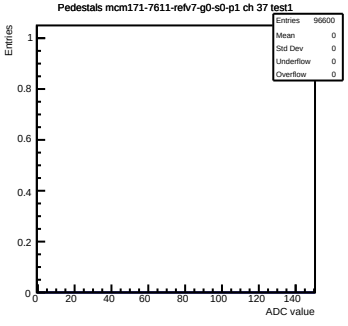

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 32 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

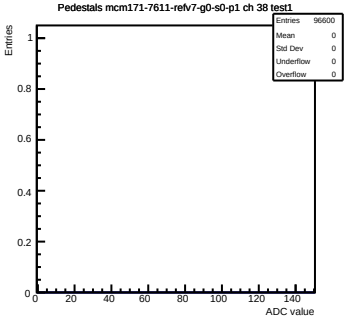

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 33 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

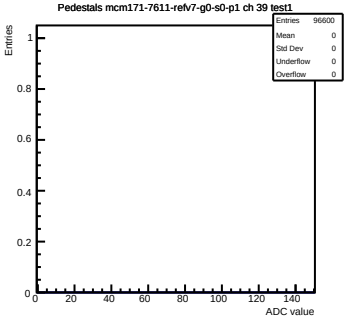

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 34 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

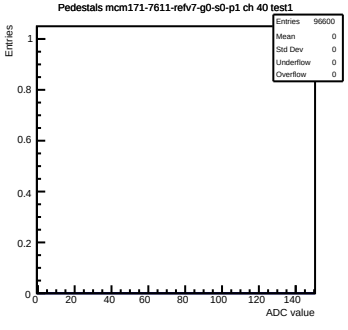

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 35 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

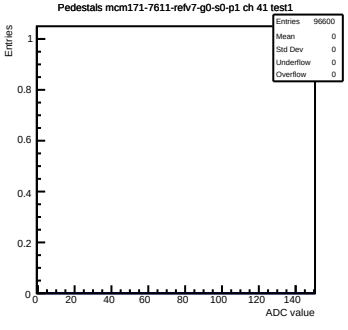

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 36 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

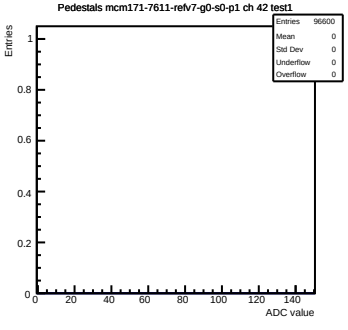

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 37 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

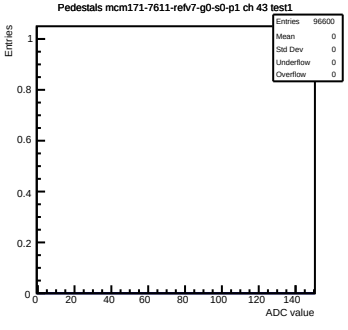

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 38 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

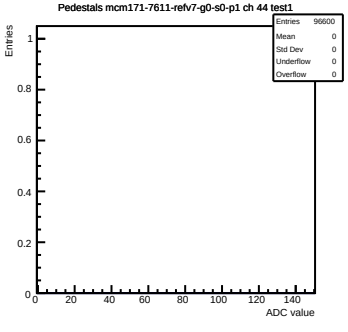

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 39 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

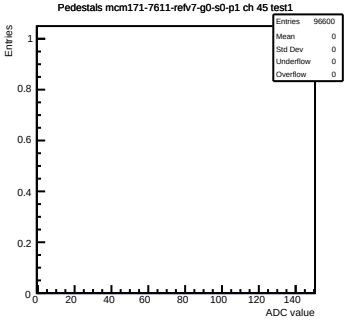

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 40 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

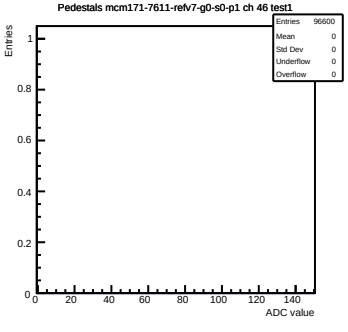

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 41 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

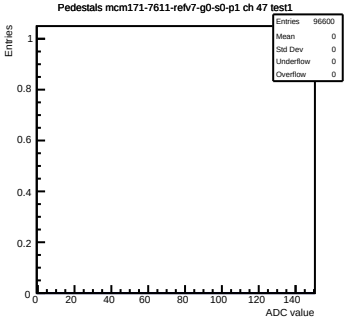

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 42 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

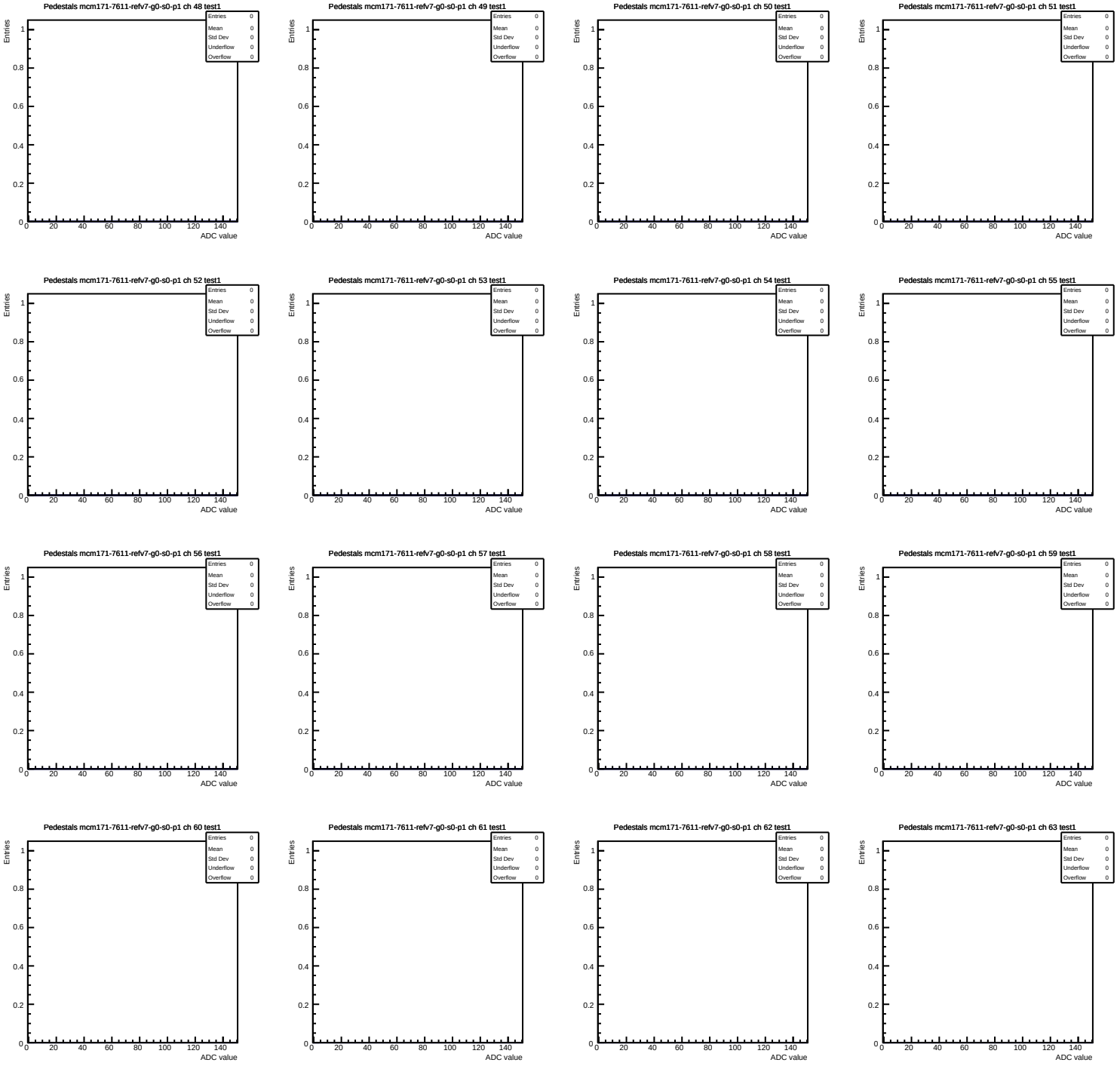

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 43 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

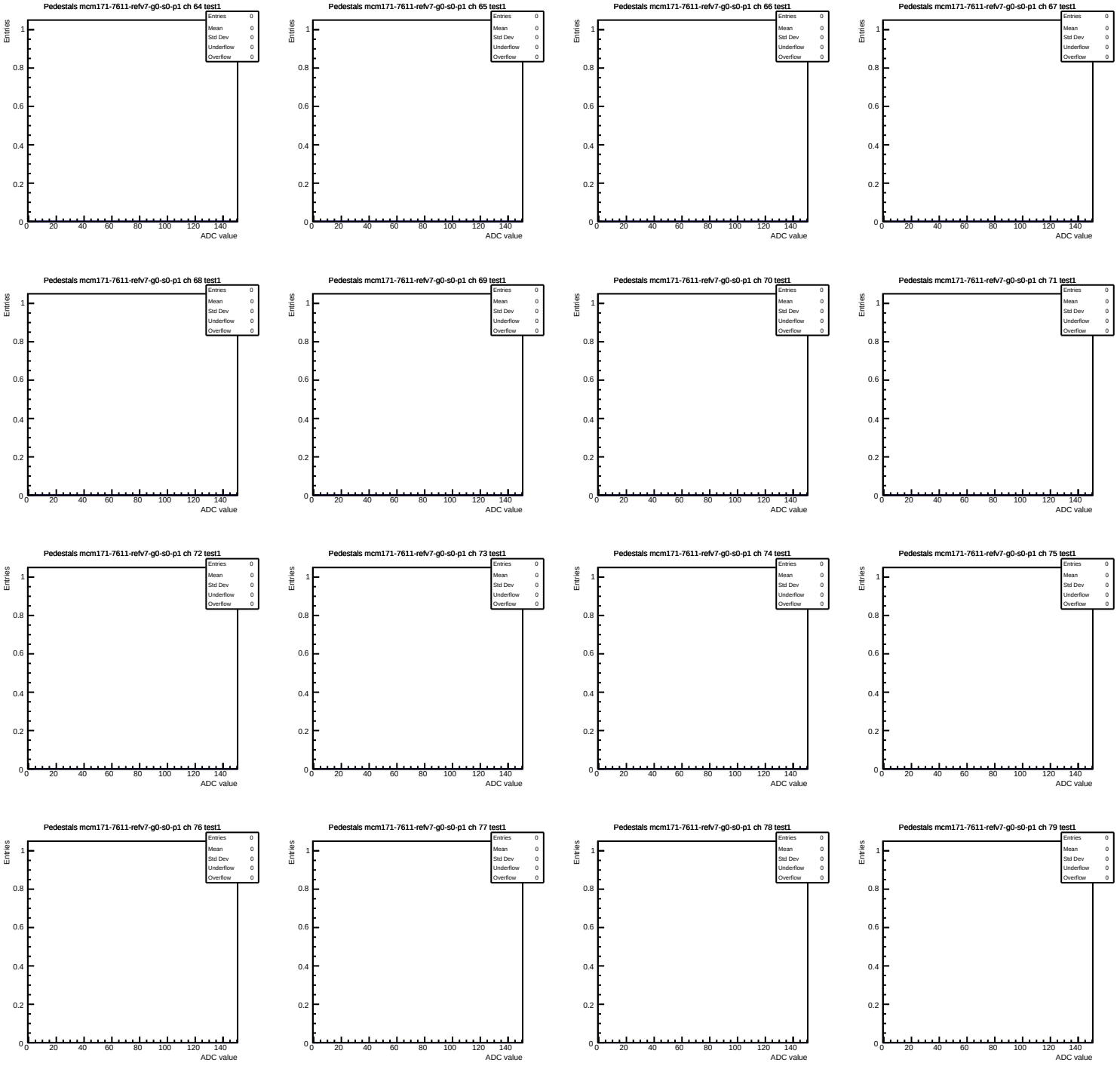

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 44 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 45 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 46 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 47 test1
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 48 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 49 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 50 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 51 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 52 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 53 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 54 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 55 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 56 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 57 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 58 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 59 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 60 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 61 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 62 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 63 test1
Entries 0
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Pedestals mcm171-7611-refv7-g0-s0-p1 ch 65 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 66 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 67 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 68 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 69 test1
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Pedestals mcm171-7611-refv7-g0-s0-p1 ch 74 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 75 test1
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Pedestals mcm171-7611-refv7-g0-s0-p1 ch 77 test1
Pedestals mcm171-7611-refv7-g0-s0-p1 ch 78 test1
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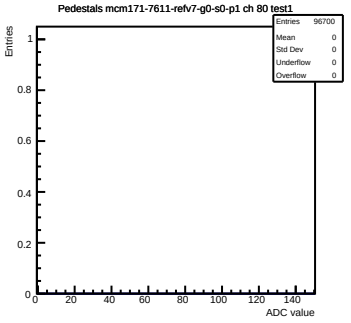
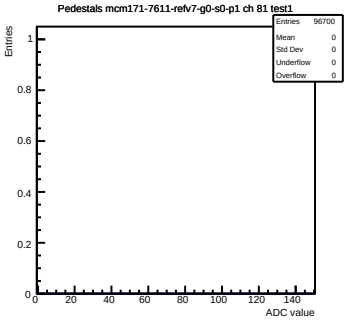
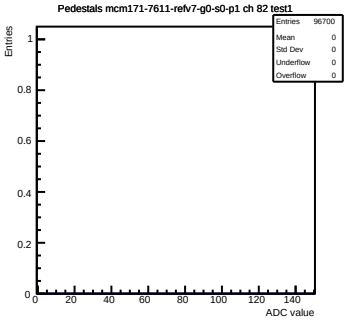
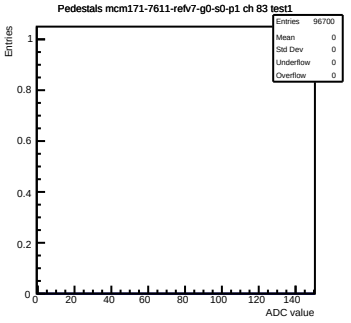
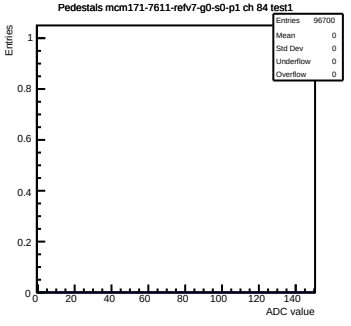
Entries 0
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

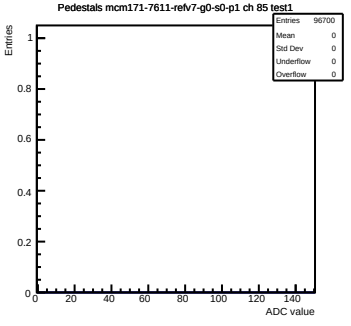

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 80 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

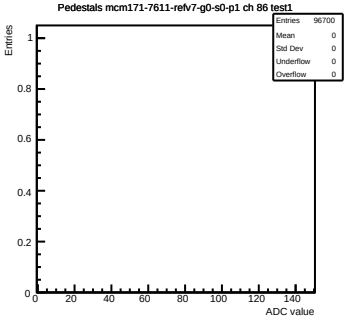

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 81 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

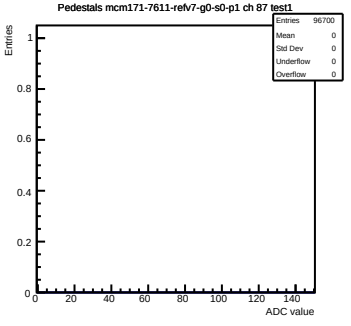

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 82 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

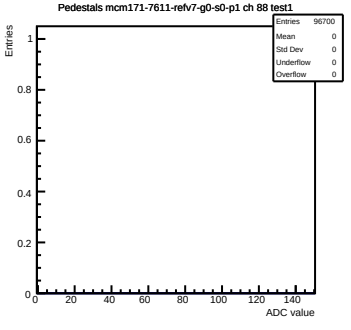

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 83 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value

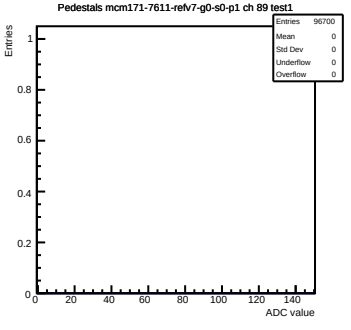

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 84 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 85 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 86 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 87 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 88 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 89 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
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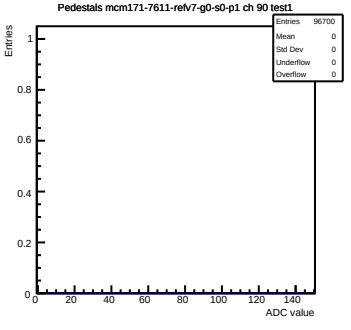
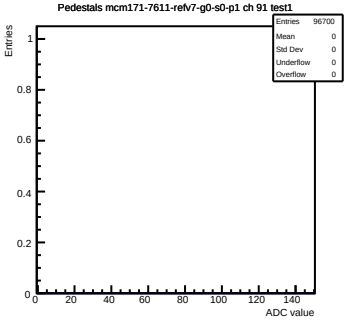
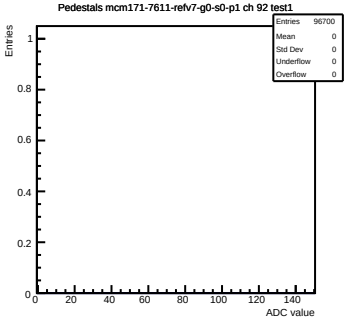
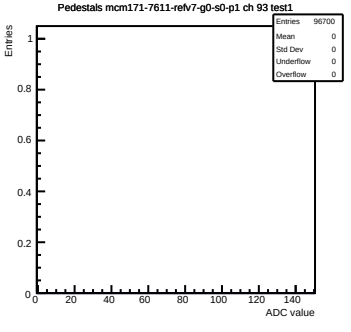
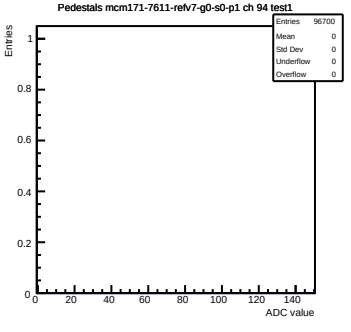
ADC value

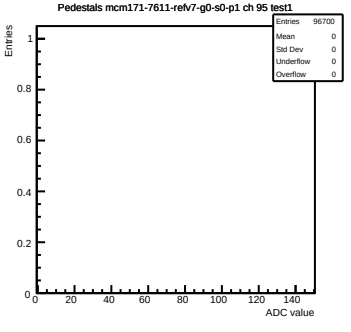

Pedestals mcm171-7611-refv7-g0-s0-p1 ch 90 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 91 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 92 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 93 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value


Pedestals mcm171-7611-refv7-g0-s0-p1 ch 94 test1
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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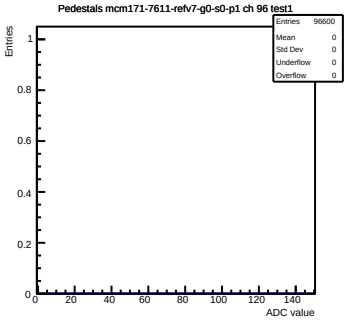
Entries 96700
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Entries 96600
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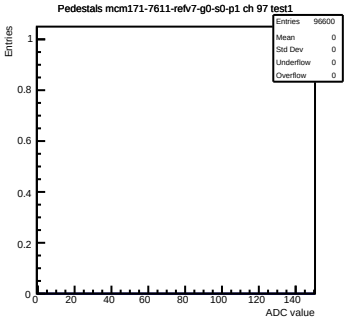
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Entries 96600
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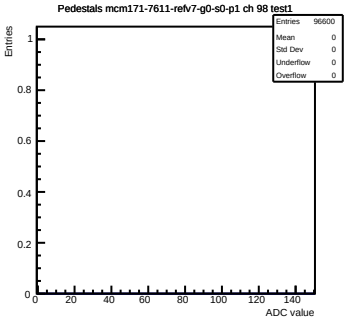
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Entries 96600
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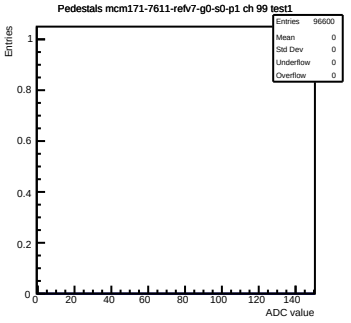
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Entries 96600
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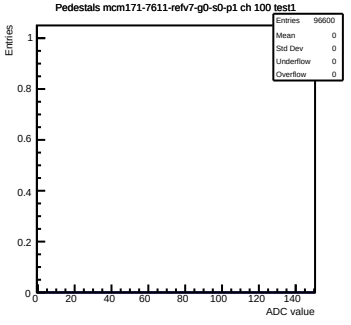
Entries
ADC value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Entries 96600
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Overflow 0
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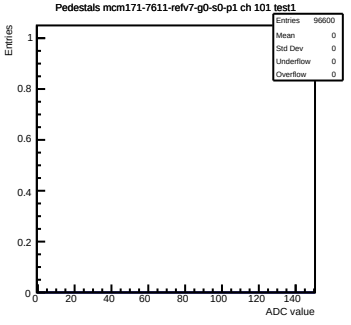
ADC value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Entries 96600
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Std Dev 0
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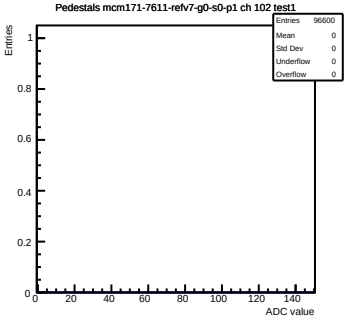
Overflow 0
Entries
ADC value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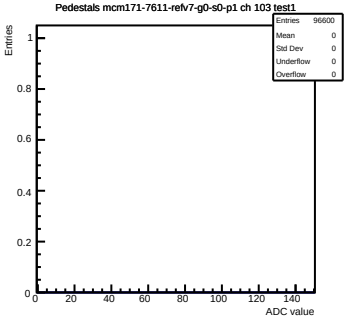
Entries 96600
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Entries 96600
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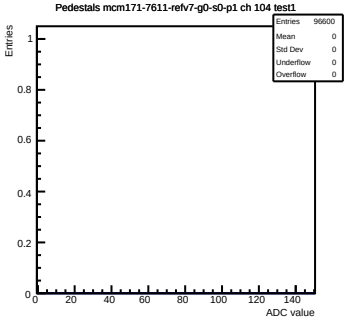
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Underflow 0
Overflow 0
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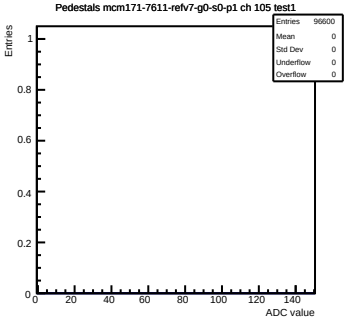
ADC value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Entries 96600
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Std Dev 0
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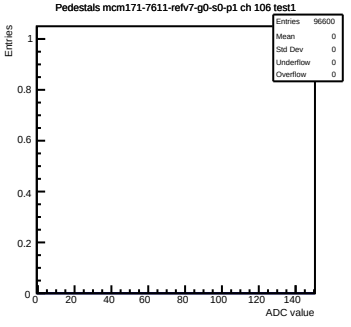
Overflow 0
Entries
ADC value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Entries 96600
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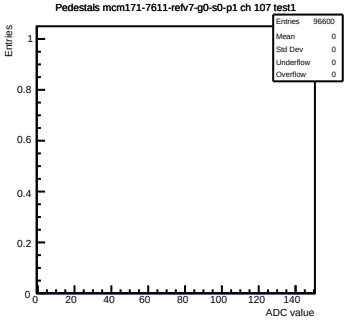
Overflow 0
Entries
ADC value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Entries 96600
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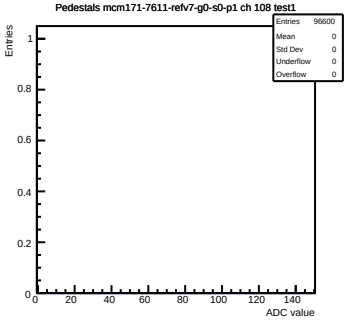
Overflow 0
Entries
ADC value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Entries 96600
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Std Dev 0
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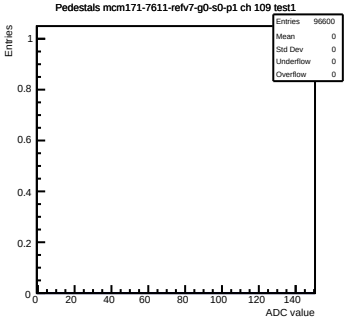
Overflow 0
Entries
ADC value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Entries 96600
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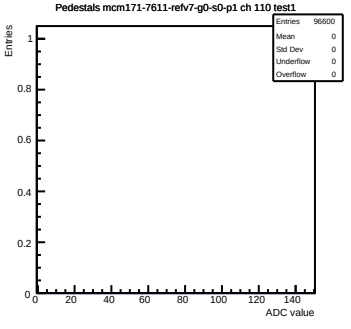
ADC value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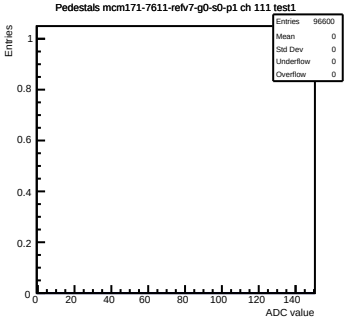
ADC value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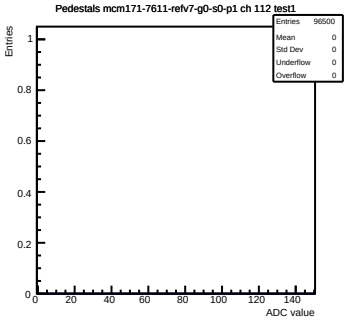
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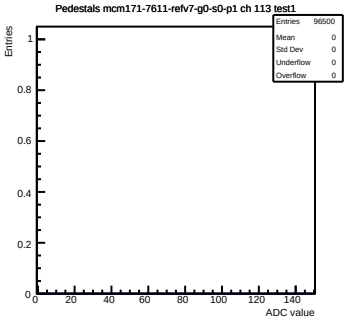
ADC value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Entries 96500
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Overflow 0
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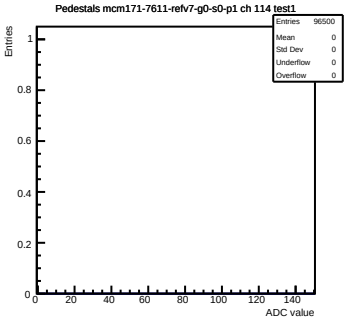
ADC value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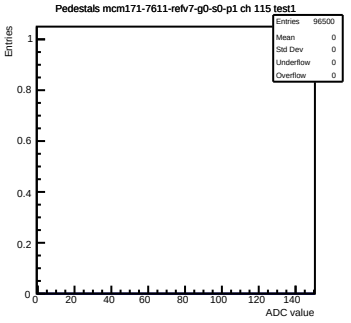
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Std Dev 0
Underflow 0
Overflow 0
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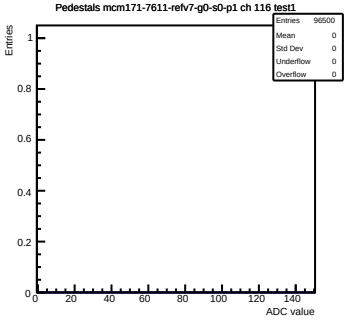
ADC value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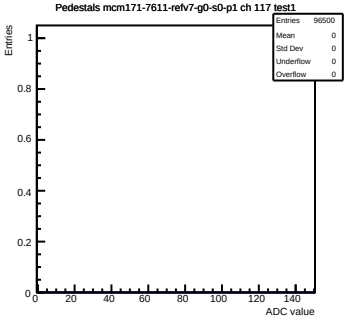
Entries 96500
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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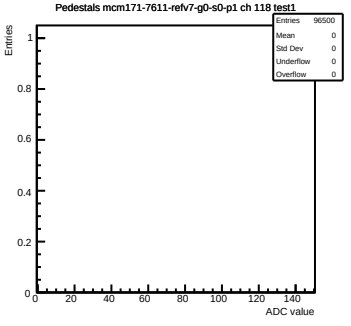
Entries 96500
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Std Dev 0
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Overflow 0
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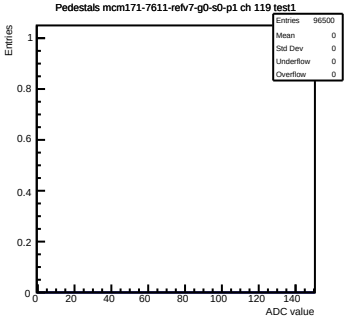
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Entries 96500
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Std Dev 0
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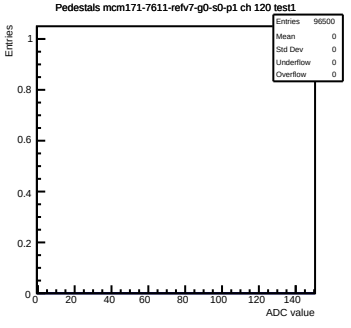
Overflow 0
Entries
ADC value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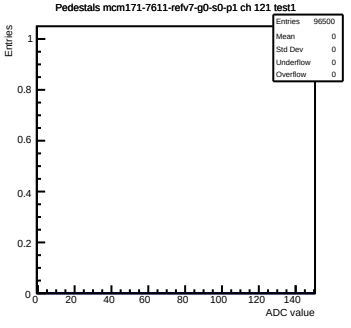
Entries 96500
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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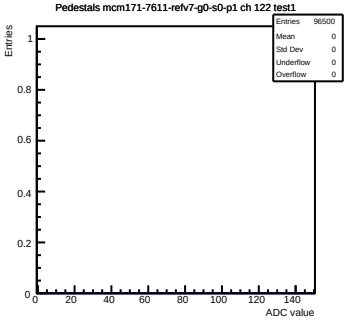
Entries 96500
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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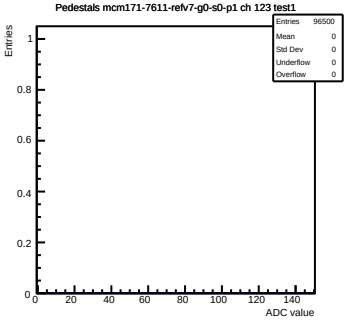
ADC value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Overflow 0
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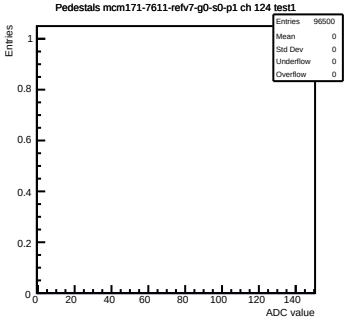
ADC value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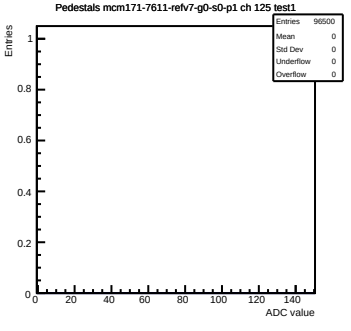
Entries 96500
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Entries 96500
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Std Dev 0
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Overflow 0
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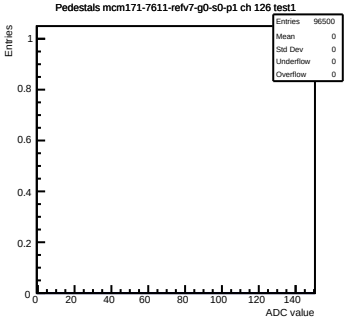
ADC value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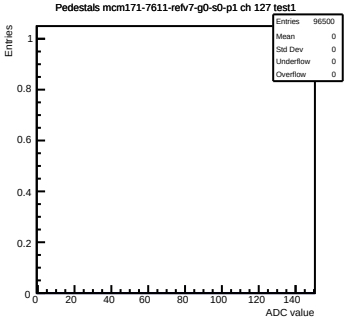
Entries 96500
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value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Entries 96500
Mean 0
Std Dev 0
Underflow 0
Overflow 0
Entries
ADC value