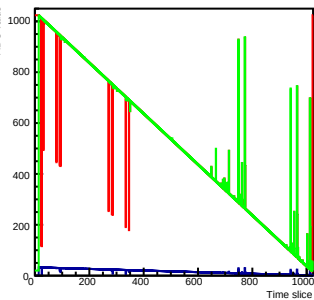
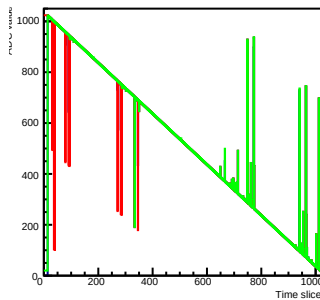


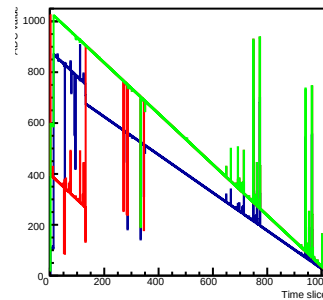
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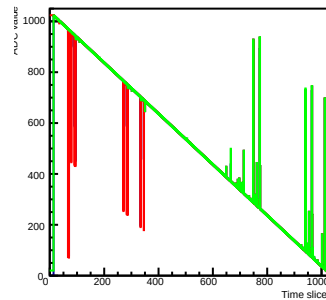
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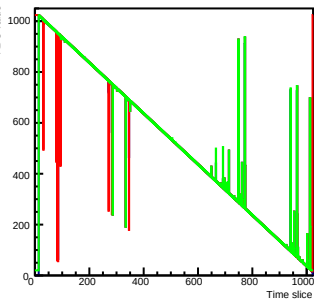
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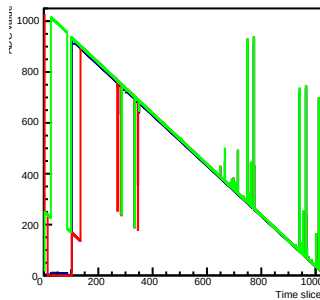
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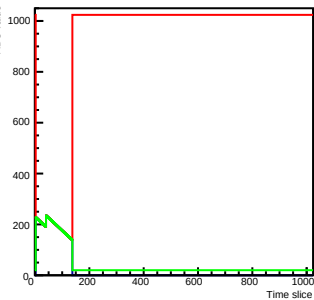
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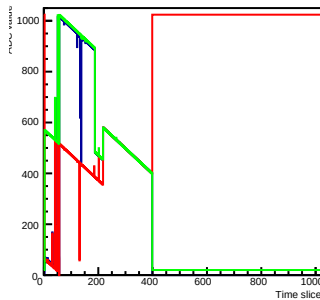
mcm 0 chip 0 chan 5 test1

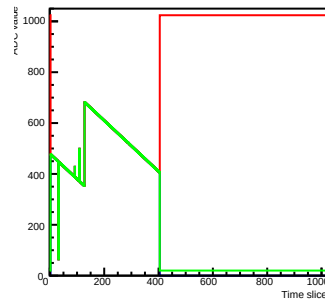


mcm 0 chip 0 chan 8 test1

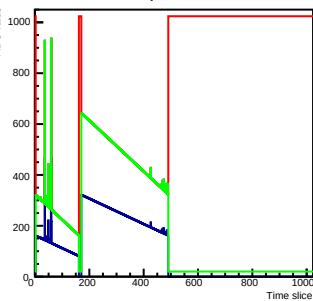


mcm 0 chip 0 chan 13 test1

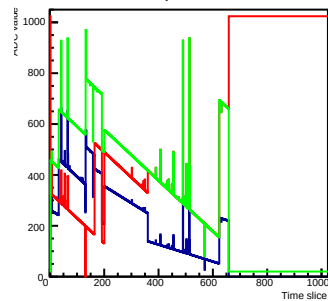




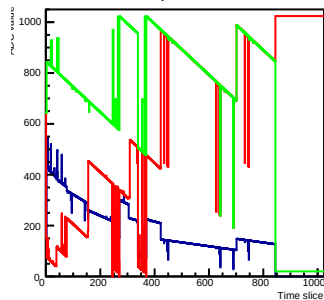
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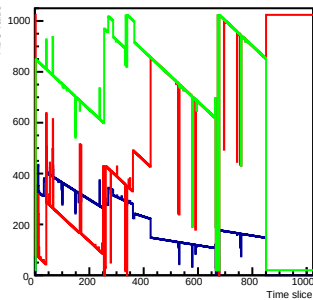
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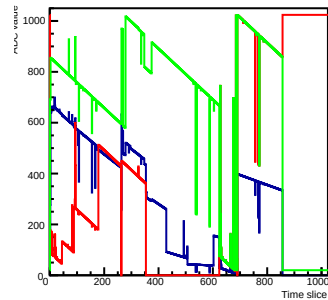
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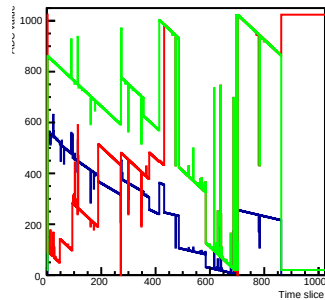
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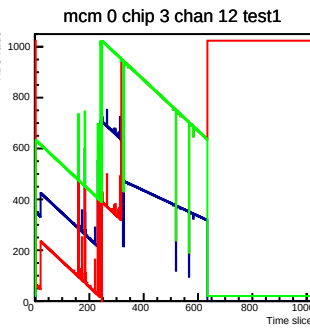
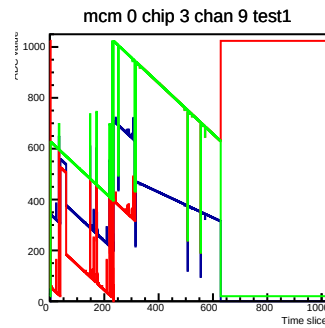
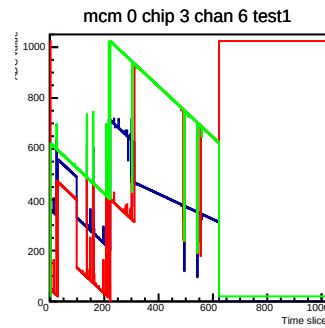
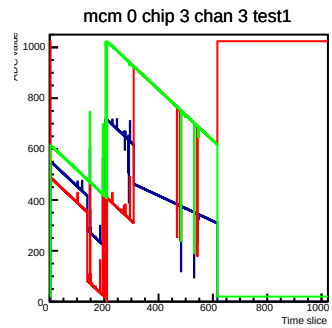


mcm 0 chip 2 chan 11 test1

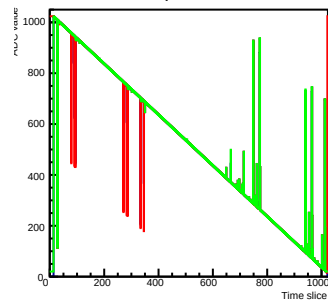


mcm 0 chip 2 chan 14 test1

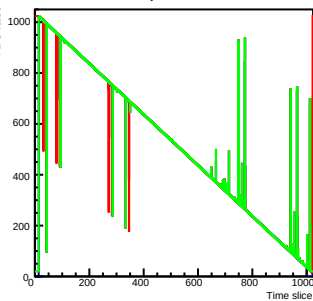




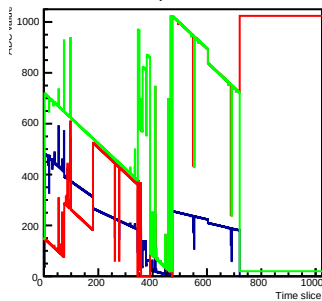
mcm 0 chip 4 chan 3 test1



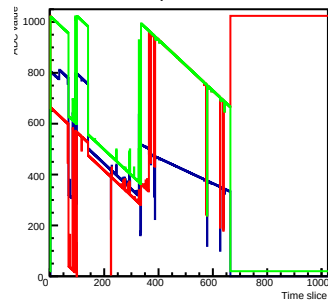
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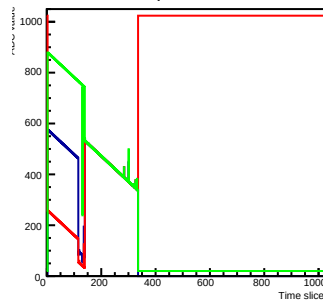
mcm 0 chip 4 chan 9 test1

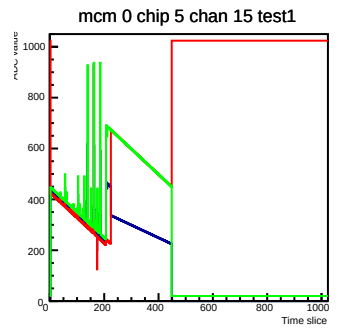
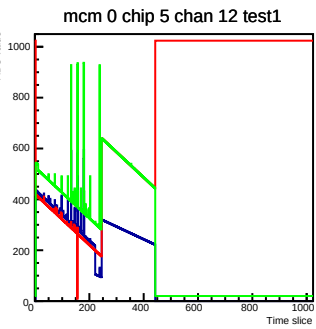
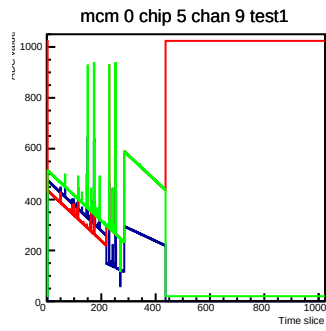
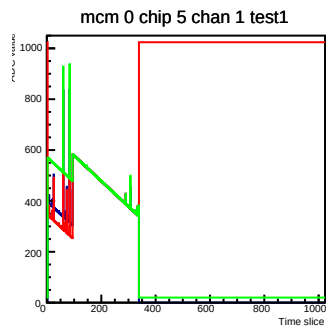


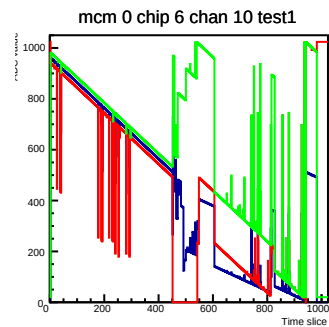
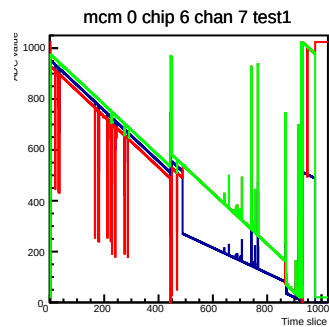
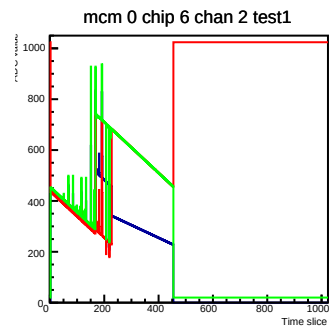
mcm 0 chip 4 chan 11 test1

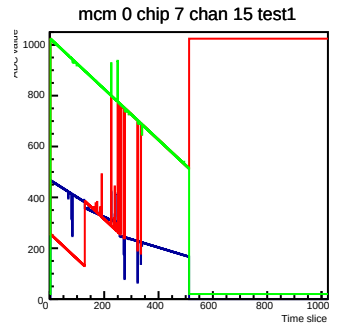
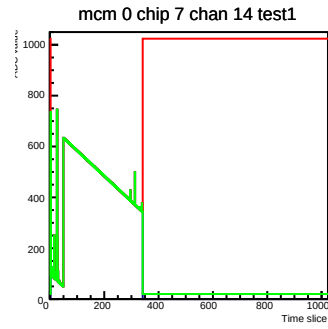
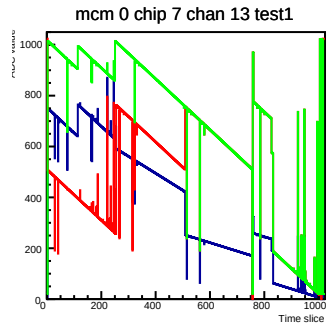
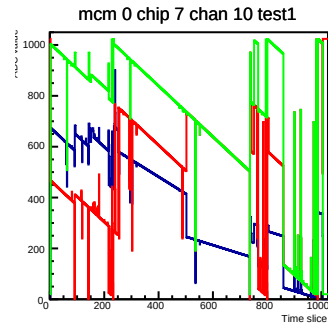
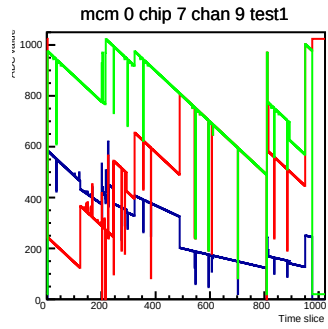
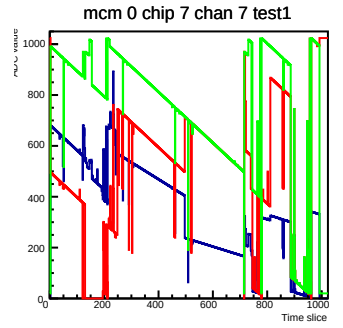
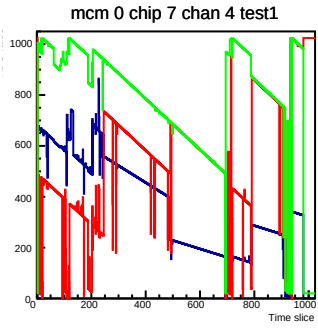
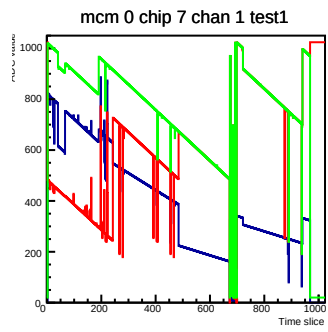


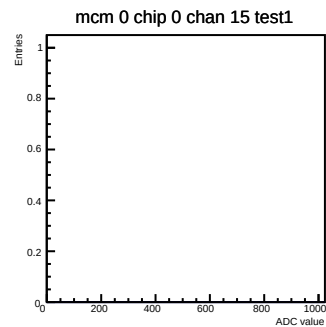
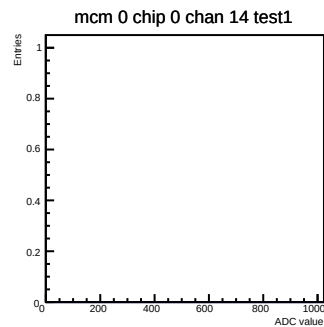
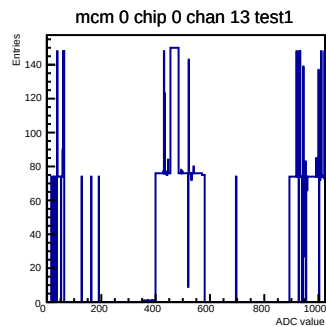
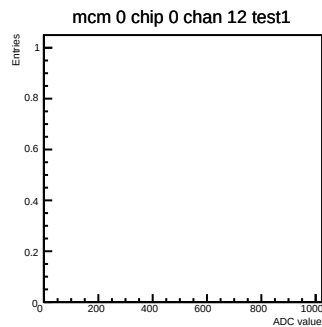
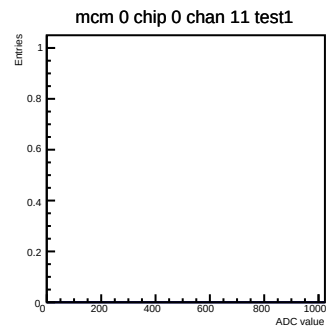
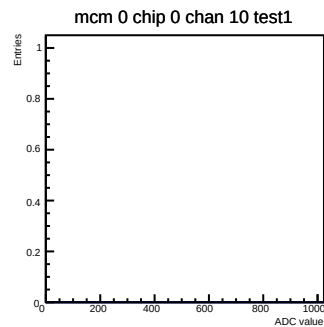
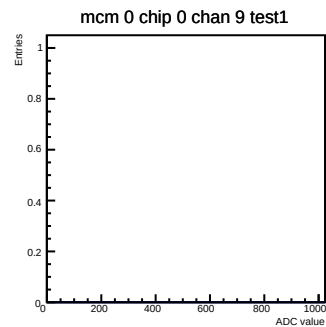
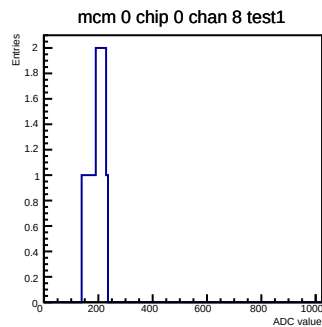
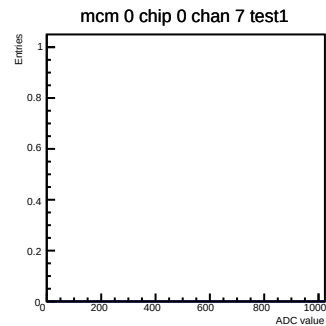
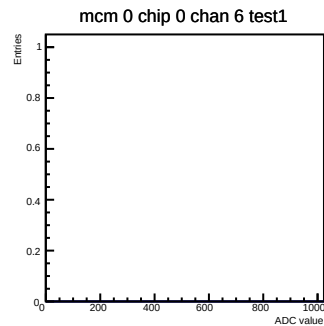
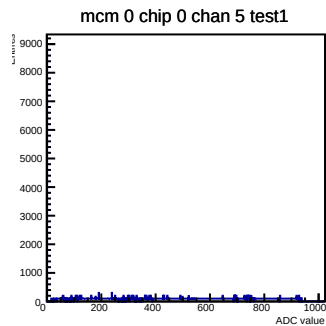
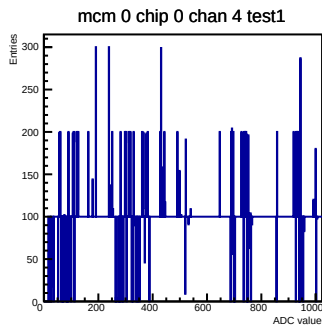
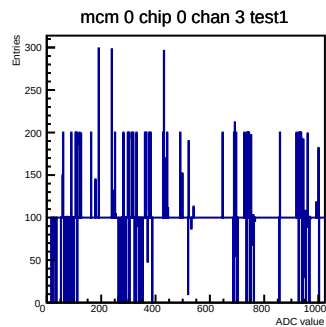
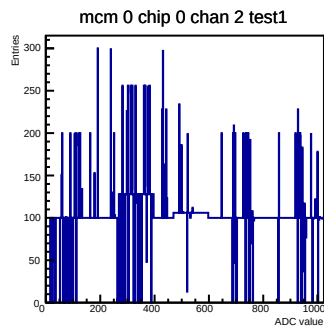
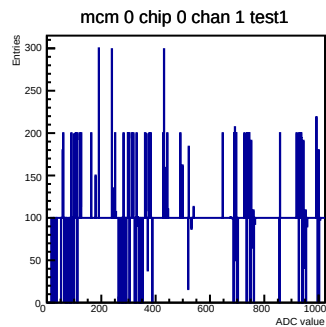
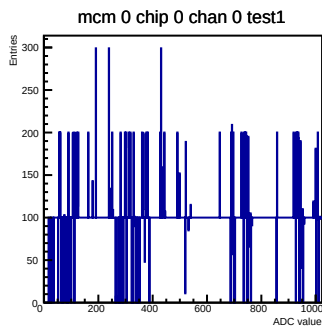
mcm 0 chip 4 chan 14 test1



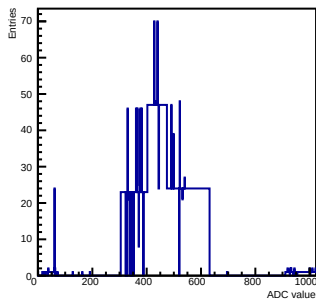




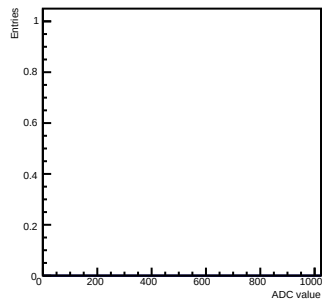




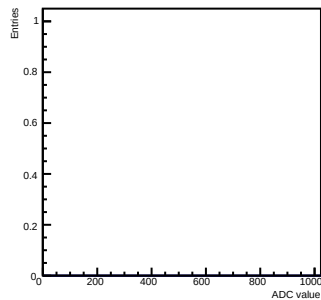
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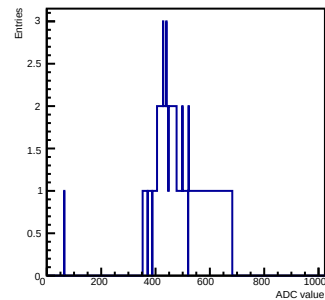
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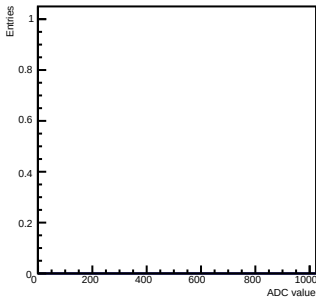
mcm 0 chip 1 chan 2 test1



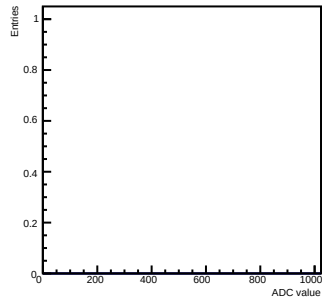
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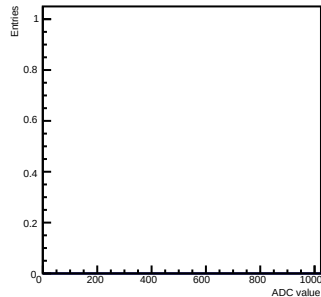
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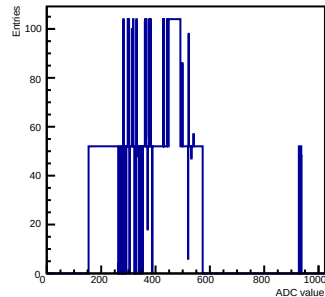
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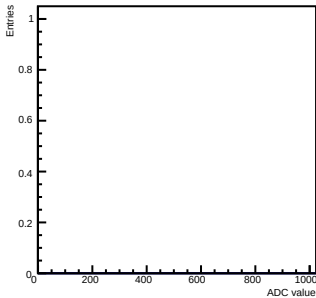
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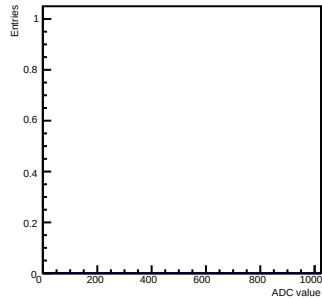
mcm 0 chip 1 chan 7 test1



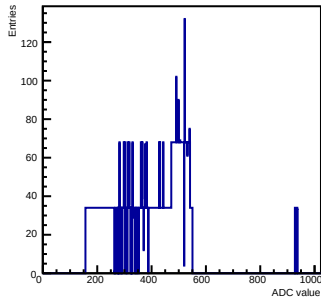
mcm 0 chip 1 chan 8 test1



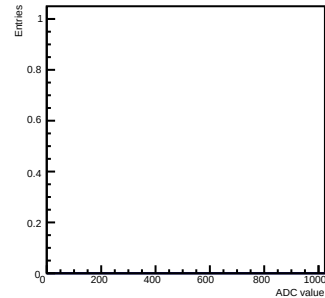
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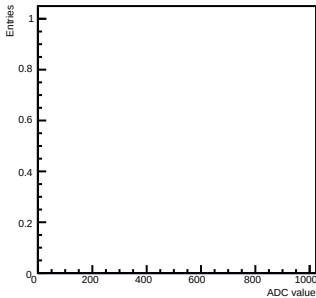
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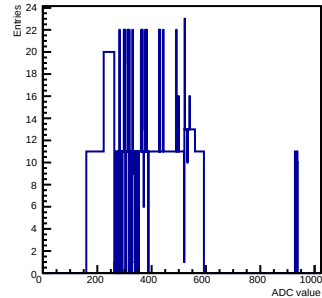
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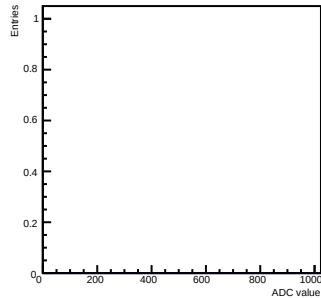
mcm 0 chip 1 chan 12 test1



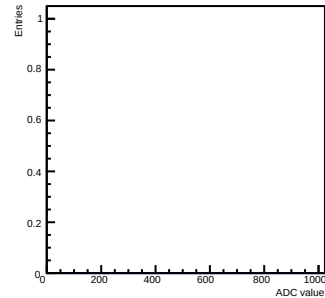
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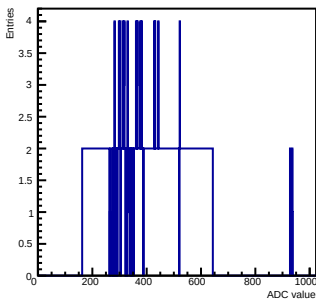
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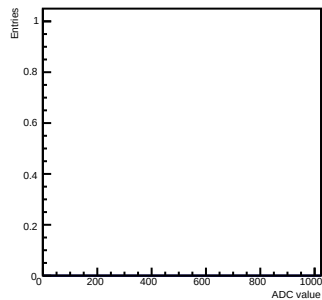
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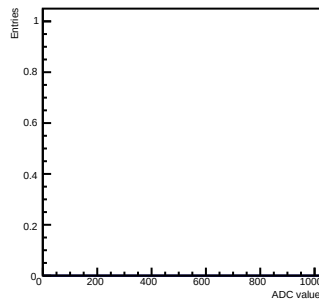
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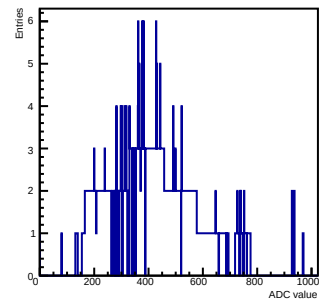
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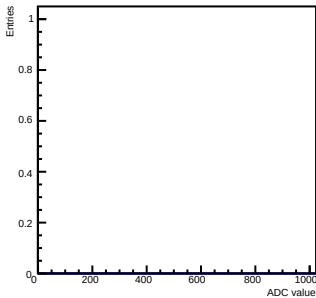
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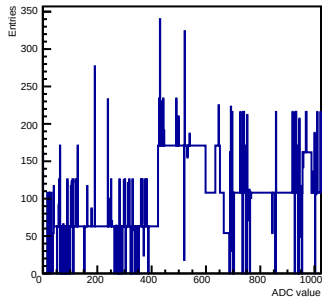
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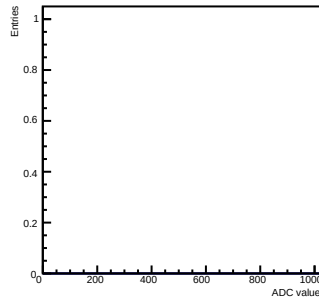
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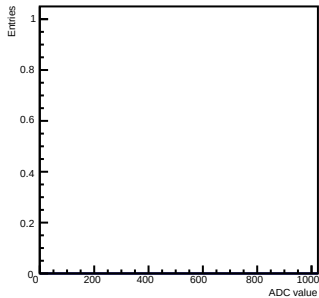
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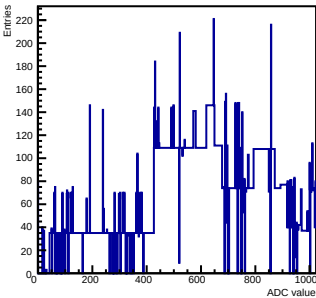
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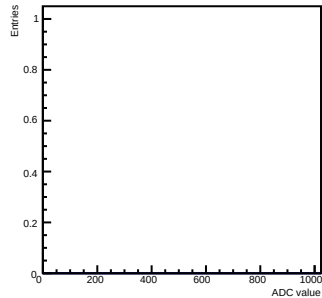
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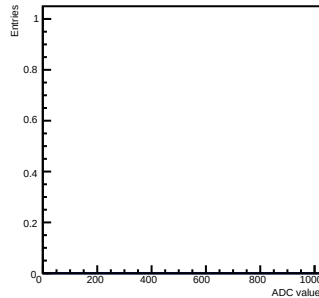
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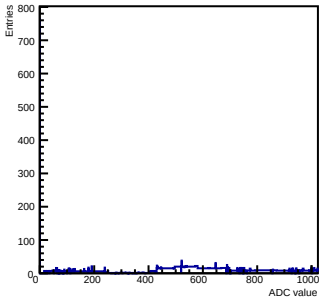
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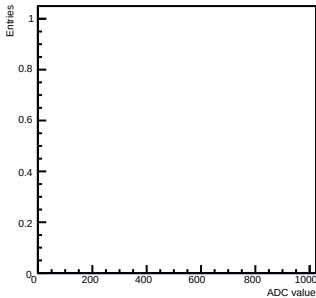
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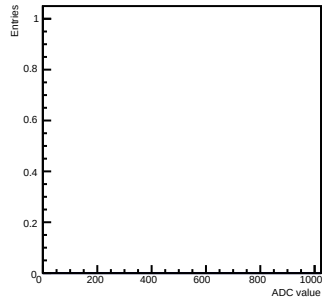
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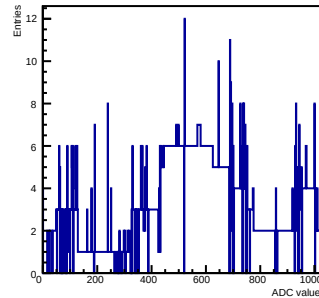
mcm 0 chip 2 chan 12 test1



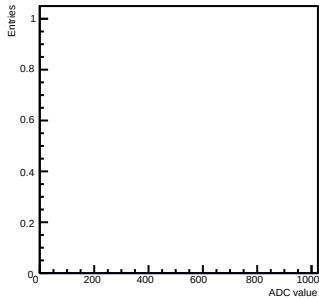
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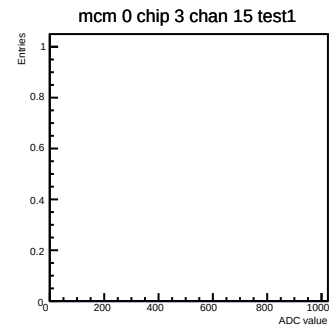
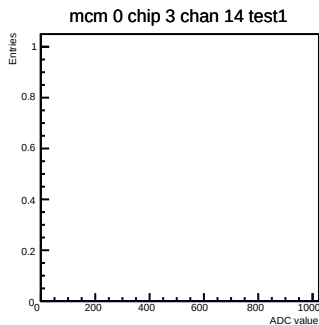
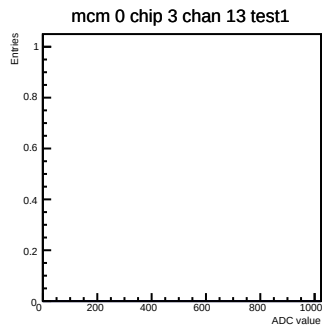
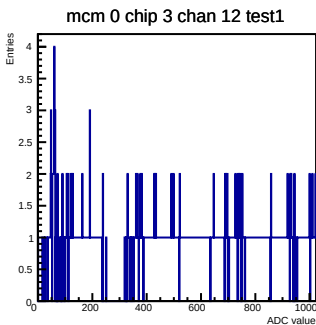
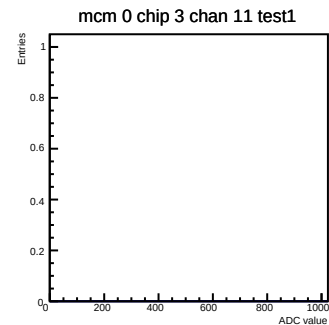
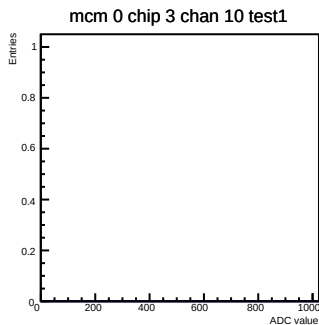
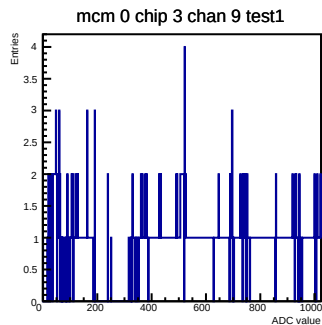
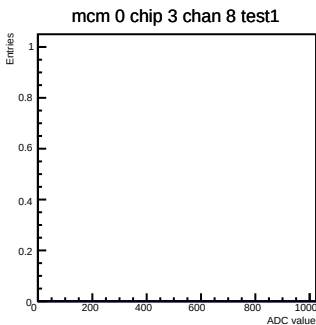
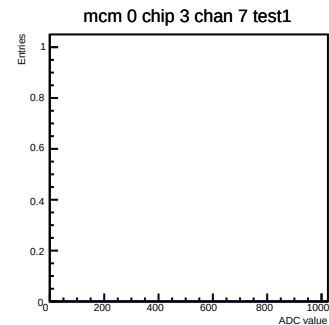
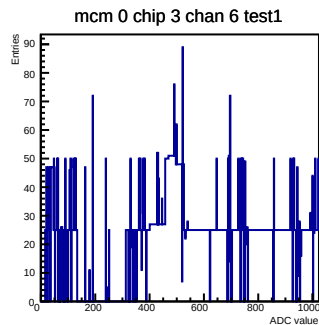
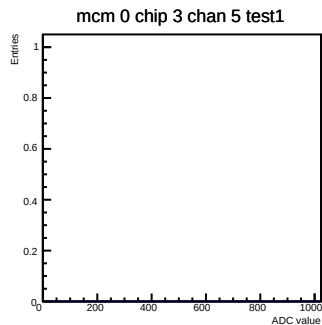
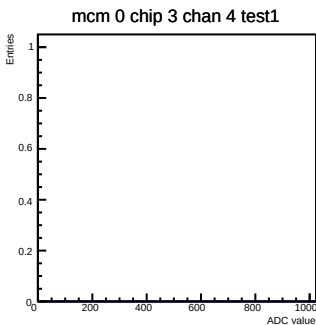
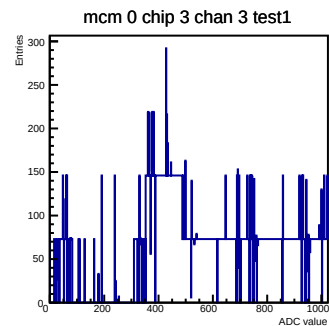
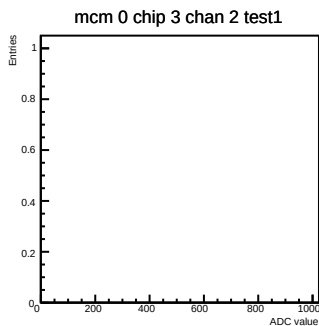
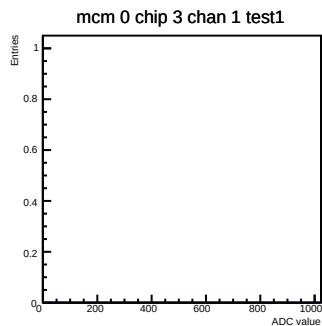
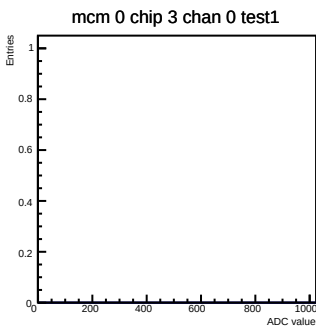


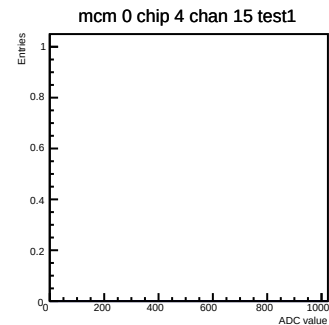
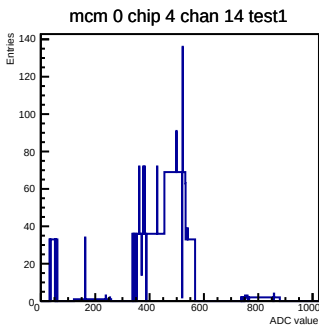
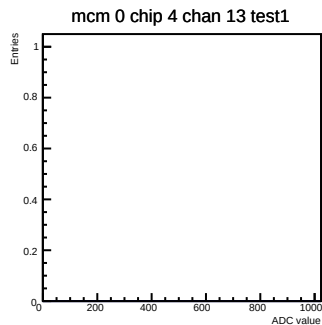
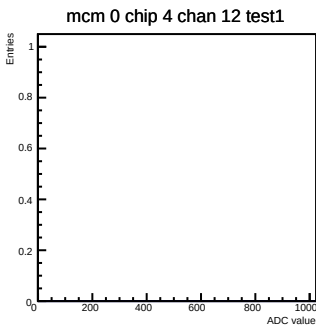
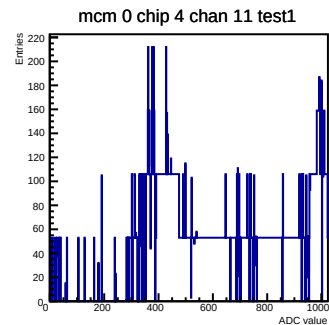
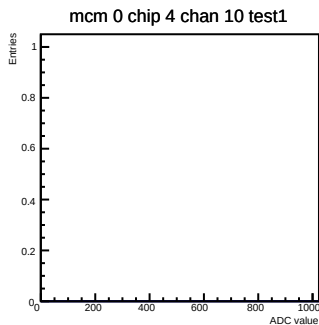
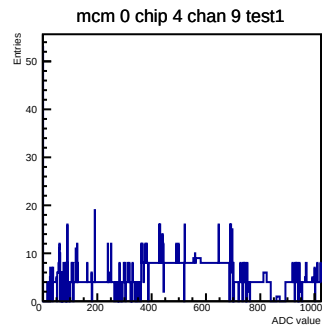
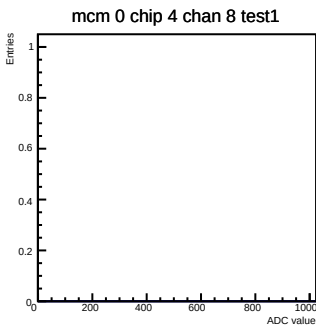
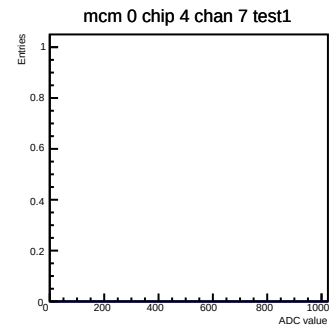
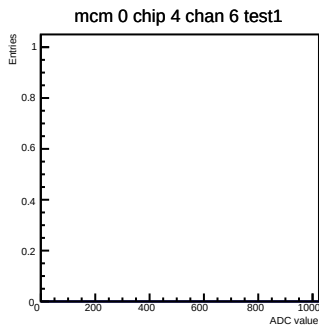
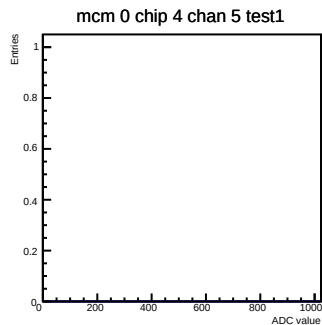
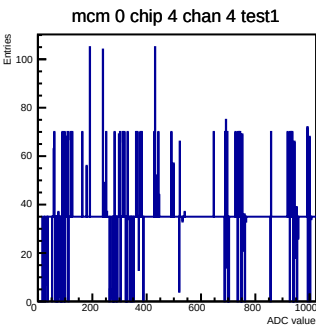
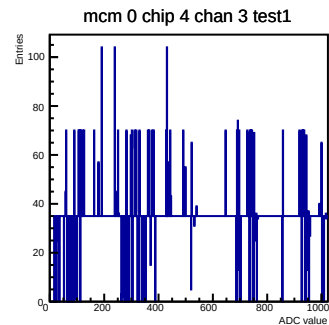
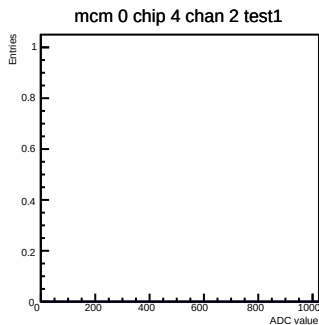
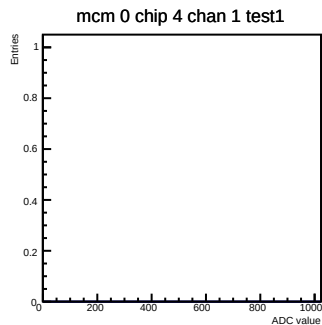
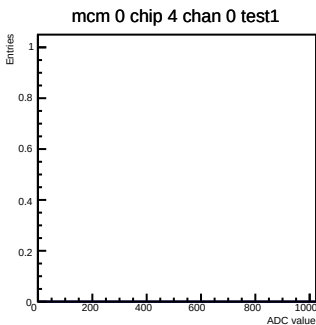
mcm 0 chip 2 chan 14 test1



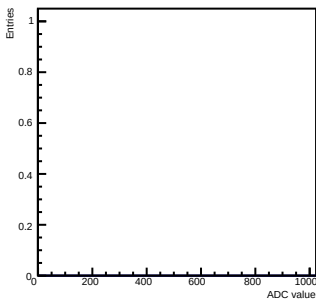
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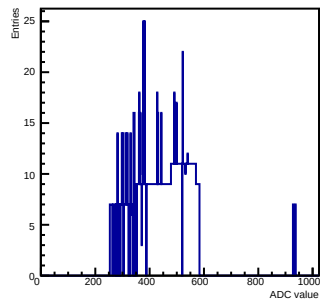




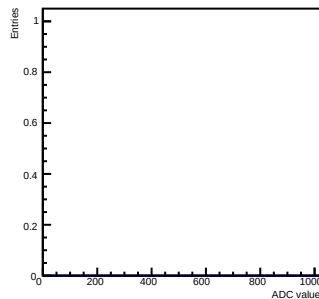
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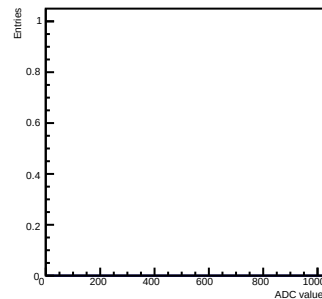
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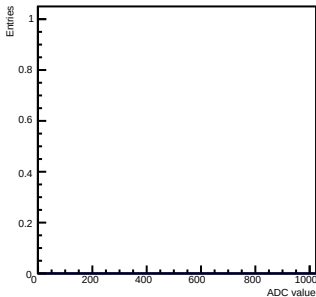
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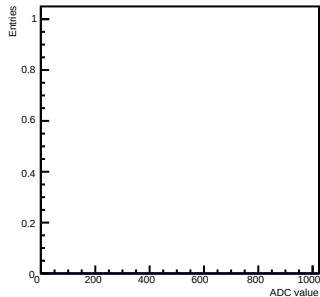
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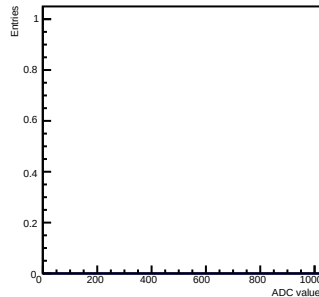
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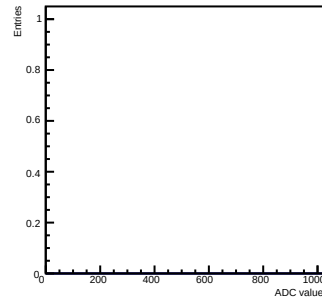
mcm 0 chip 5 chan 5 test1



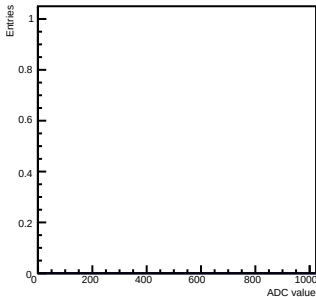
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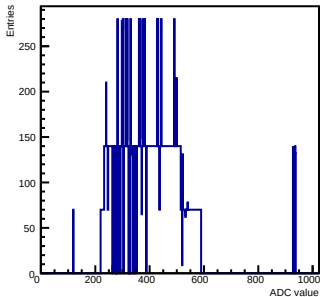
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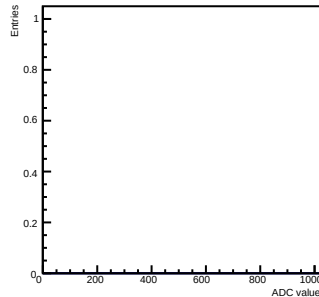
mcm 0 chip 5 chan 8 test1



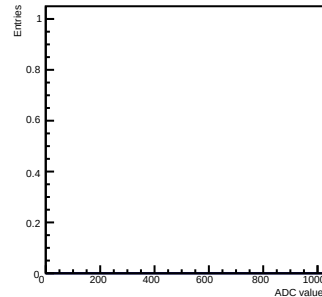
mcm 0 chip 5 chan 9 test1



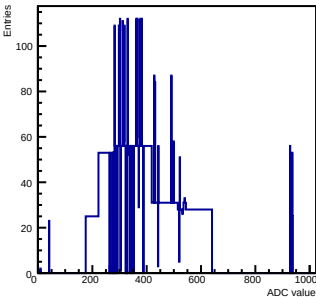
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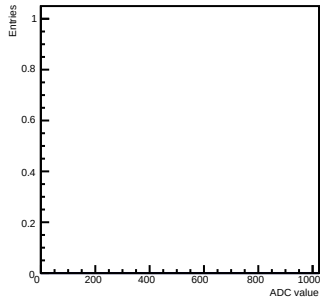
mcm 0 chip 5 chan 11 test1



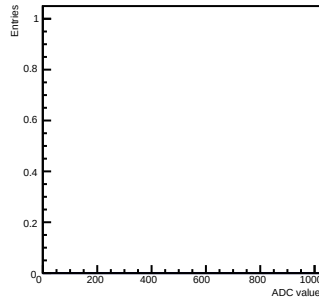
mcm 0 chip 5 chan 12 test1



mcm 0 chip 5 chan 13 test1



mcm 0 chip 5 chan 14 test1



mcm 0 chip 5 chan 15 test1

