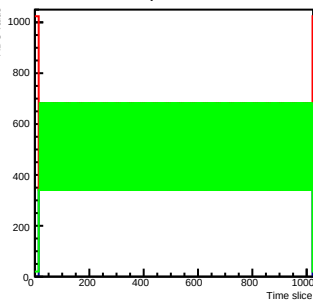
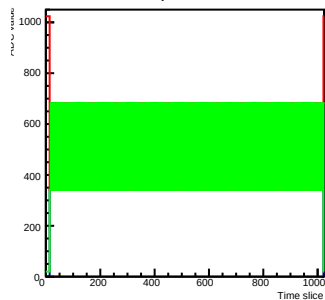


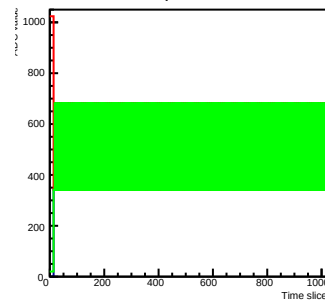
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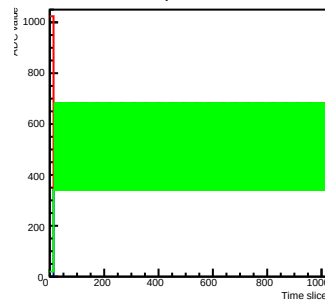
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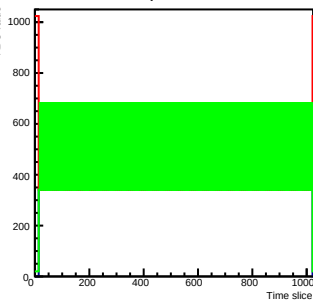
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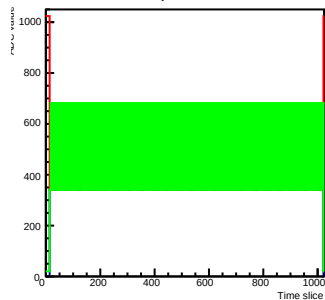
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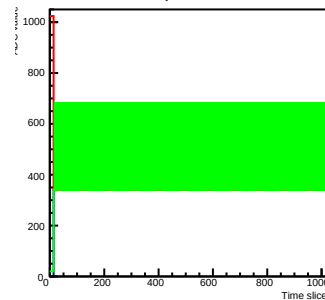
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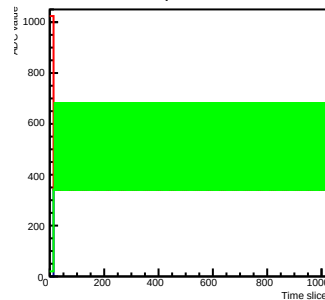
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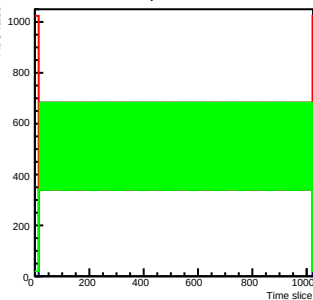
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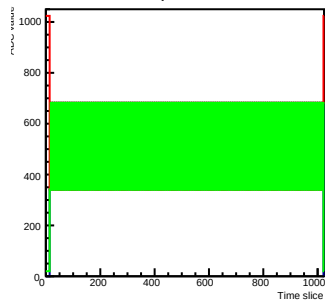
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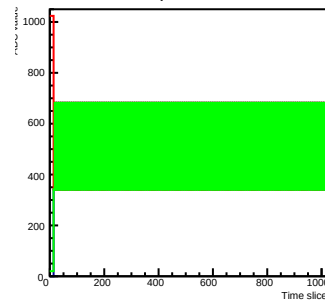
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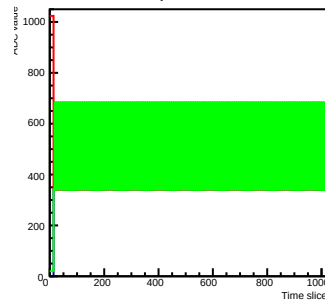
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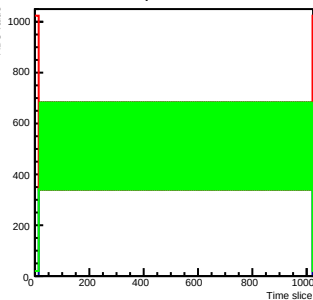
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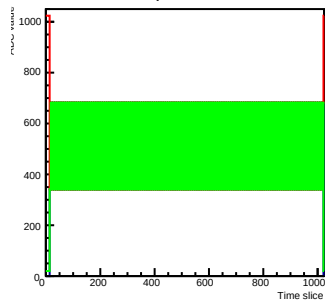
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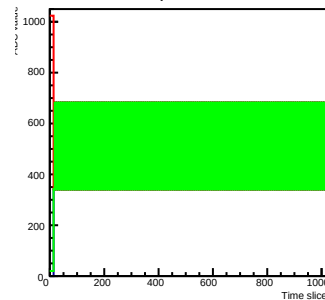
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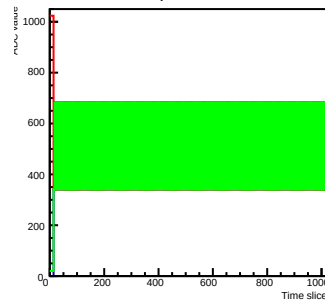
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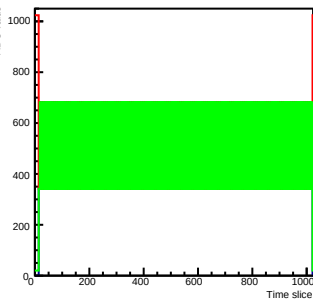
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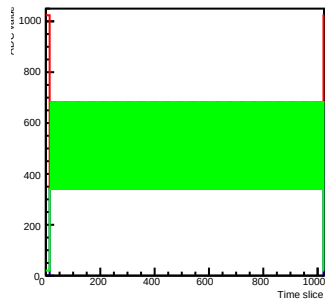
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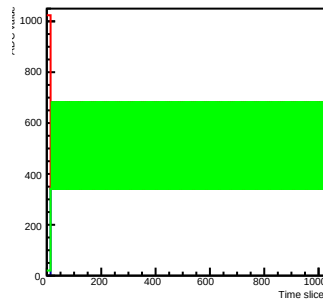
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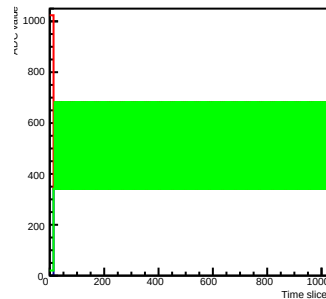
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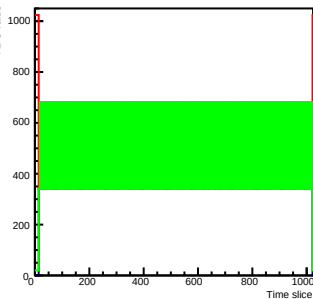
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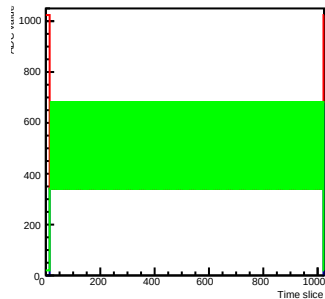
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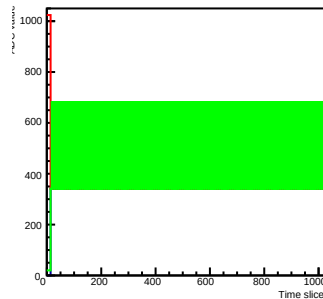
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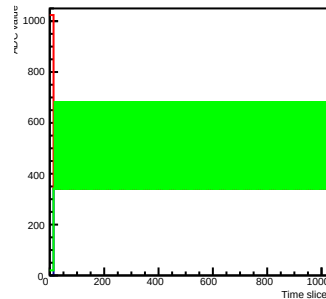
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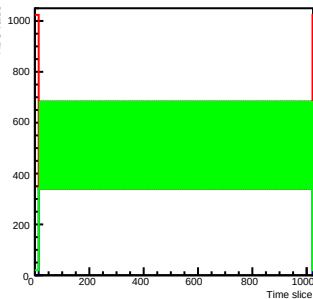
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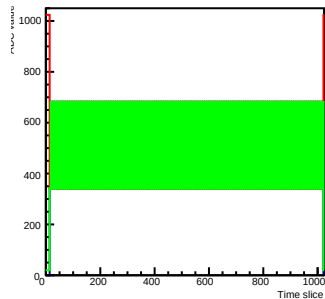
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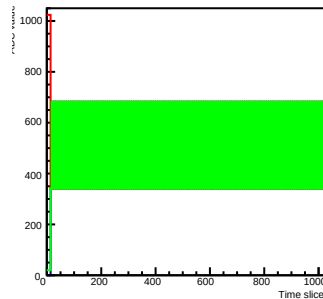
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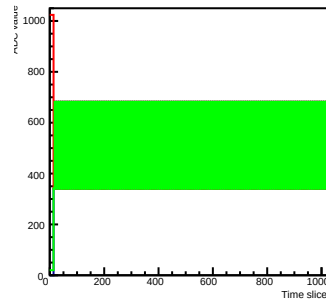
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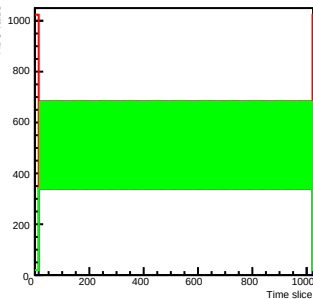
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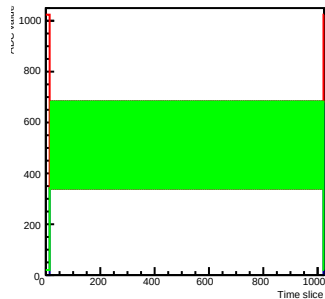
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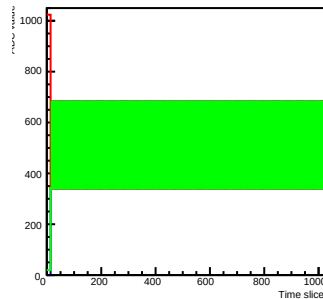
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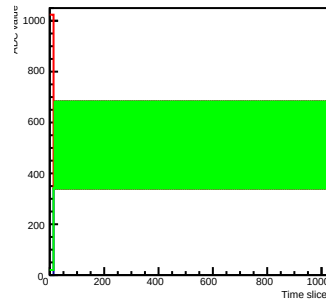
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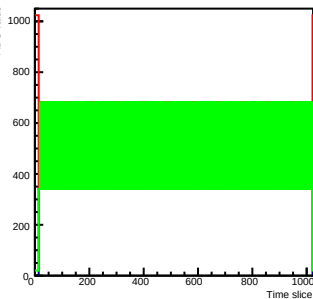
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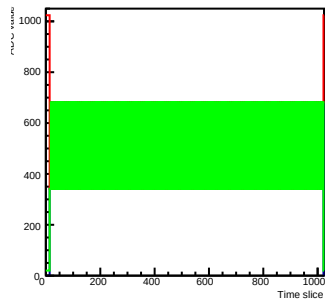
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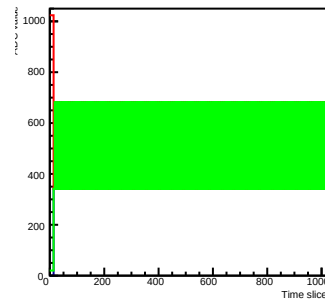
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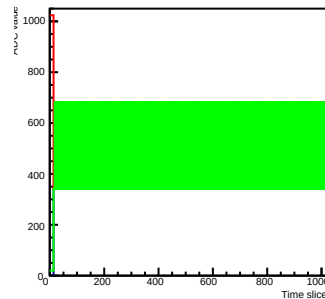
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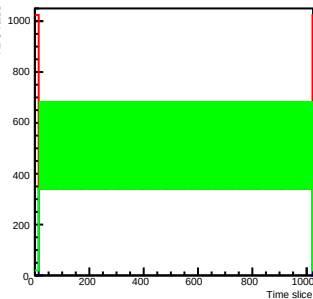
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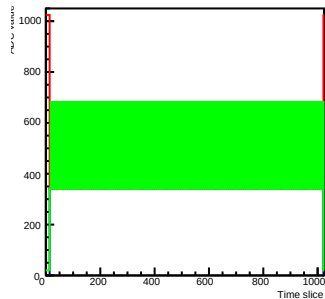
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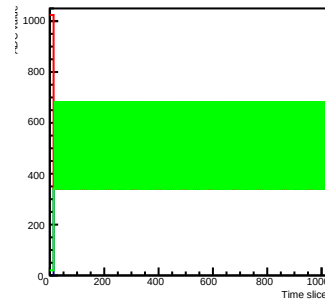
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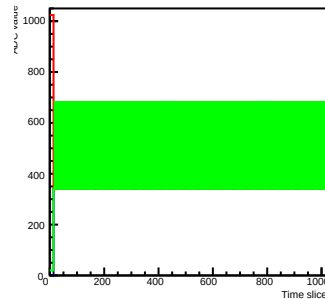
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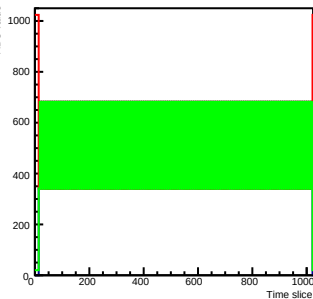
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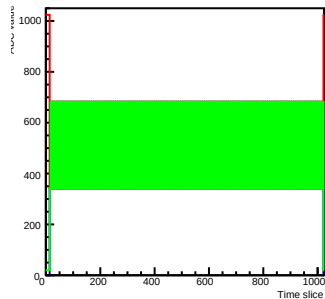
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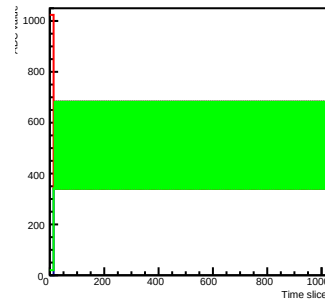
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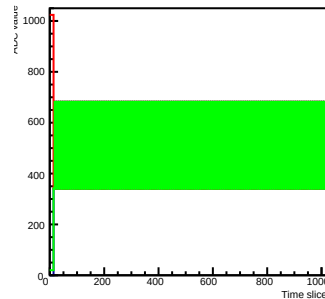
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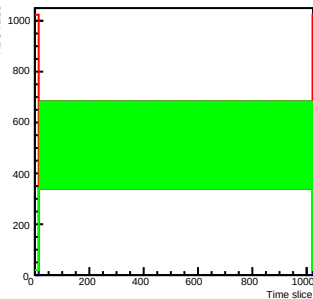
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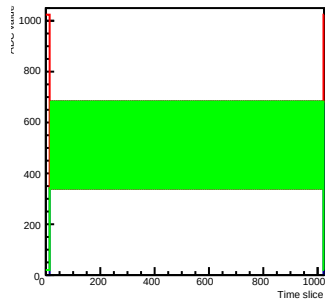
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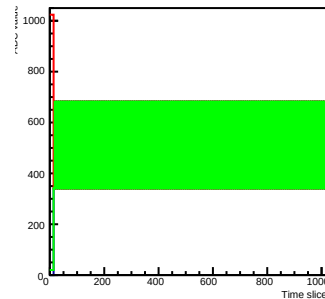
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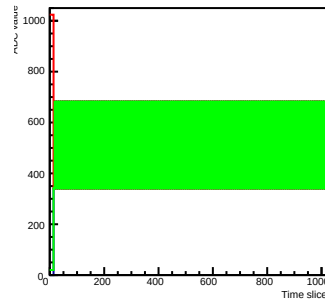
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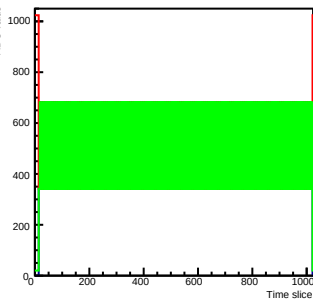
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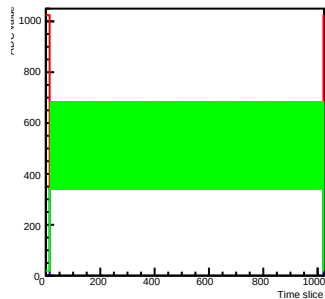
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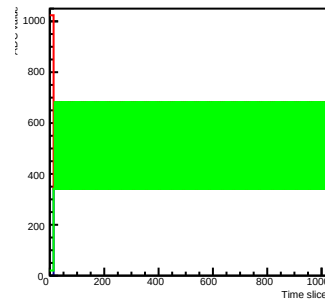
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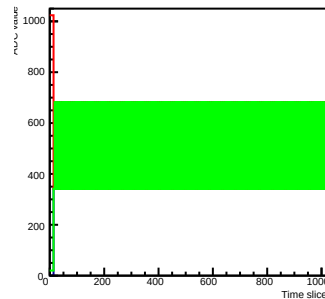
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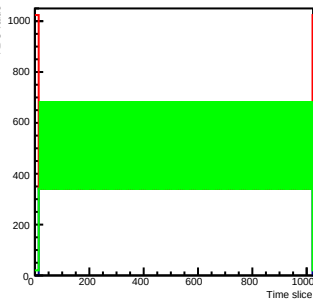
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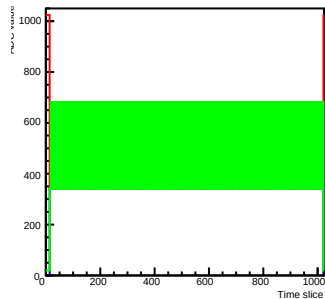
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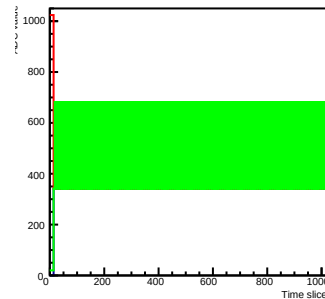
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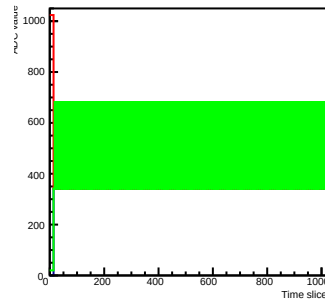
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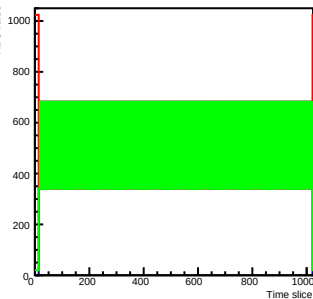
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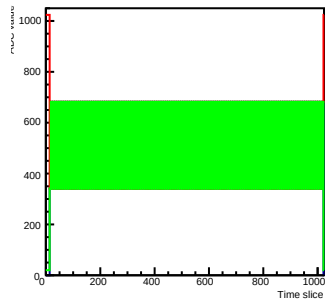
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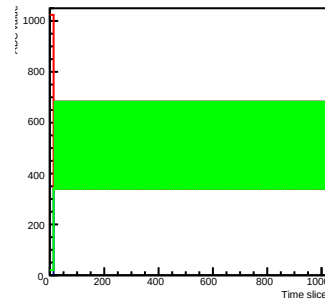
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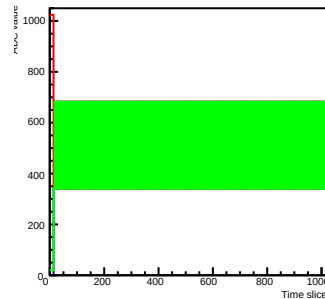
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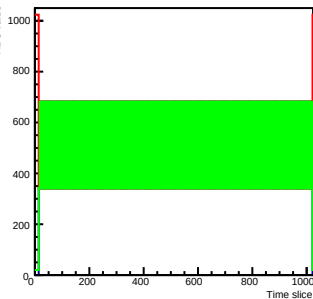
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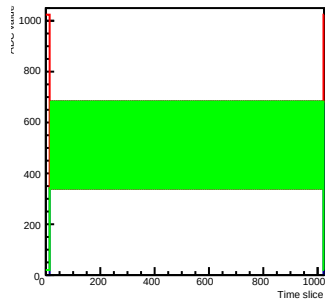
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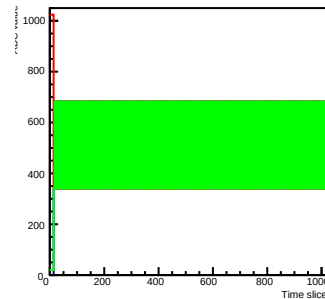
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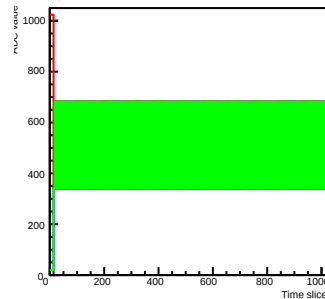
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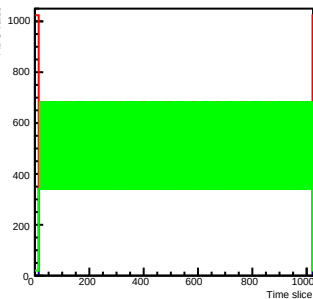
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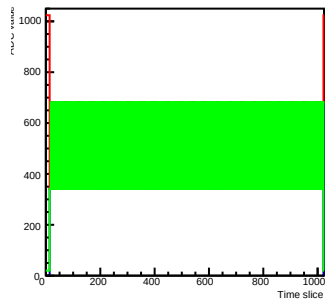
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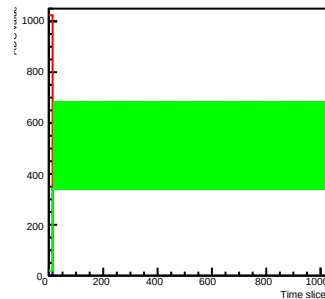
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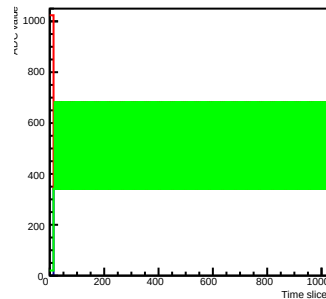
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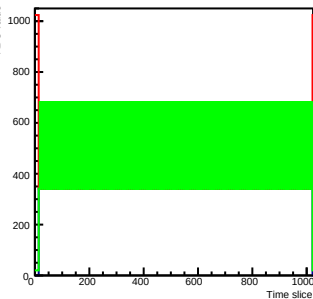
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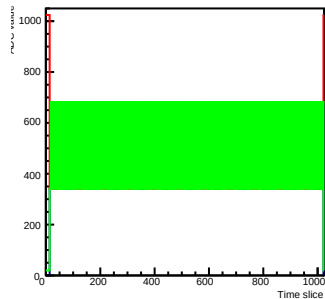
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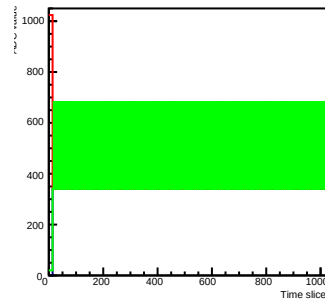
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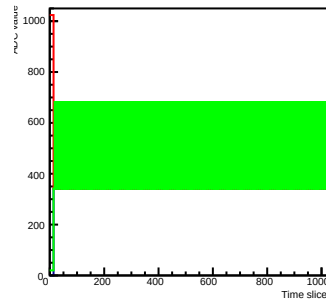
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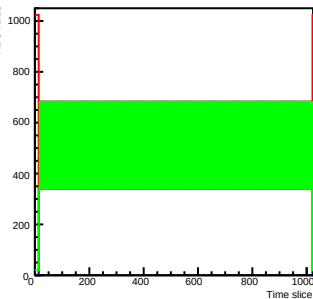
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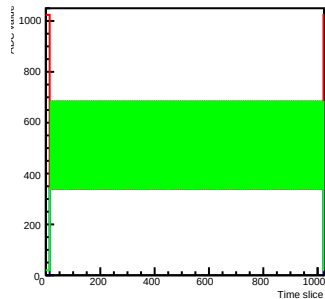
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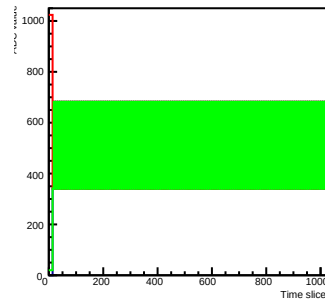
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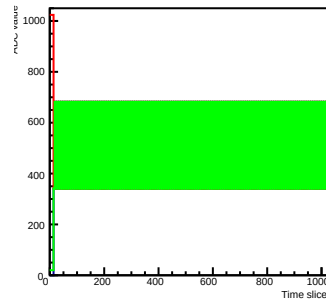
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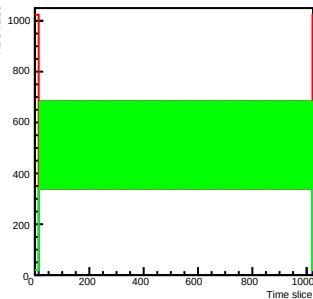
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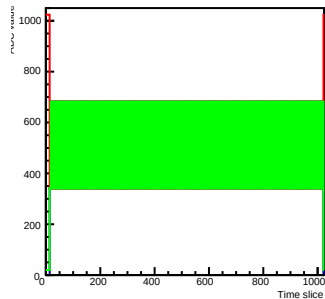
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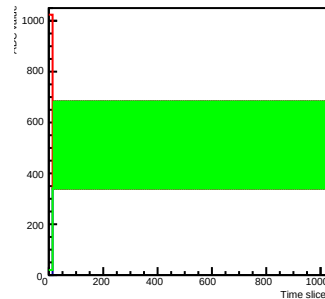
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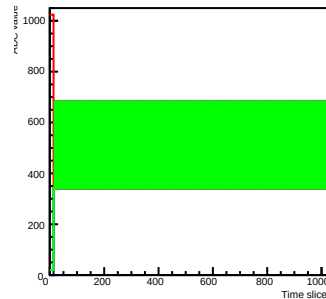
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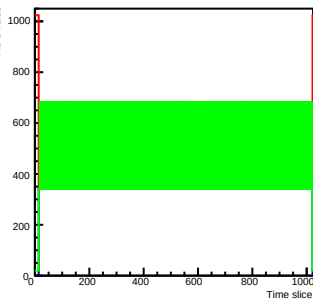
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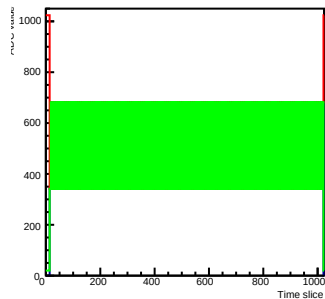
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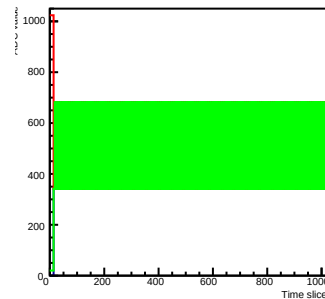
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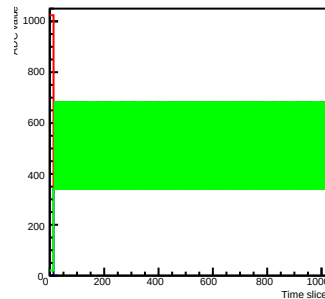
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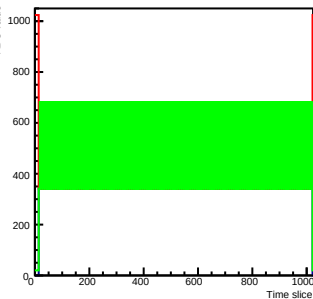
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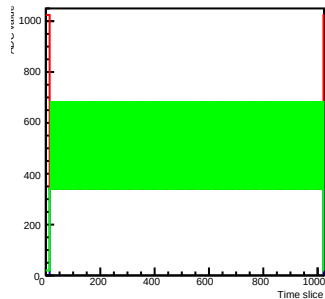
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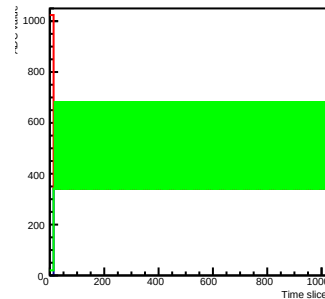
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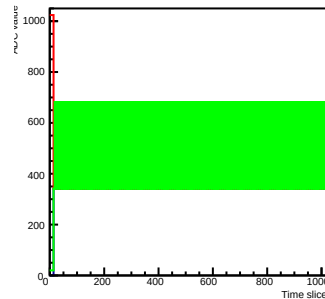
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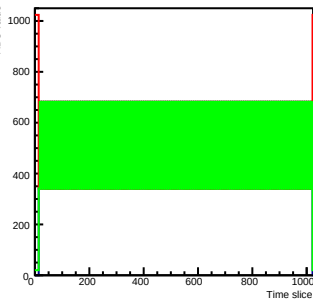
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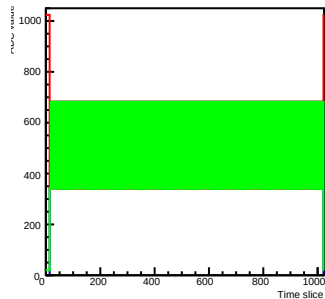
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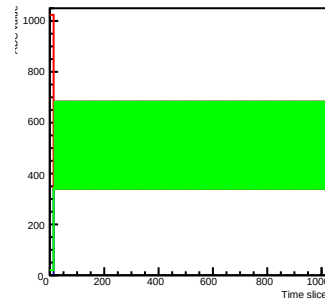
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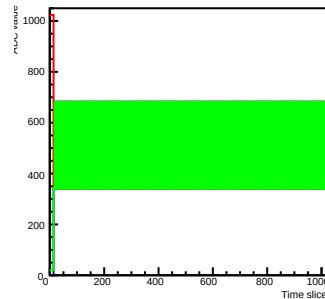
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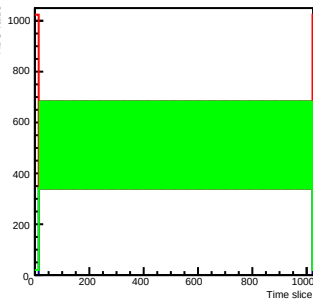
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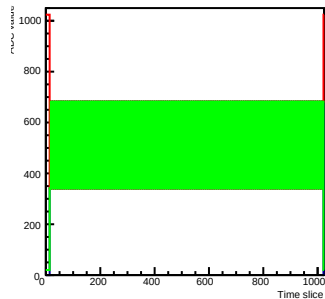
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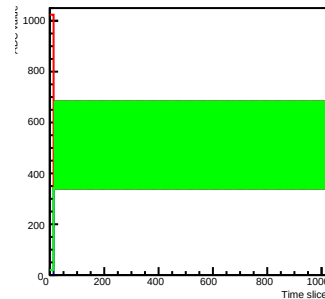
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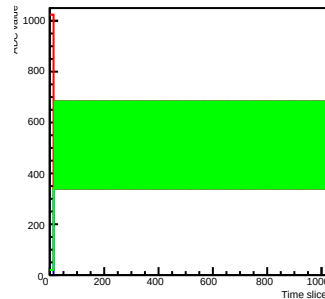
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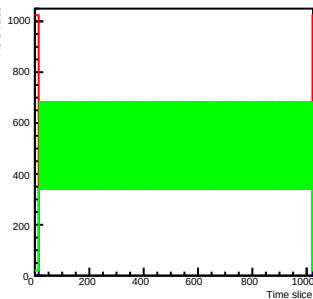
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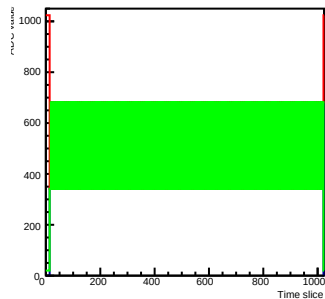
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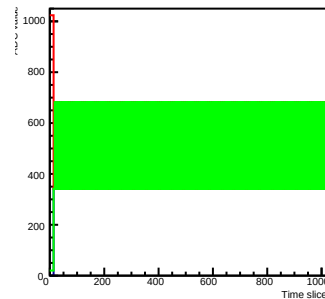
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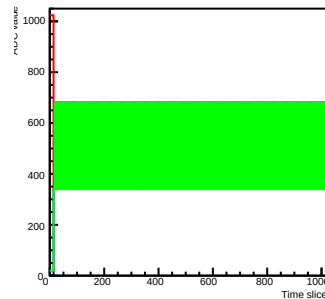
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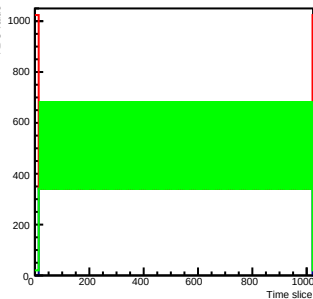
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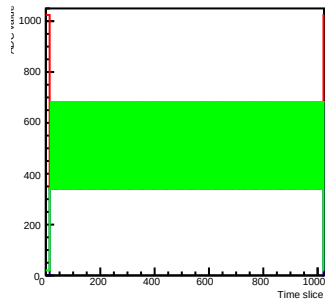
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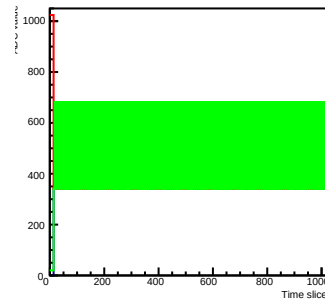
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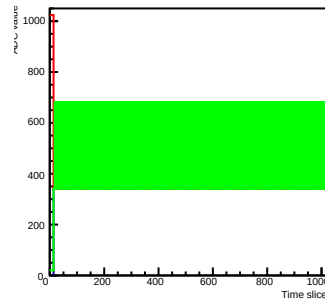
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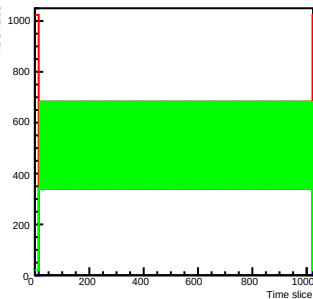
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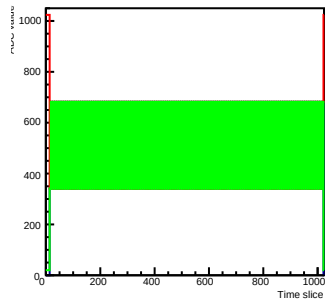
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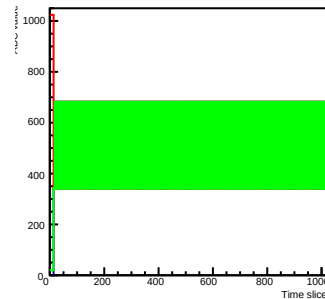
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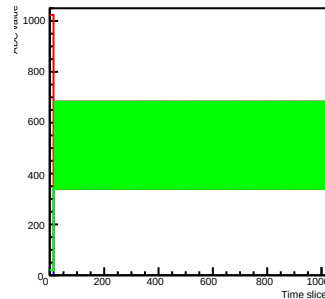
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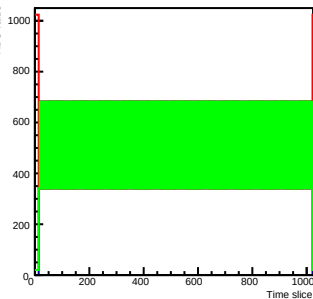
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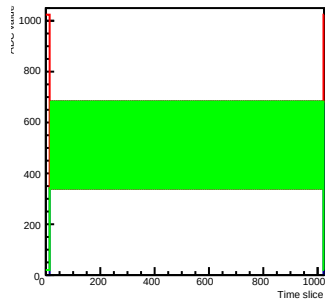
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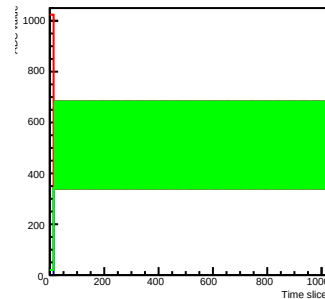
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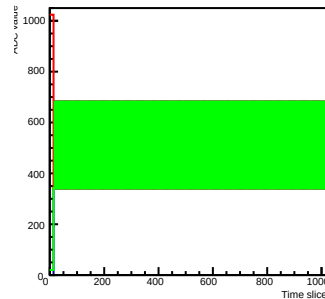
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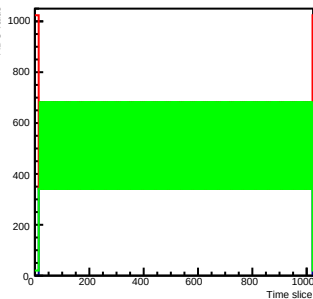
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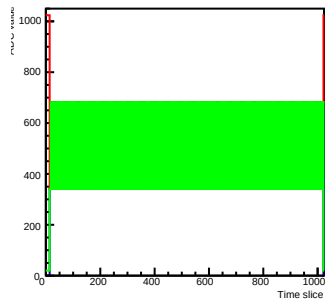
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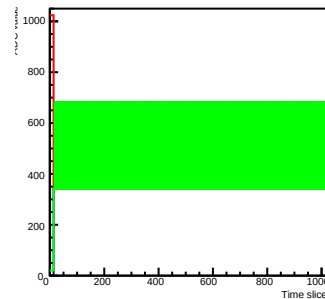
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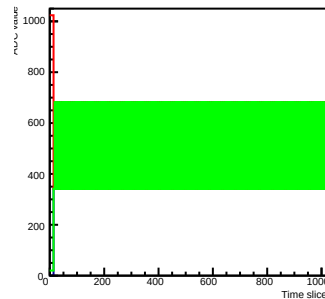
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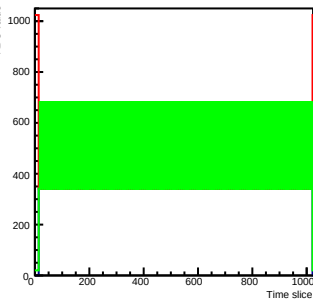
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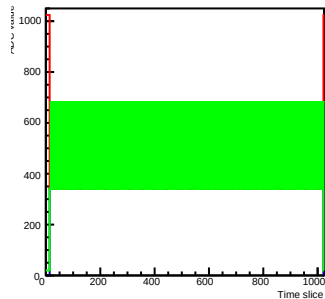
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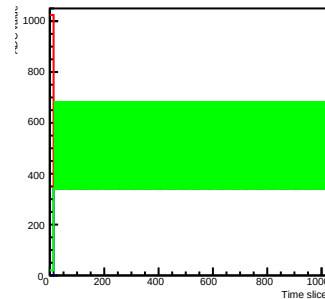
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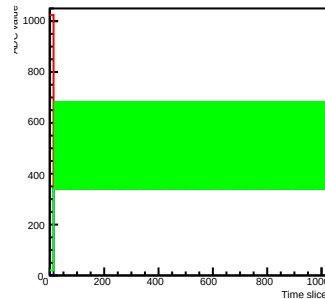
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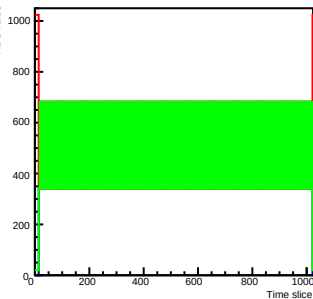
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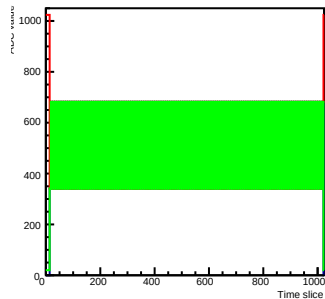
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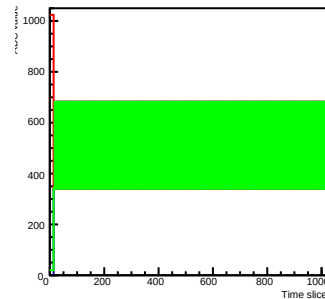
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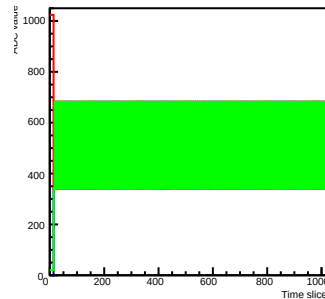
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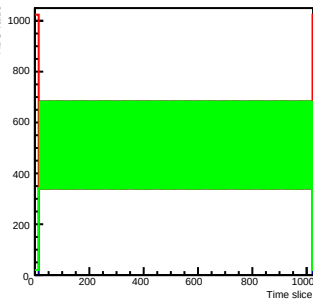
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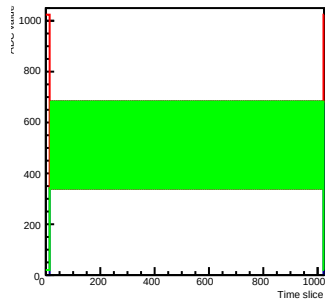
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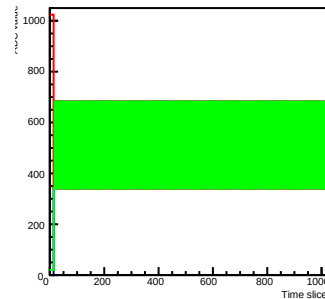
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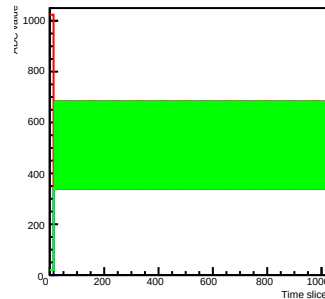
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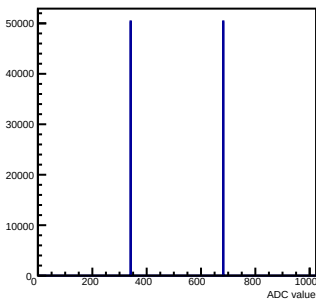
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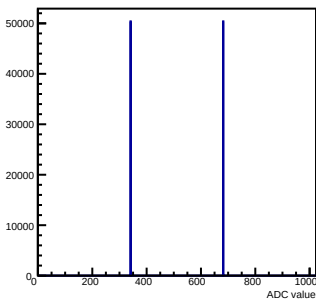
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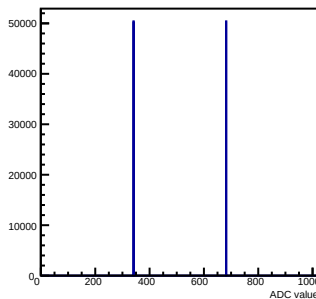
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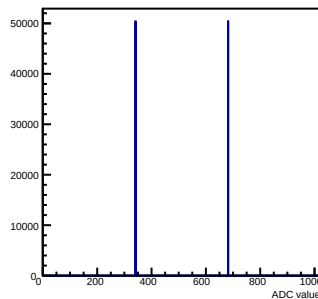
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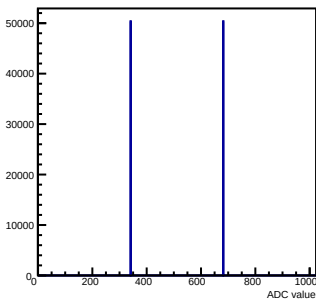
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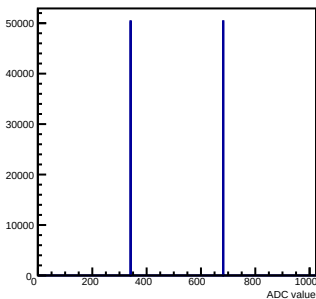
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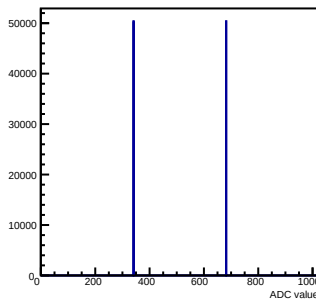
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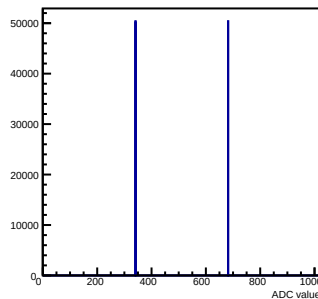
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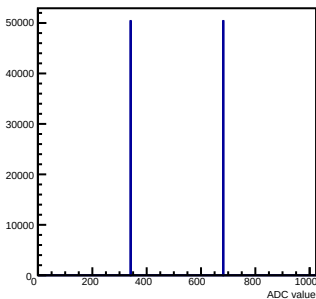
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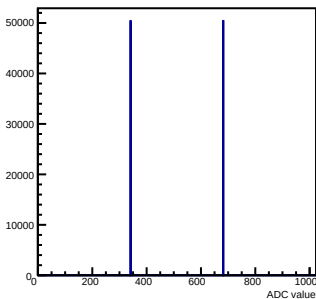
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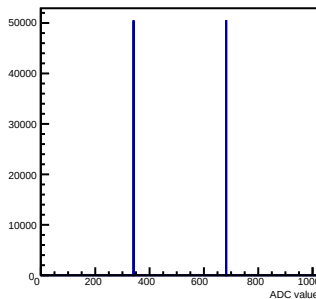
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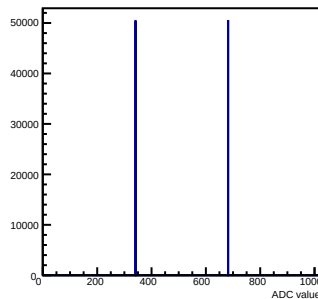
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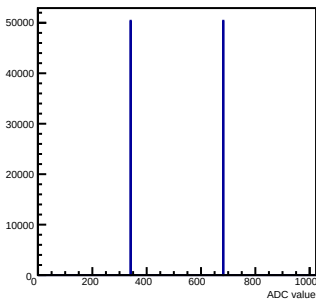
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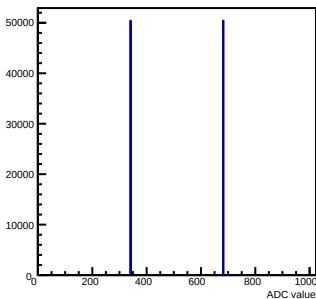
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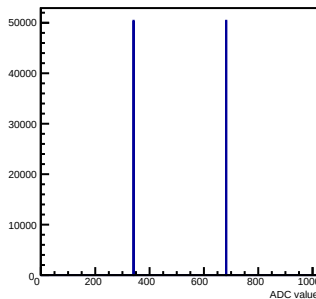
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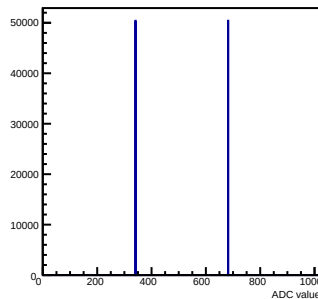
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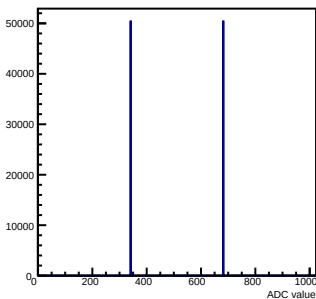
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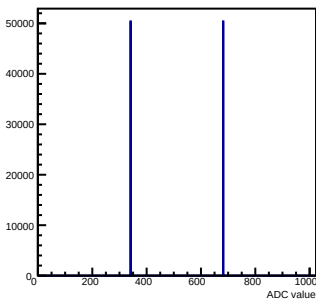
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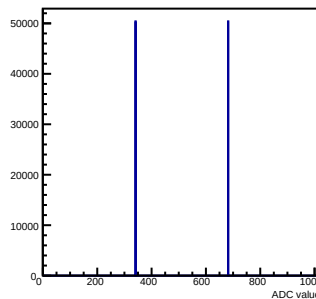
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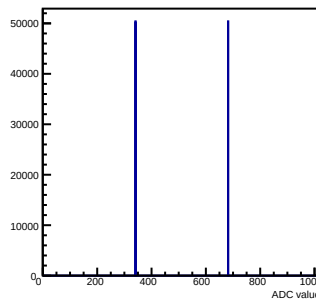
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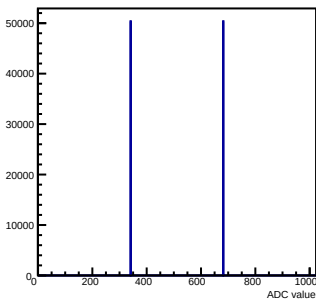
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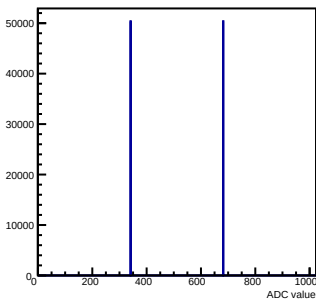
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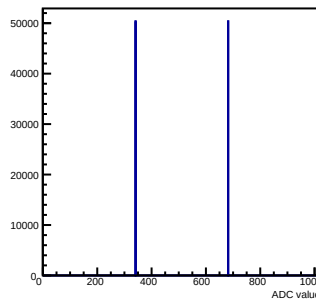
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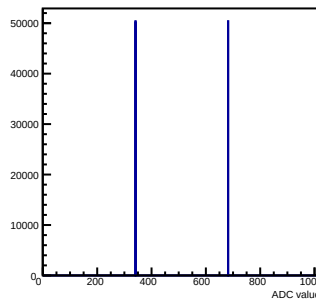
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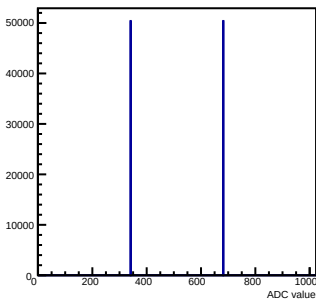
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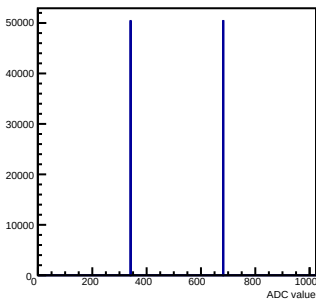
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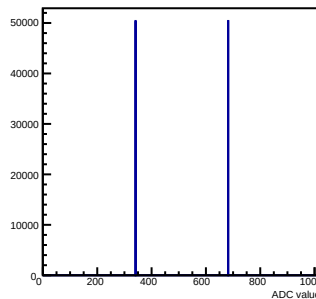
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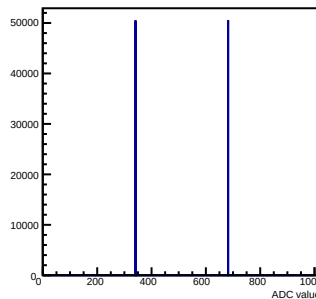
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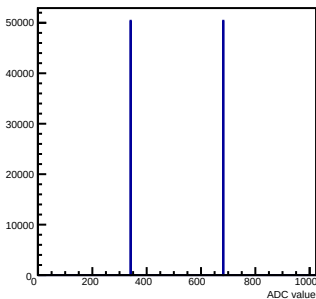
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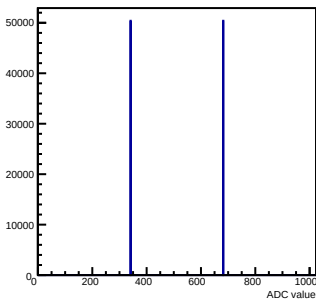
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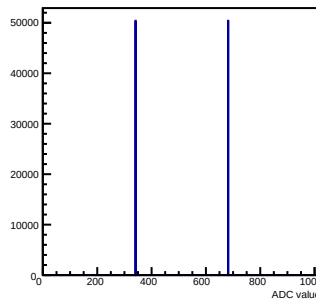
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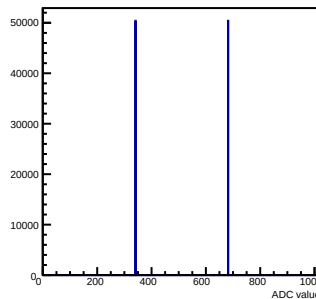
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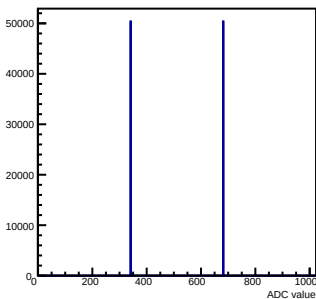
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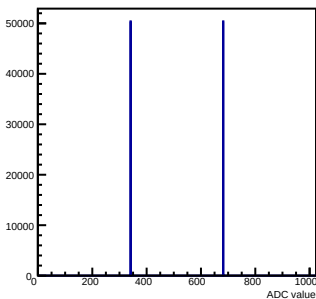
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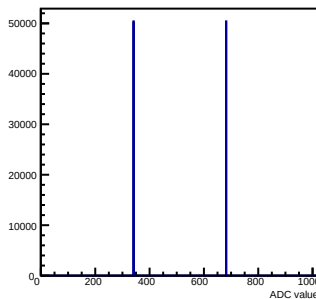
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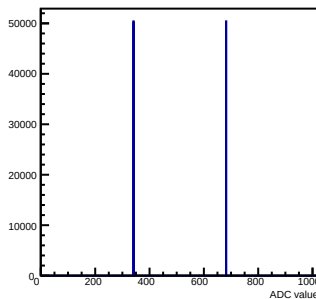
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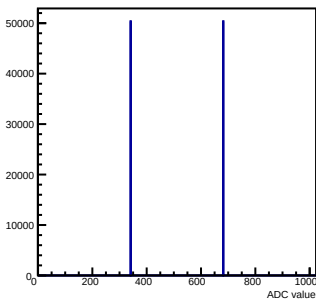
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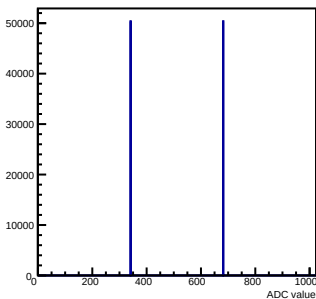
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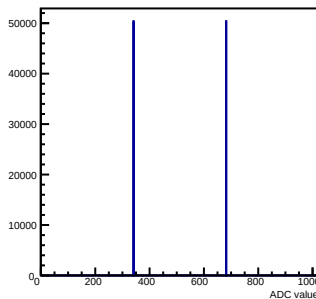
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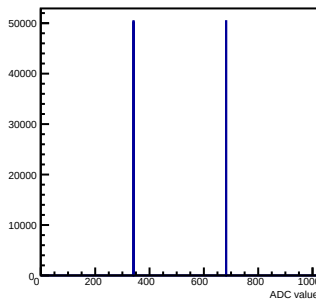
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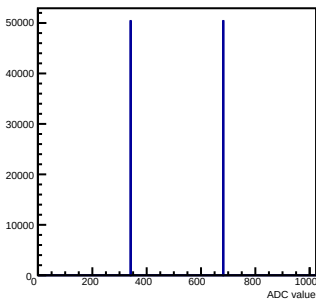
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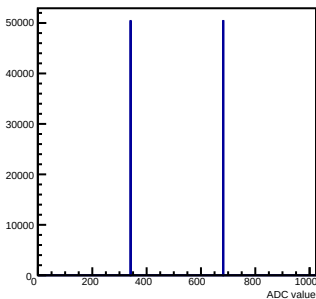
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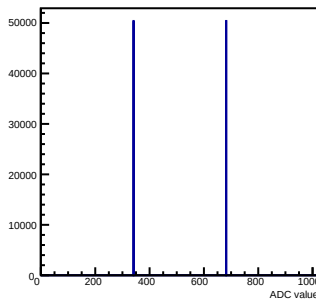
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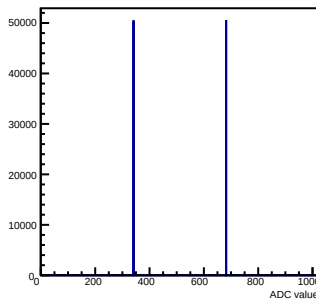
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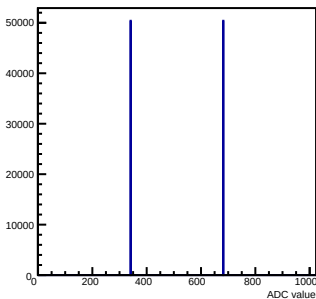
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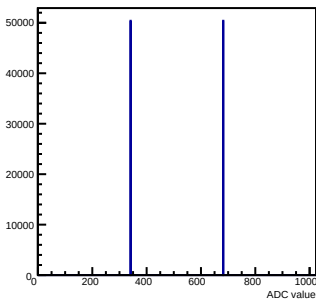
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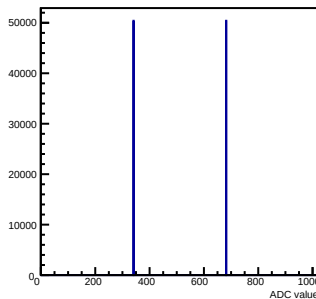
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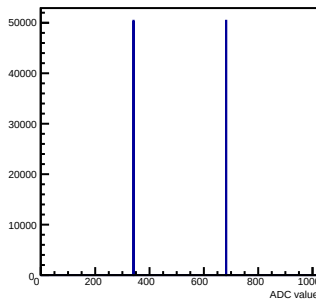
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mcm 0 chip 2 chan 14 test1



mcm 0 chip 2 chan 15 test1

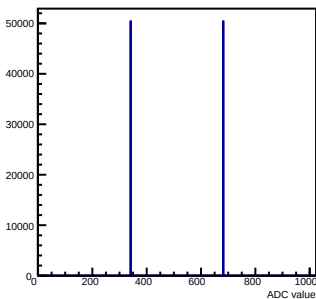


[illegible]

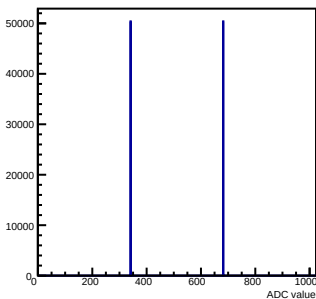
A histogram plot showing the distribution of ADC values for the two most significant bits of the output code. The x-axis is labeled "ADC value" and ranges from 0 to 1000 with major ticks every 200 units. The y-axis ranges from 0 to 50,000 with major ticks every 10,000 units. There are two prominent vertical bars: one at approximately 360 reaching a height of about 50,000, and another at approximately 720 also reaching a height of about 50,000. All other bins have zero frequency.

[illegible]

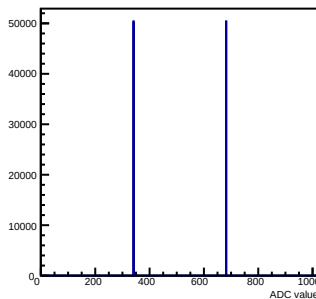
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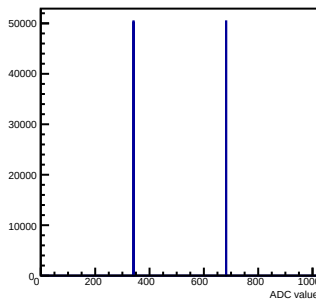
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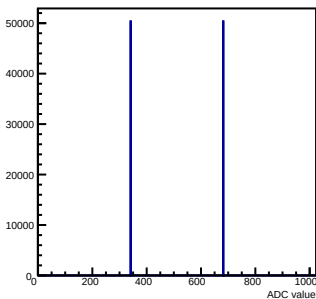
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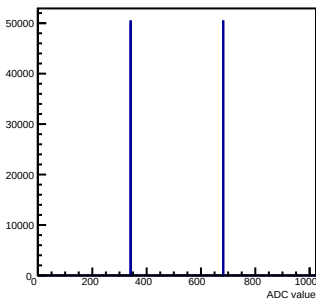
mcm 0 chip 4 chan 3 test1



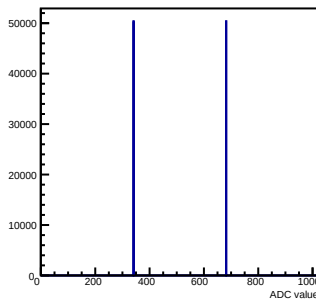
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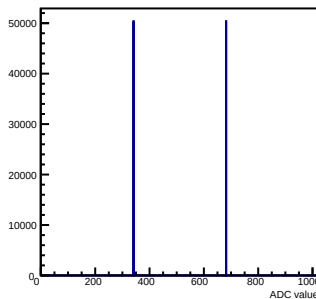
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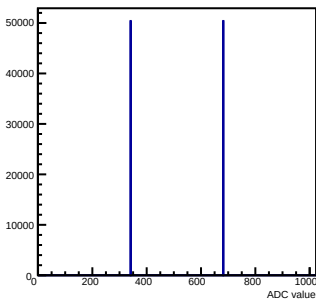
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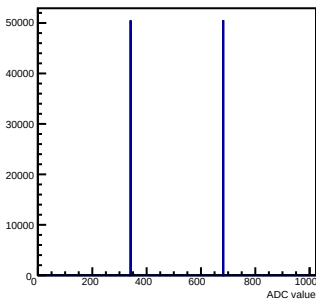
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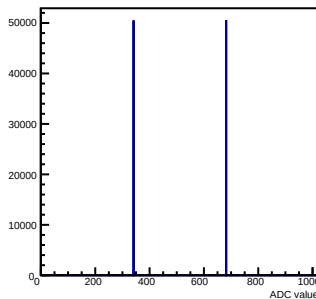
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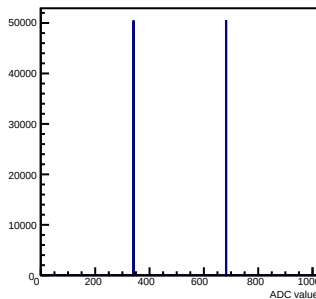
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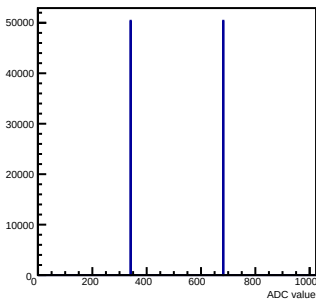
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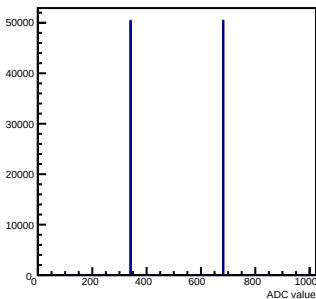
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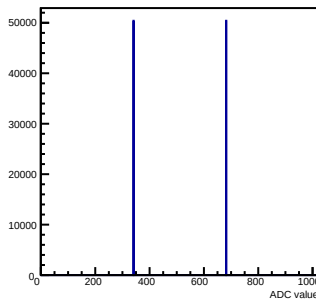
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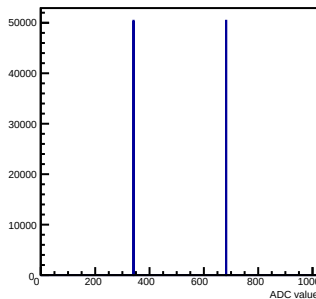
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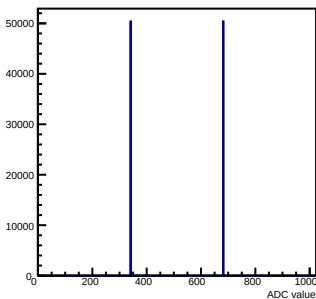
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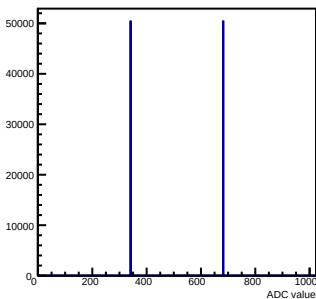
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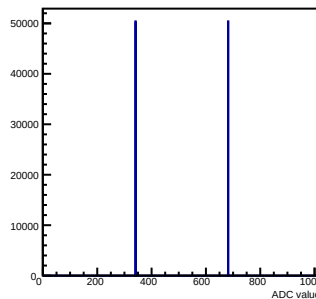
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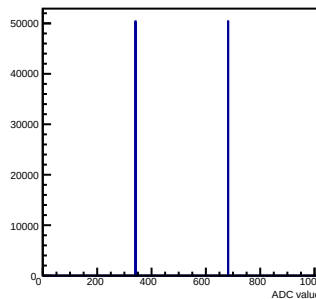
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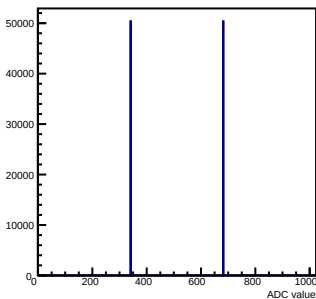
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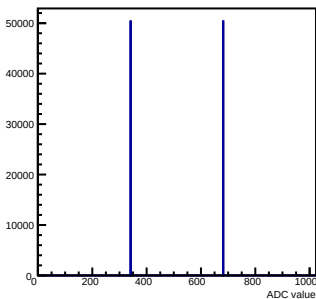
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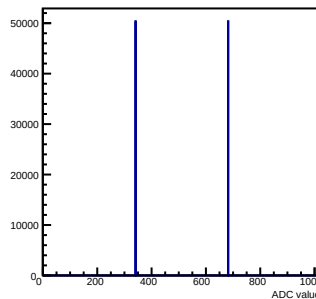
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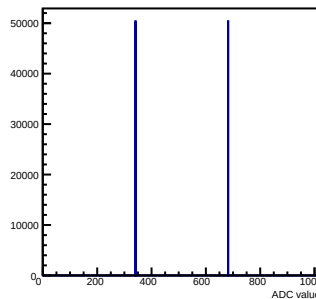
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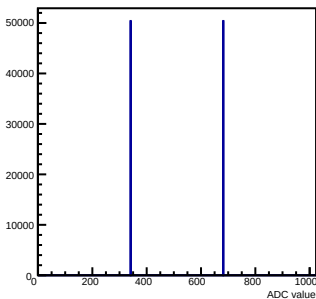
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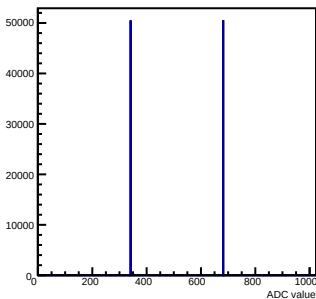
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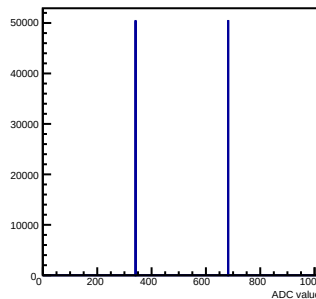
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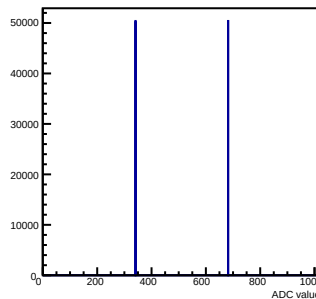
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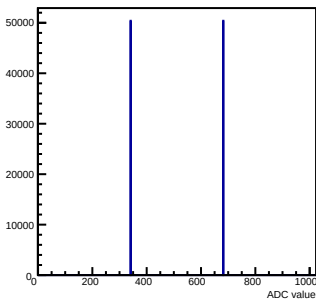
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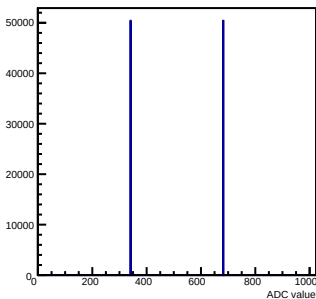
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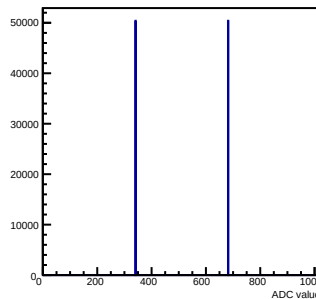
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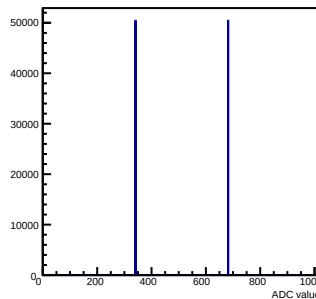
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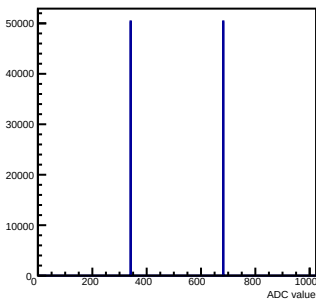
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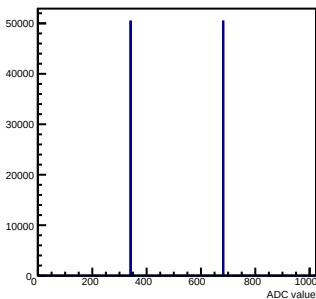
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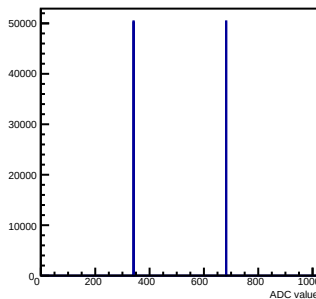
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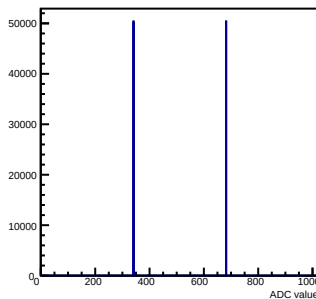
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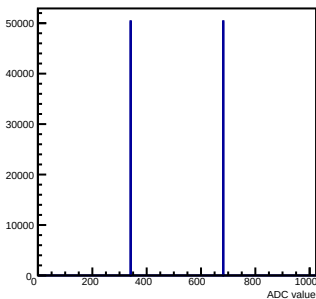
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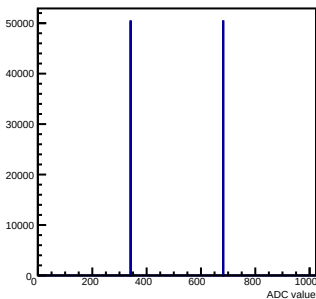
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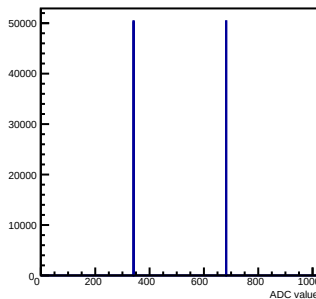
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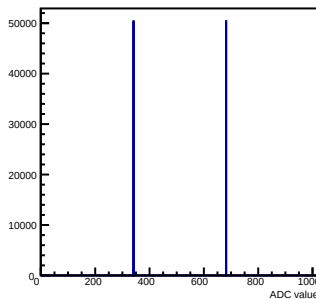
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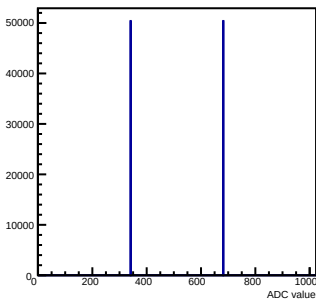
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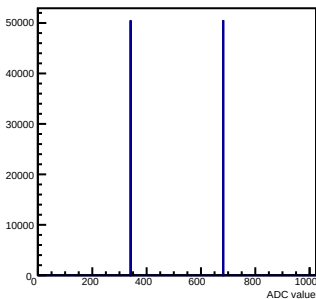
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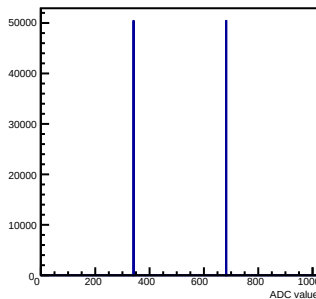
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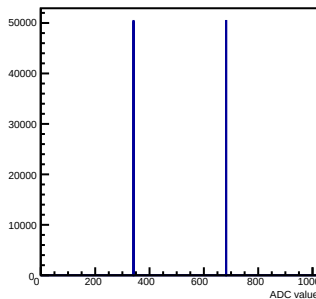
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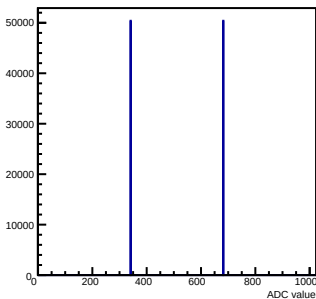
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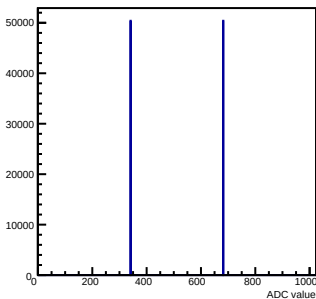
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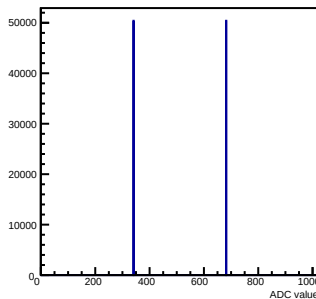
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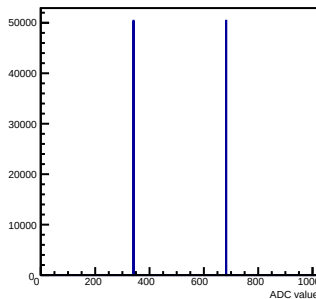
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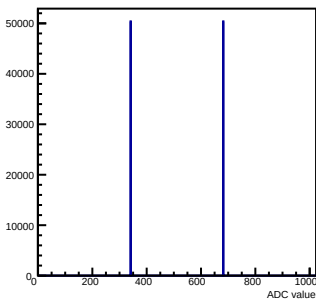
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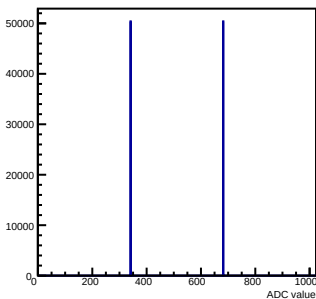
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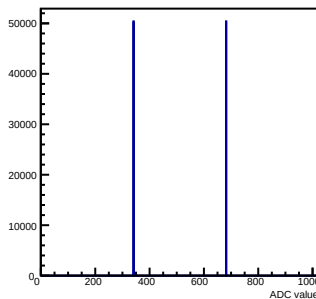
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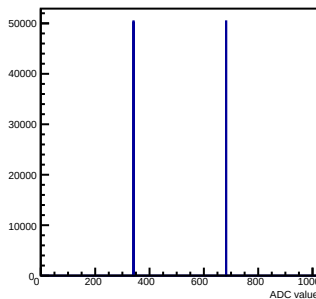
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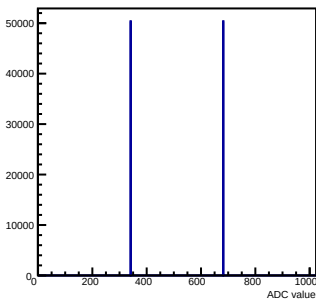
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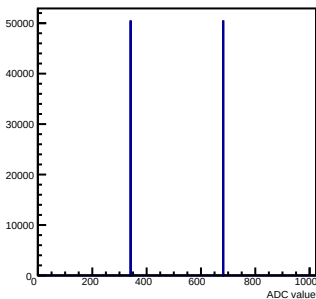
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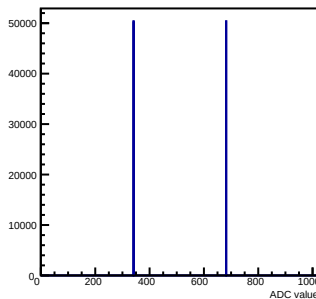
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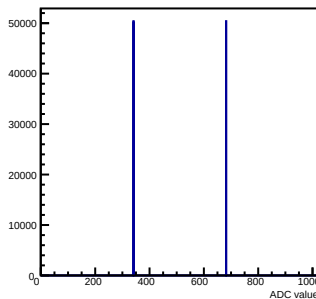
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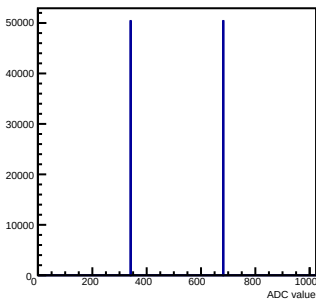
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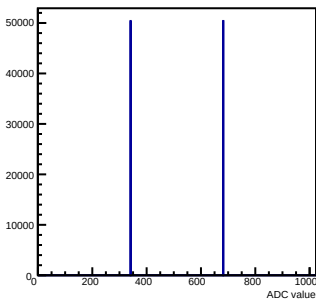
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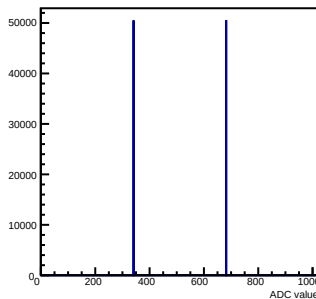
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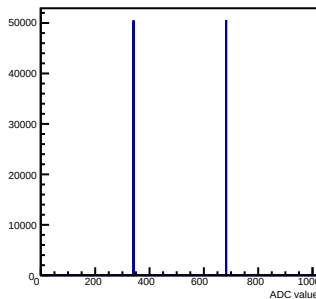
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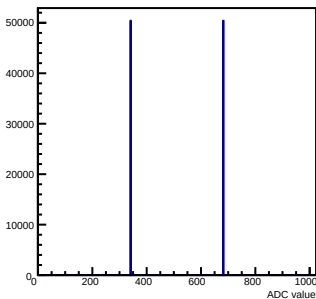
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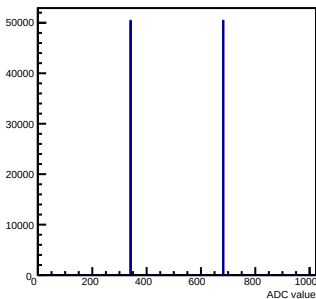
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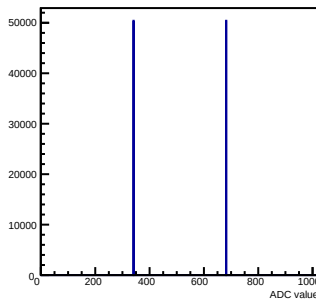
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mcm 0 chip 7 chan 13 test1



mcm 0 chip 7 chan 14 test1



mcm 0 chip 7 chan 15 test1

