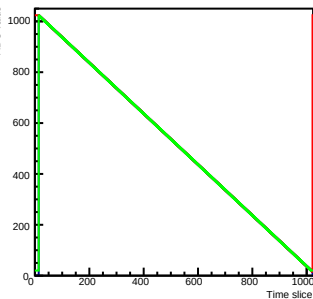
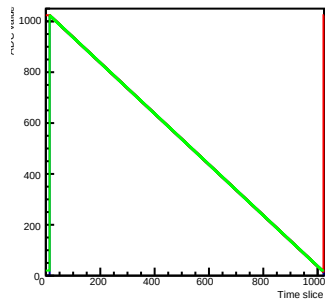


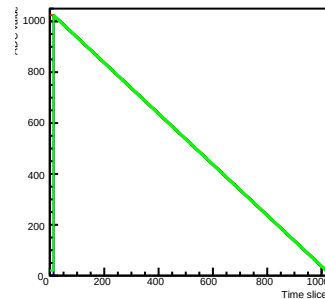
mcm 0 chip 0 chan 0 test1



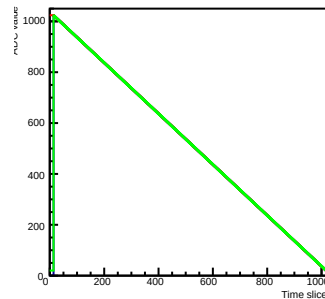
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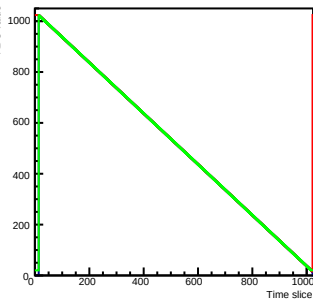
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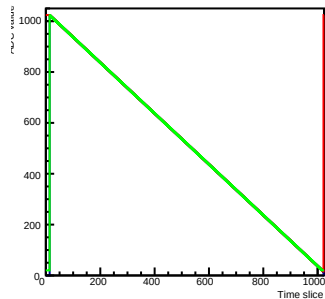
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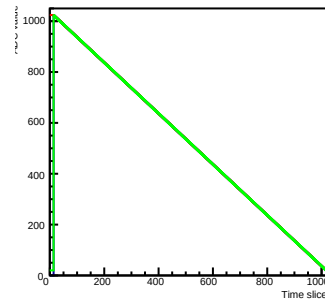
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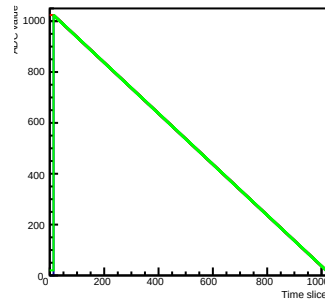
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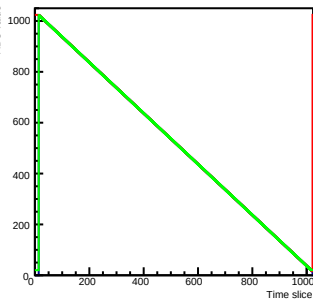
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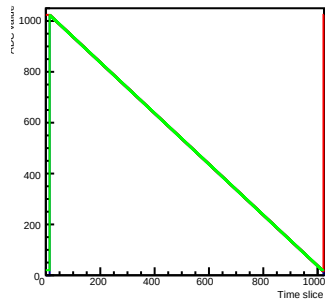
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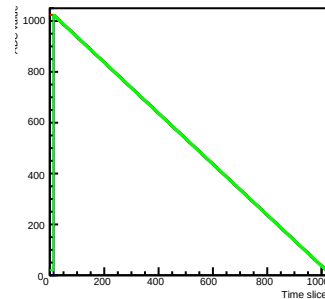
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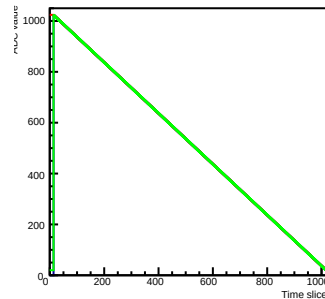
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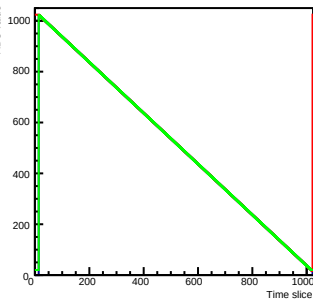
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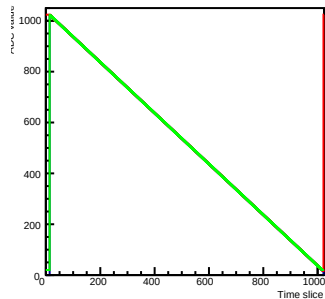
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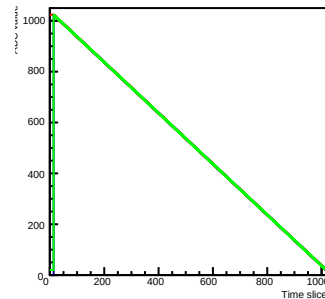
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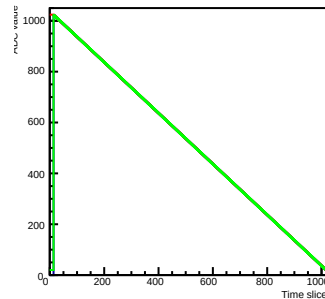
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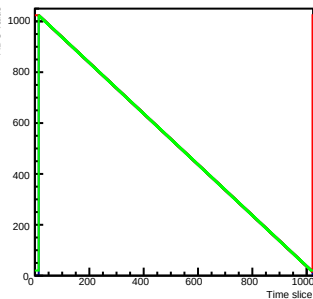
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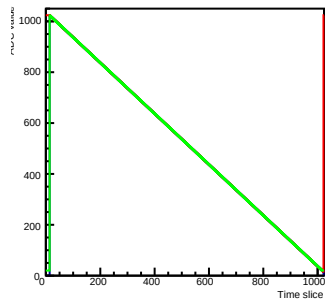
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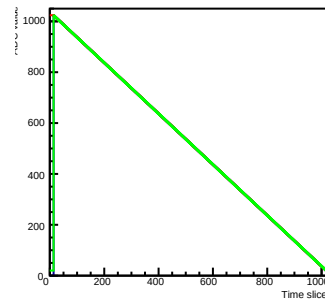
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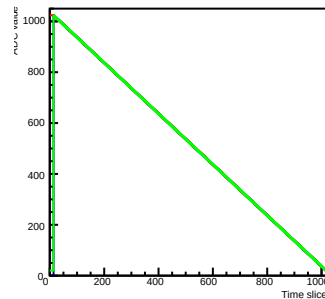
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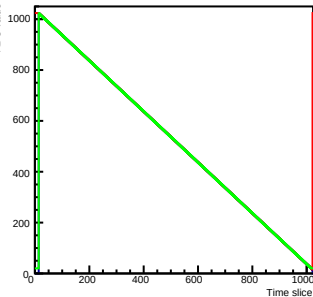
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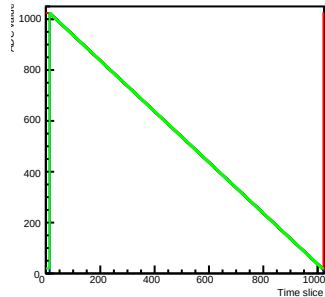
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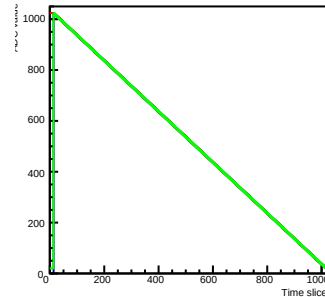
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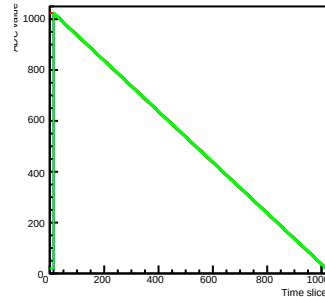
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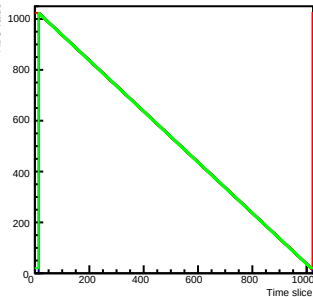
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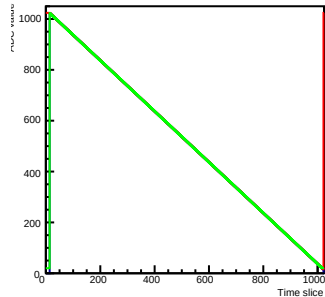
mcm 0 chip 1 chan 7 test1



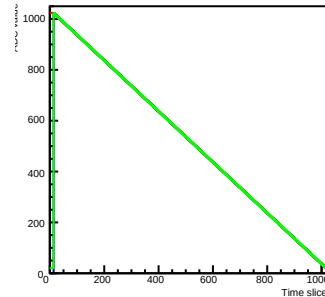
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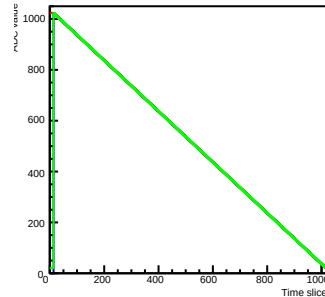
mcm 0 chip 1 chan 9 test1



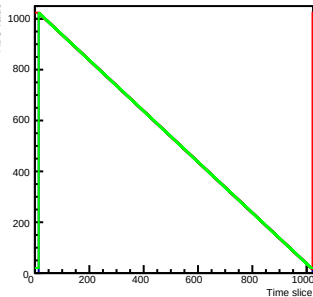
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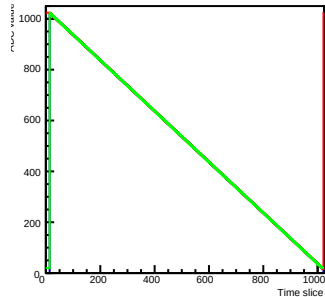
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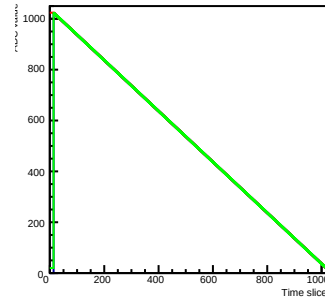
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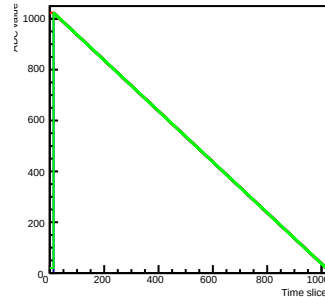
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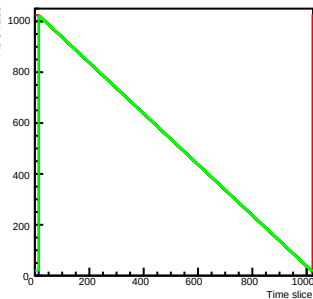
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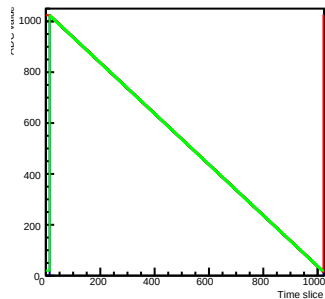
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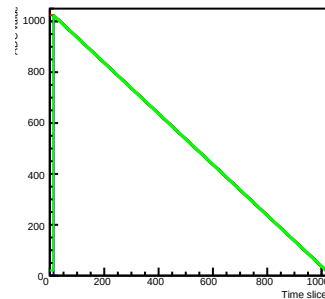
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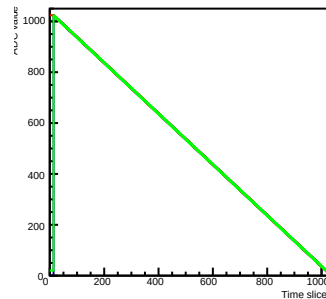
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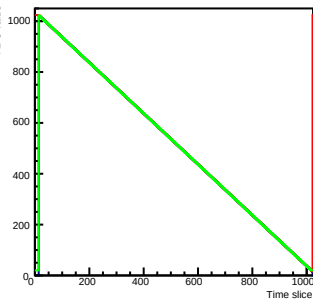
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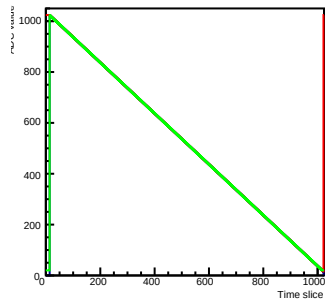
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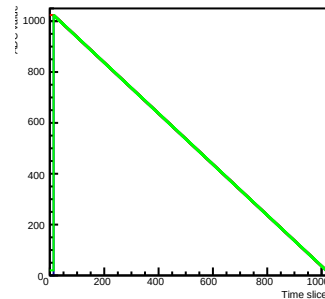
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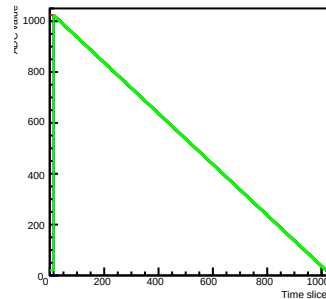
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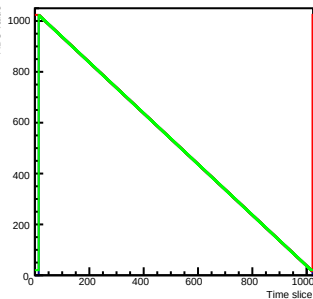
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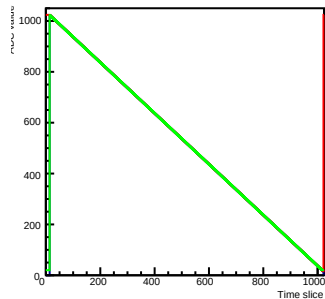
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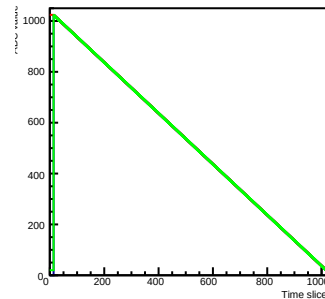
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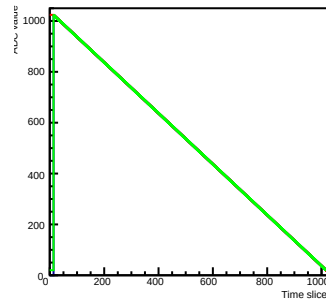
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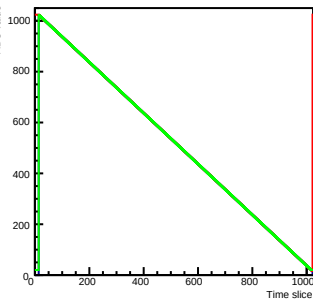
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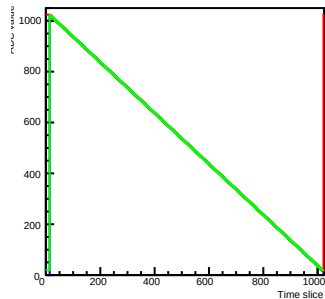
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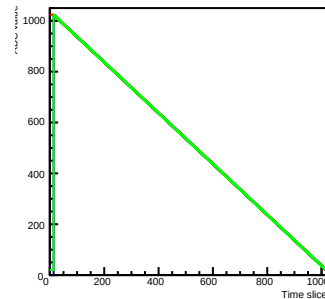
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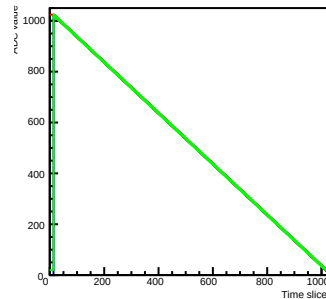
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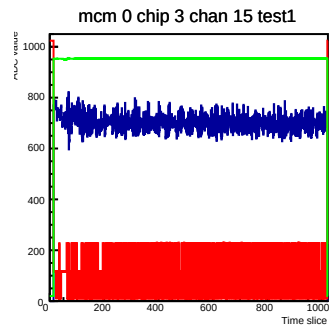
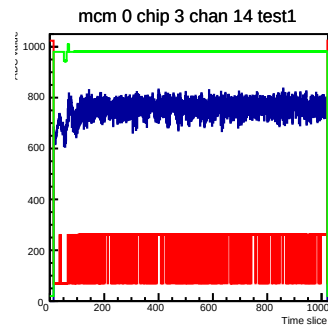
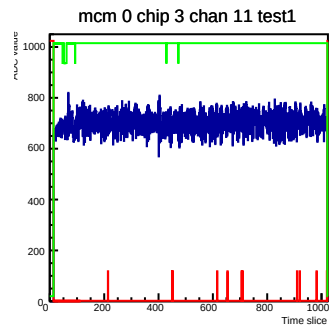
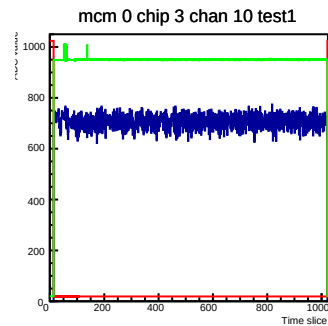
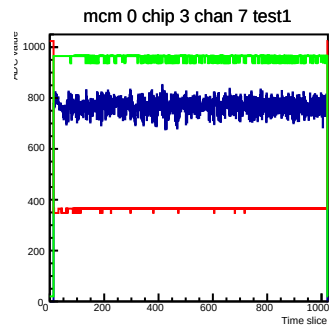
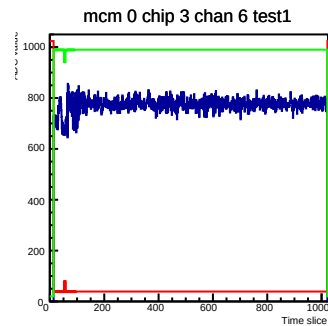
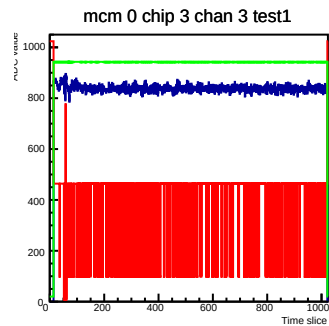
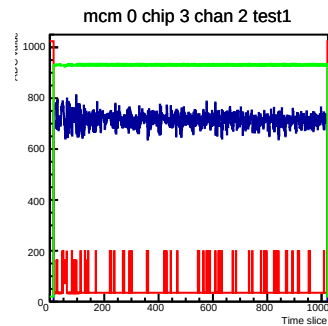


mcm 0 chip 2 chan 14 test1

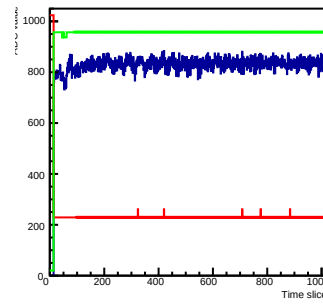


mcm 0 chip 2 chan 15 test1

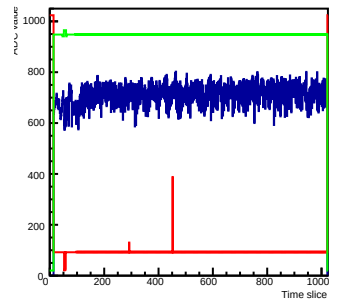




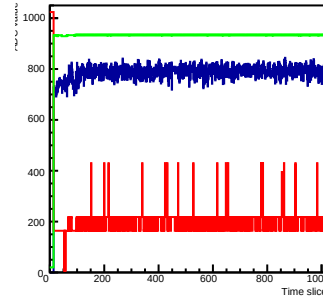
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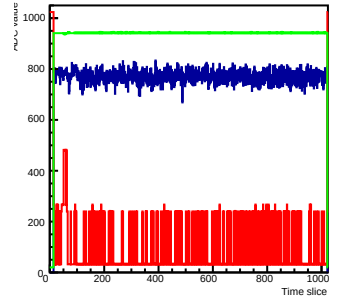
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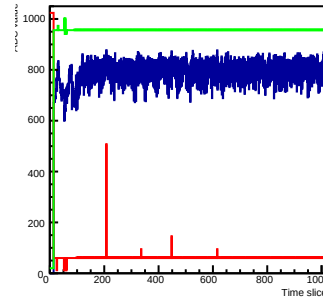
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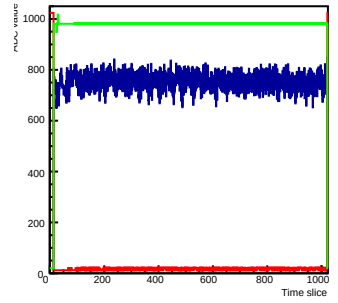
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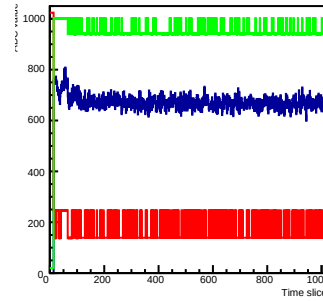
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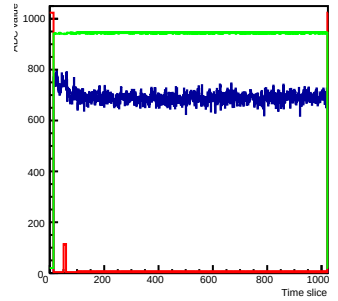
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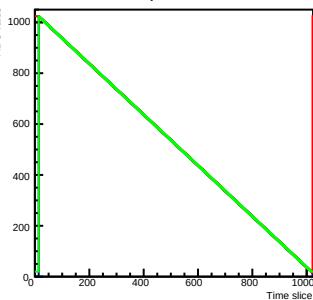
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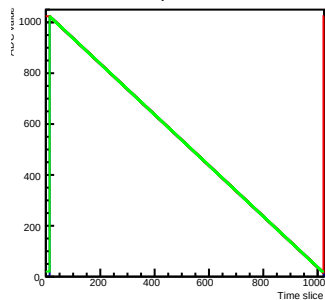
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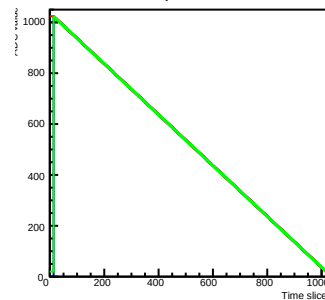
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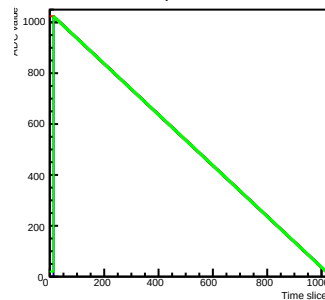
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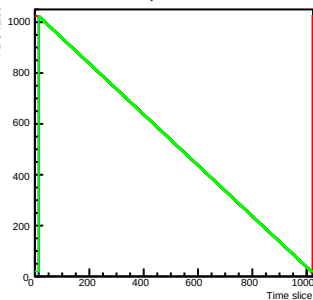
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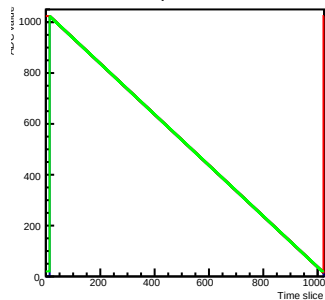
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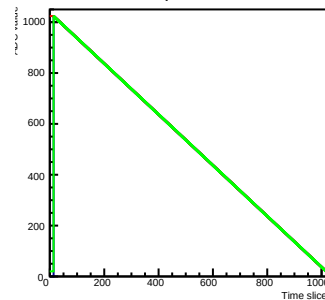
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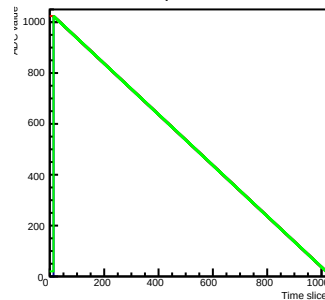
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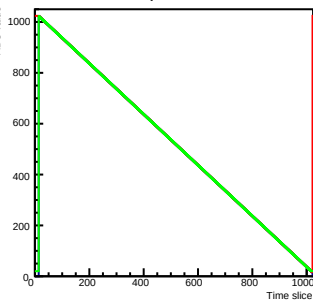
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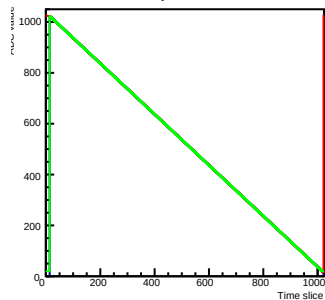
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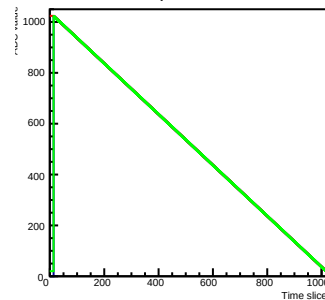
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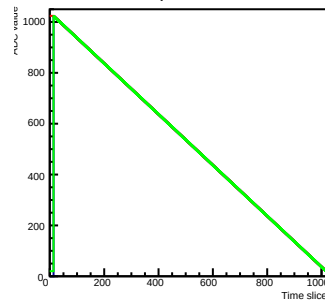
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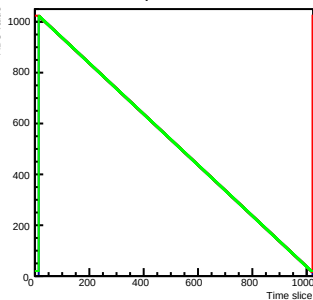
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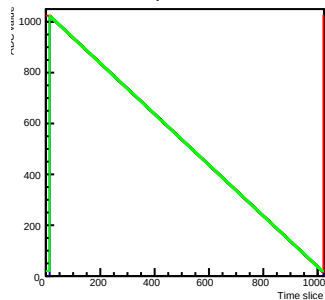
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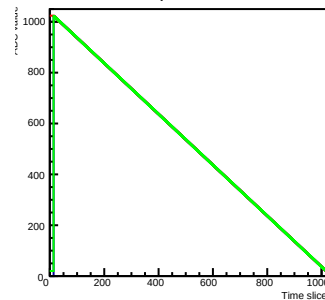
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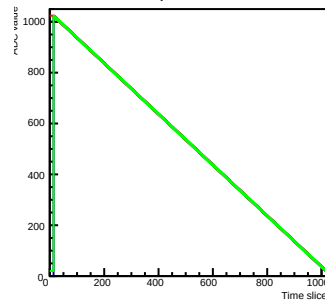
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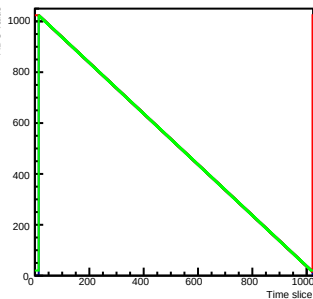
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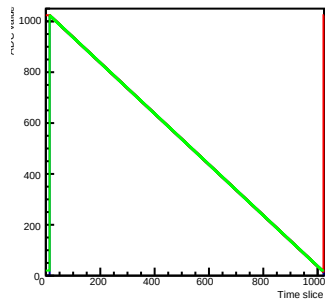
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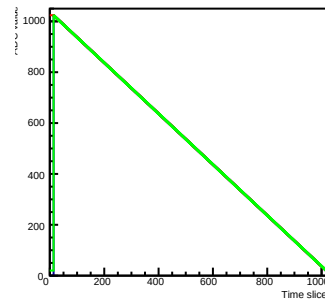
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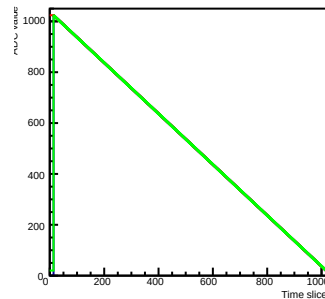
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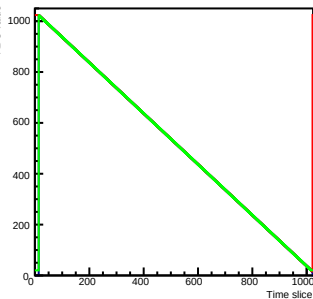
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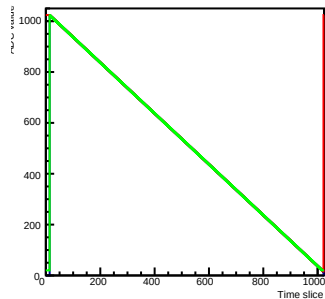
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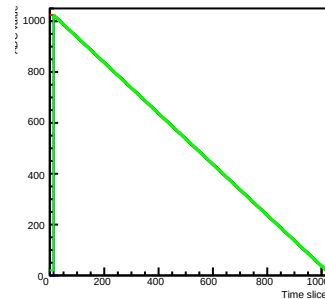
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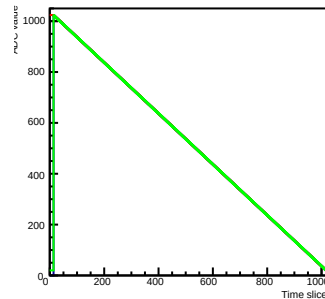
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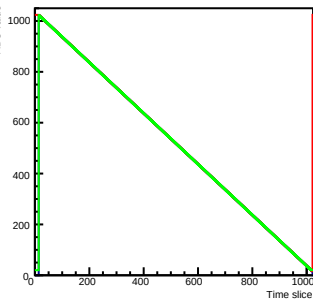
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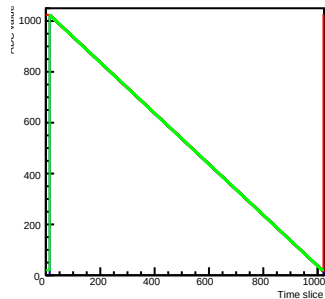
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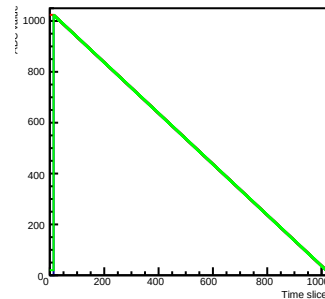
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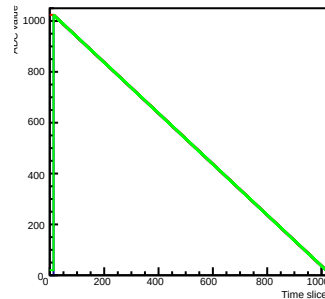
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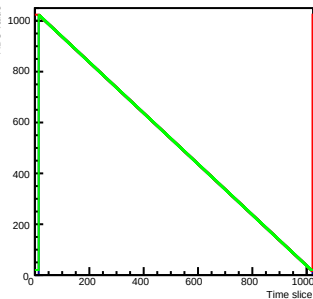
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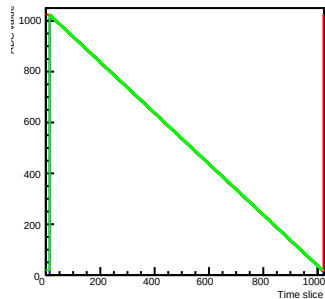
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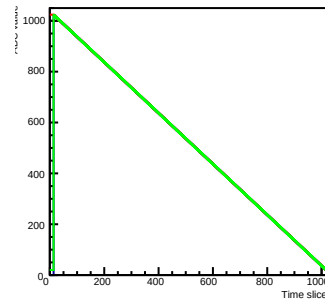
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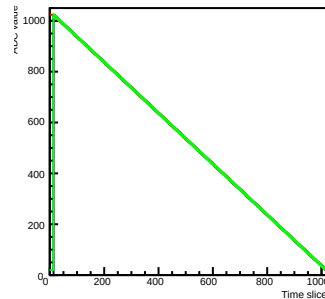
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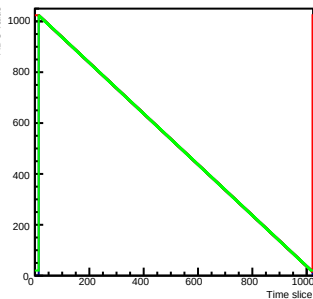
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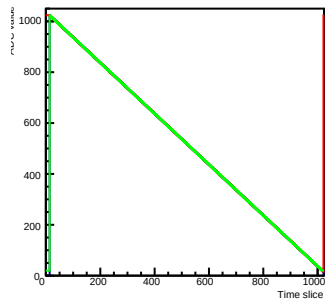
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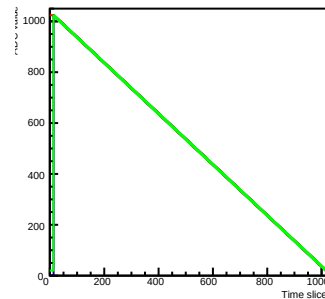
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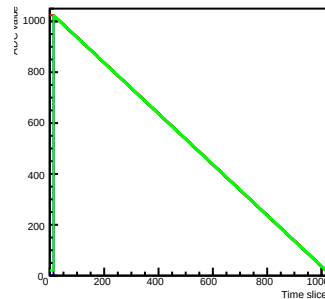
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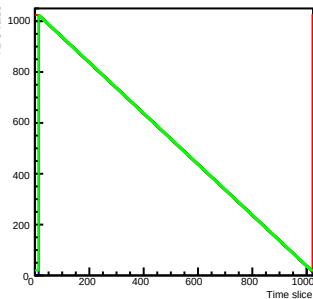
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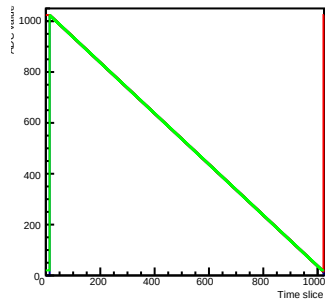
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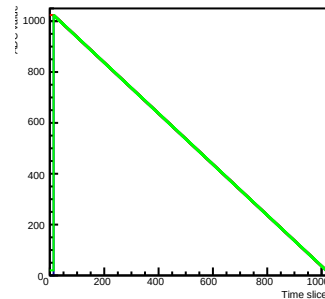
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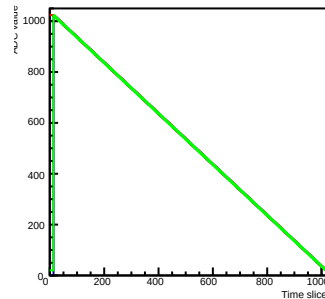
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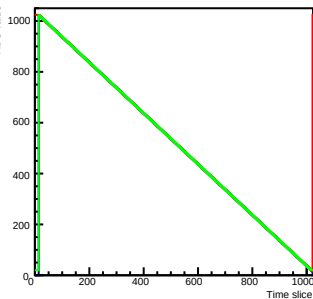
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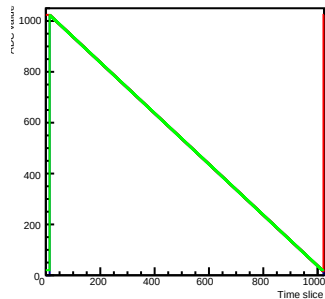
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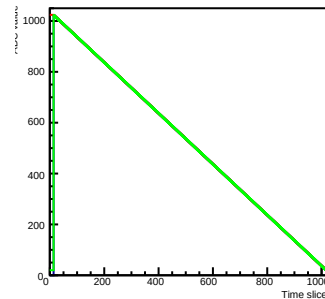
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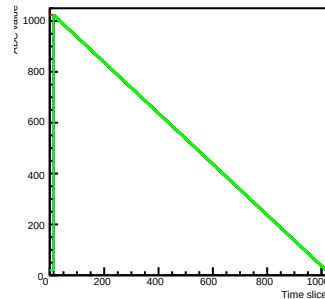
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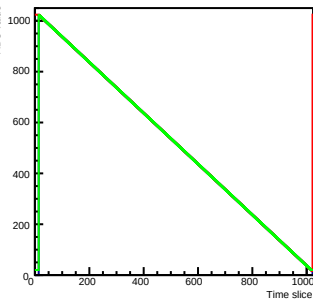
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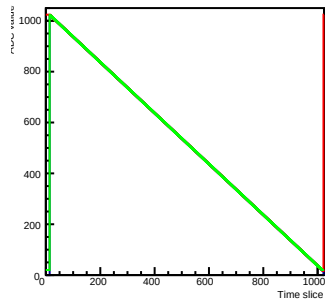
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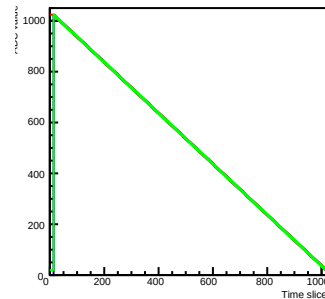
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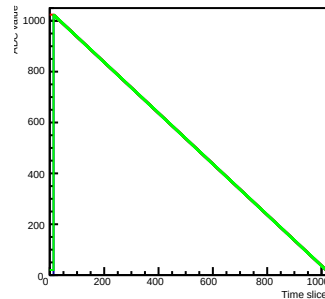
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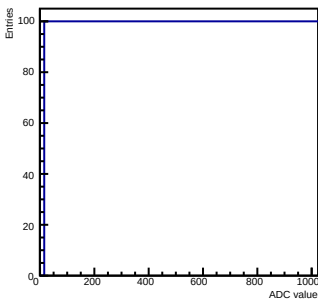
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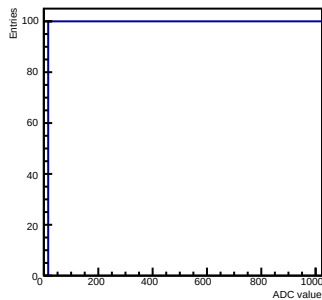
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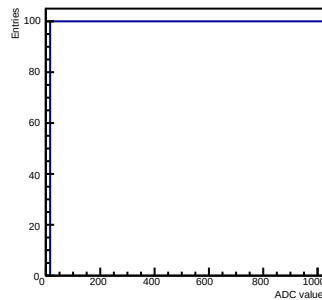
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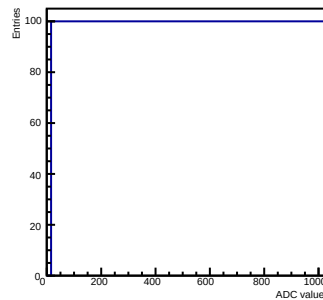
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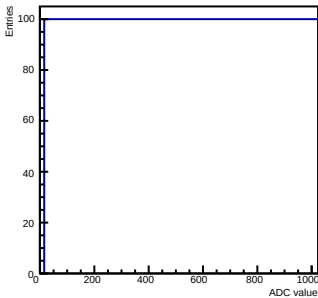
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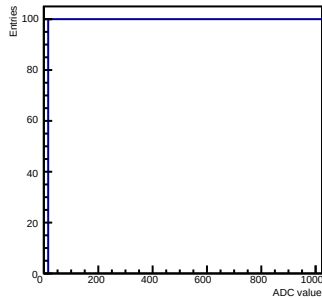
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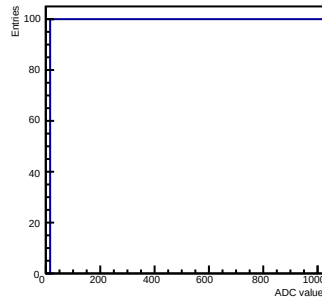
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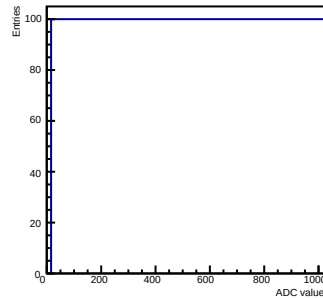
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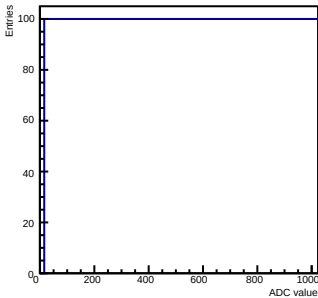
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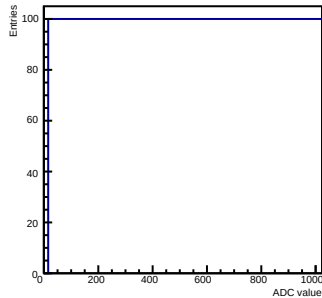
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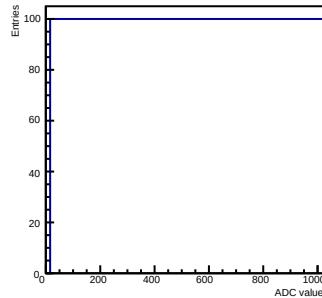
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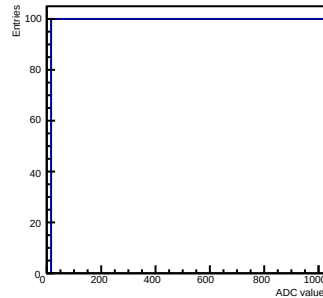
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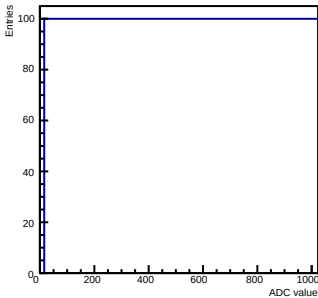
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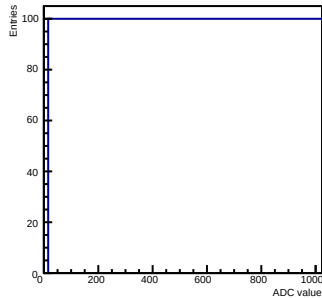
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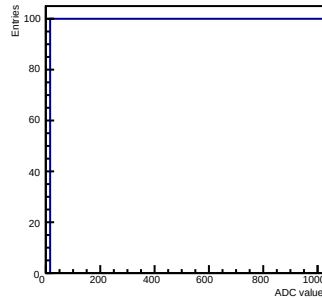
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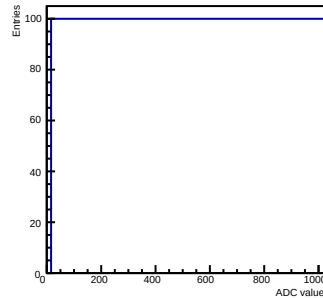
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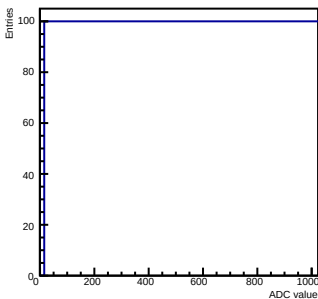
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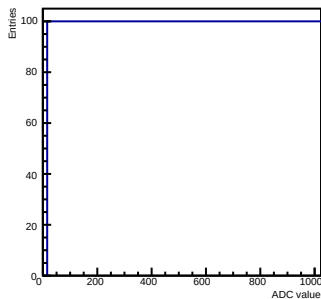
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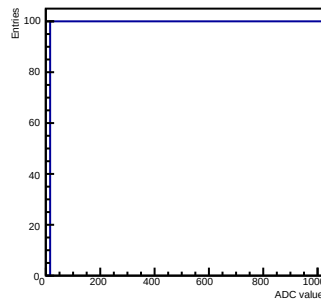
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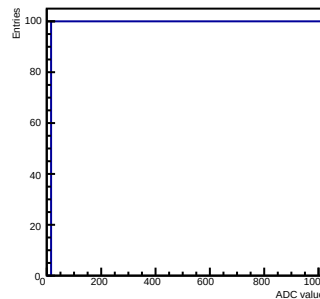
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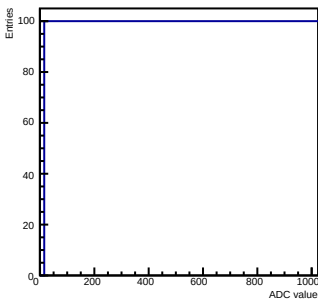
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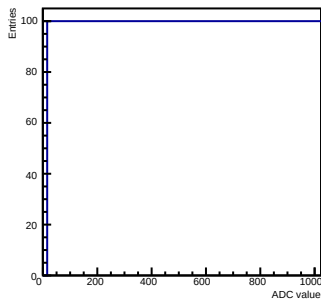
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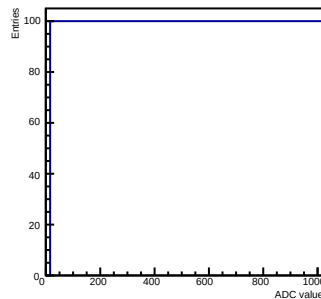
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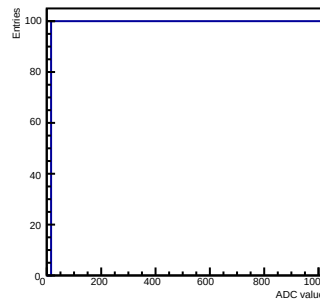
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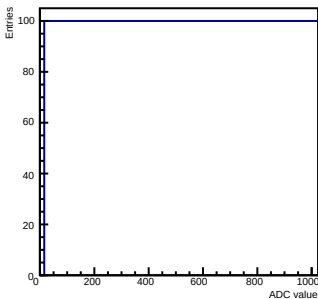
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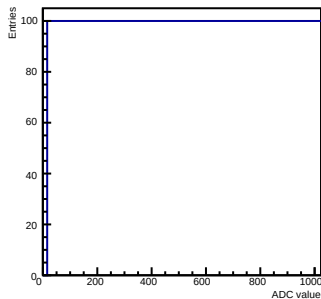
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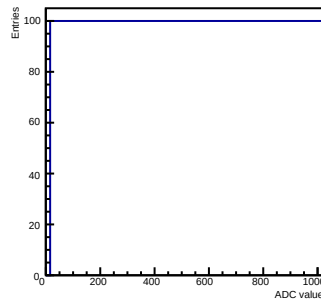
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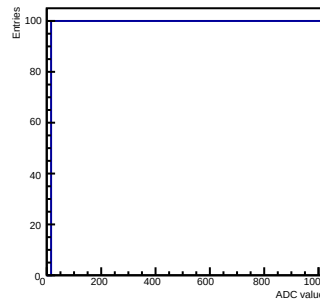
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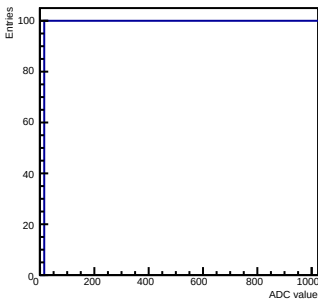
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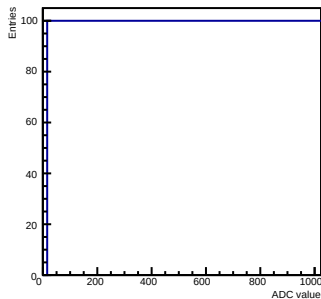
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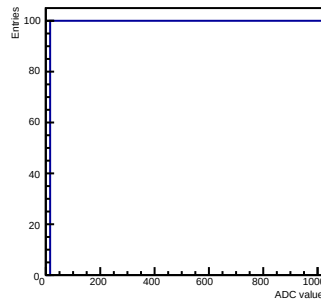
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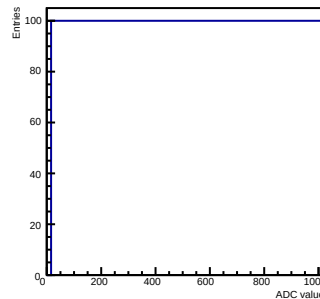
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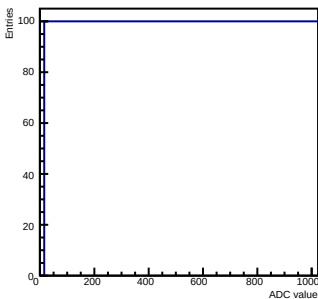
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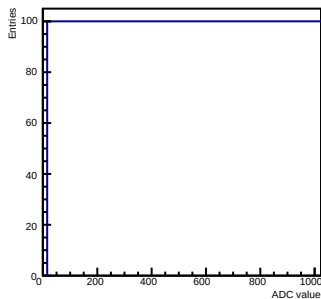
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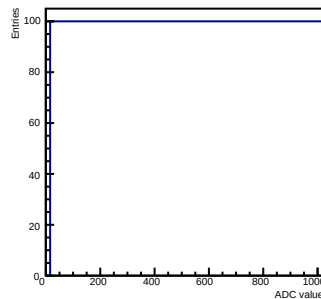
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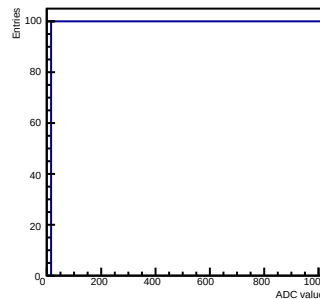
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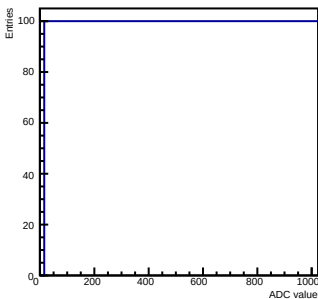
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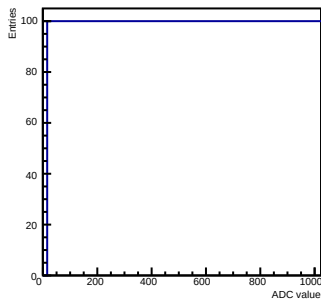
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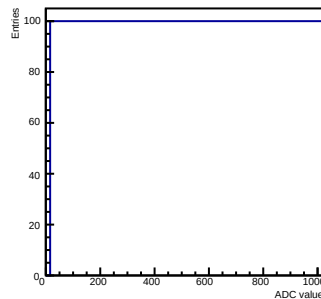
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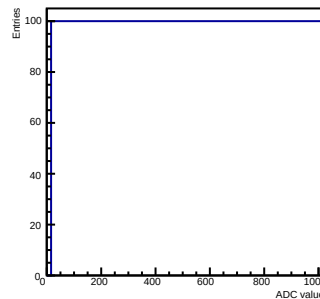
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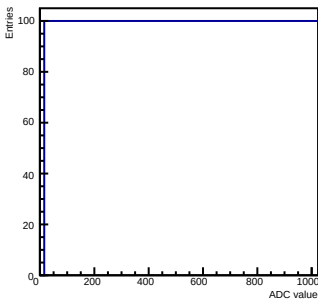
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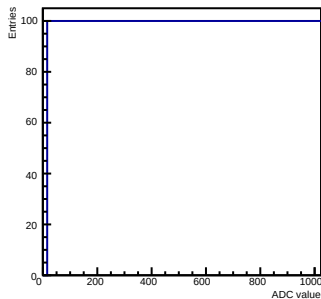
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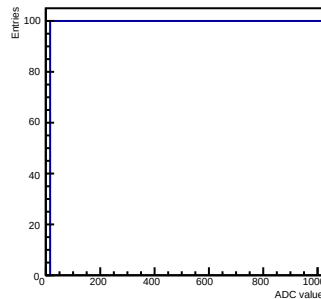
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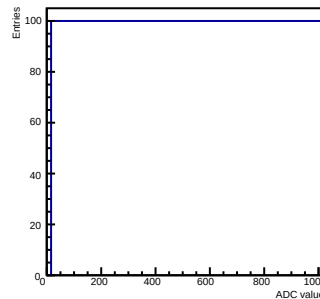
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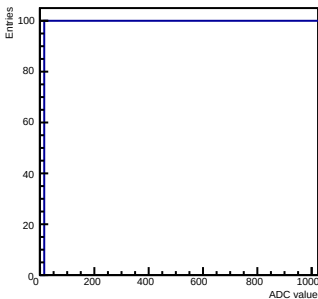
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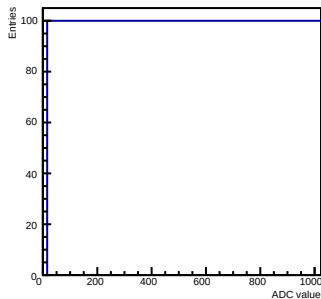
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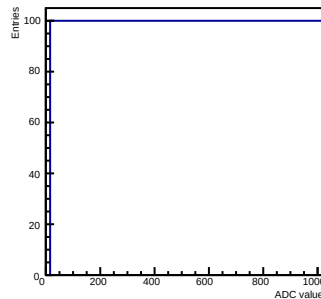
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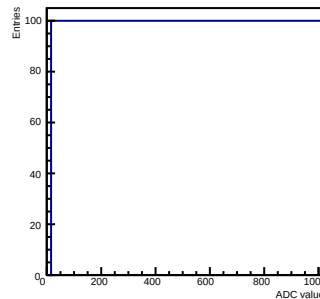
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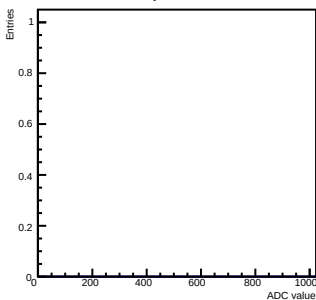
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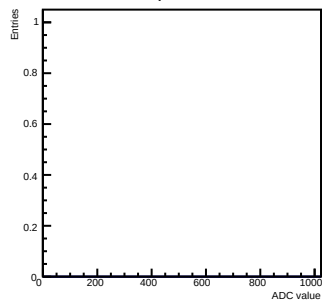
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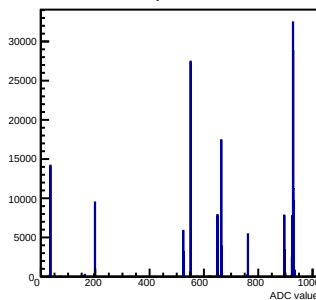
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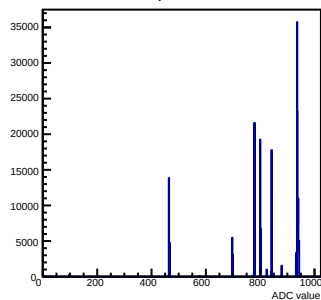
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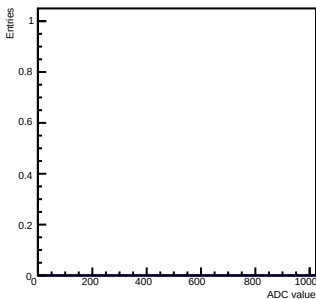
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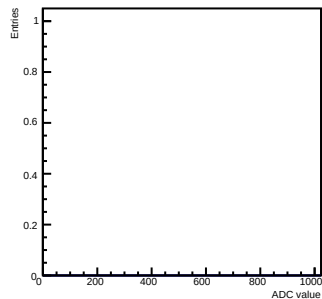
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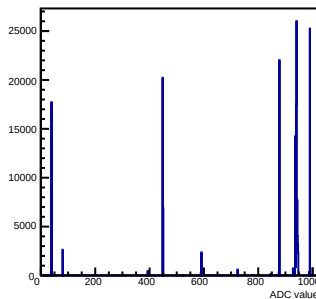
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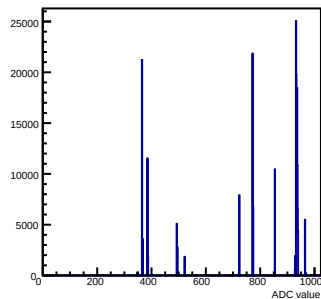
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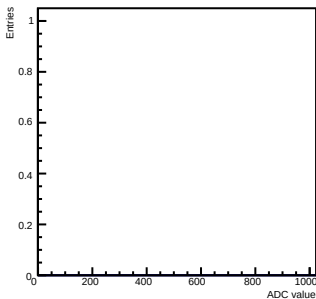
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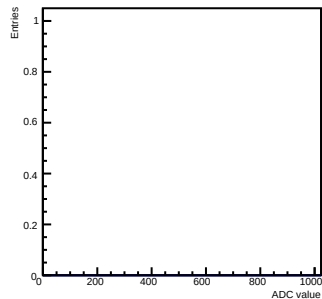
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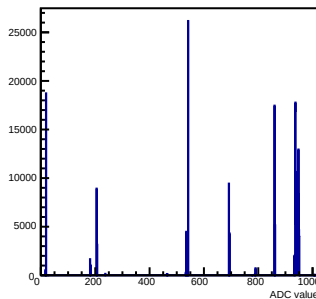
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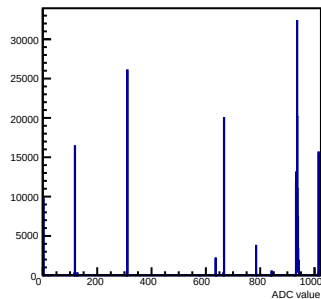
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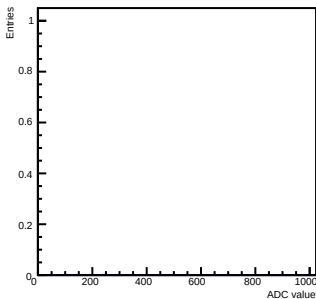
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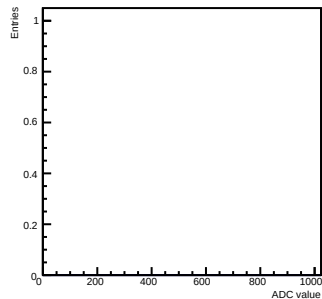
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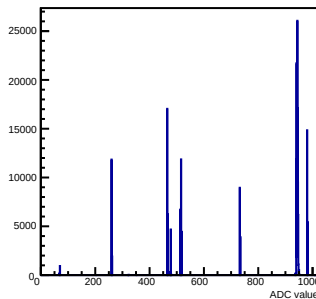
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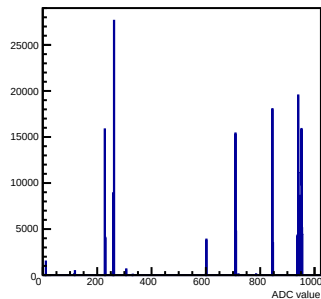
mcm 0 chip 3 chan 13 test1

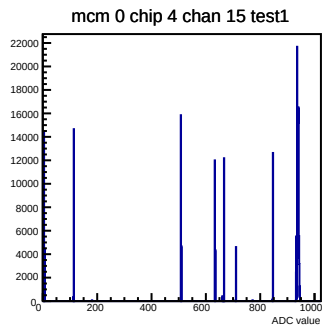
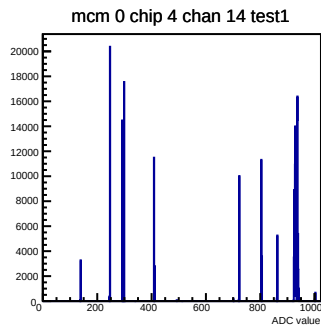
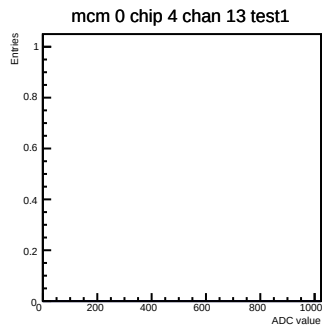
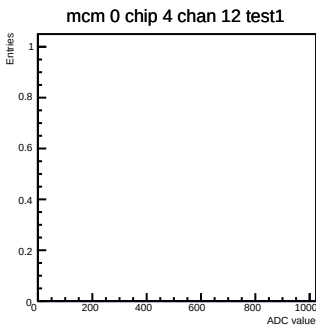
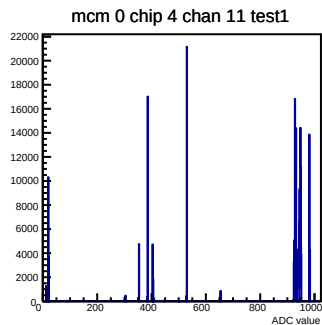
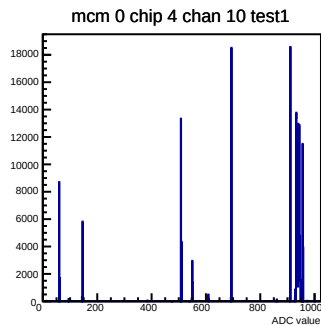
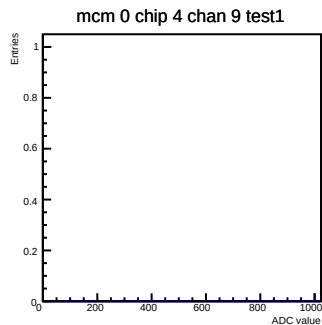
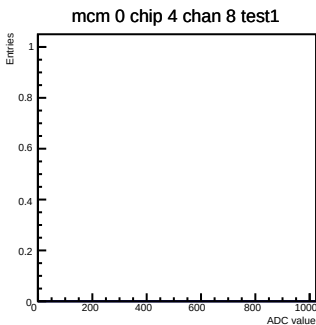
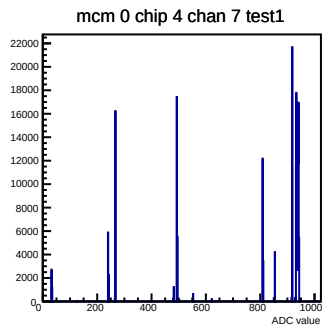
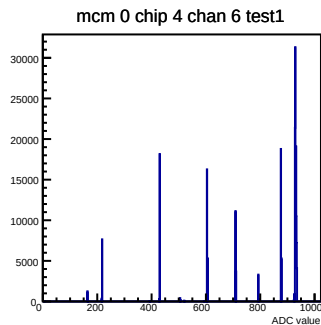
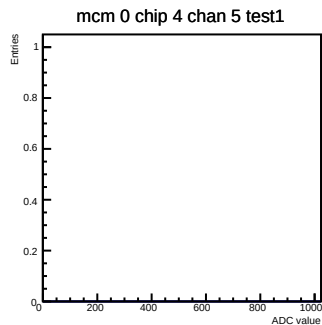
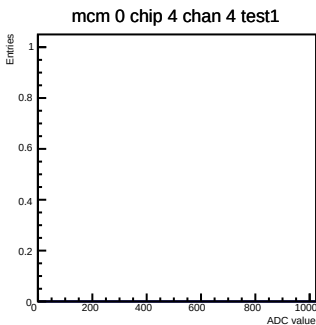
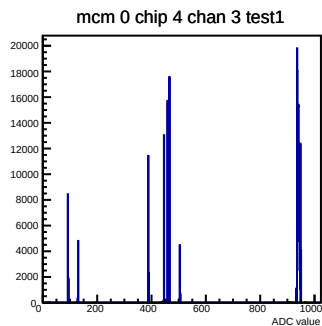
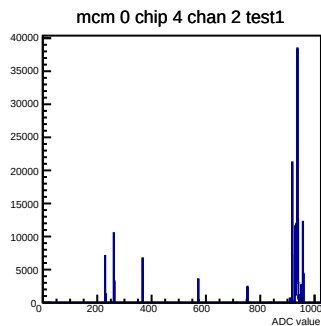
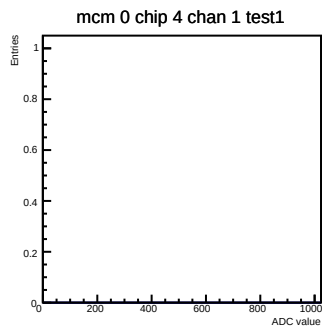
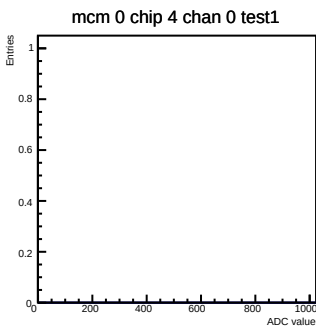


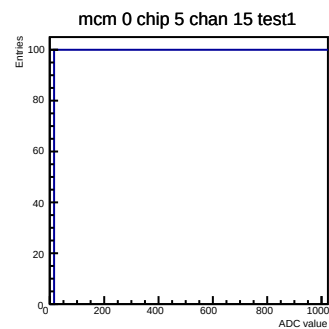
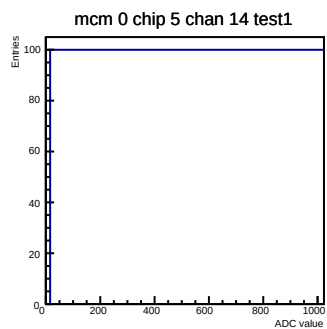
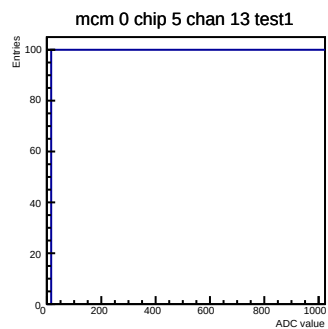
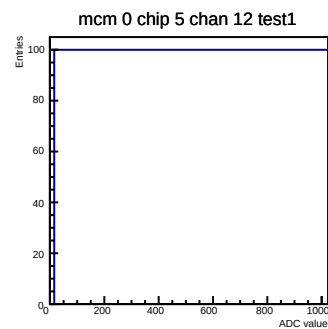
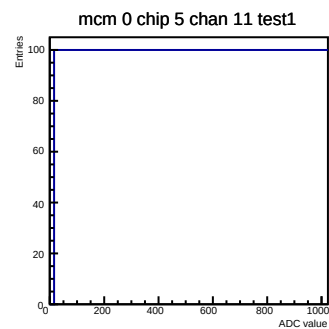
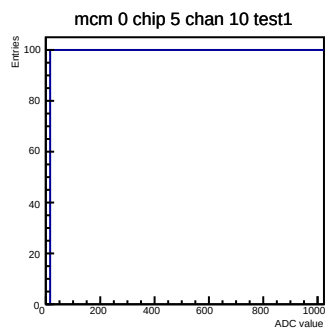
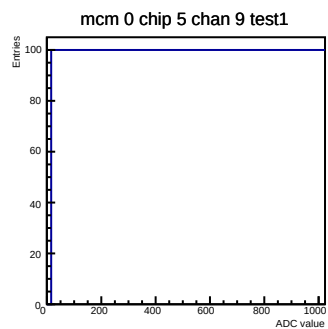
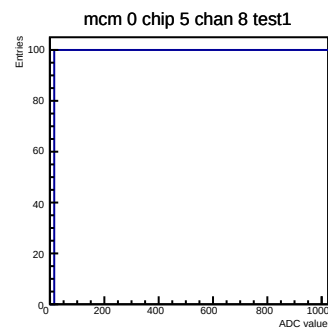
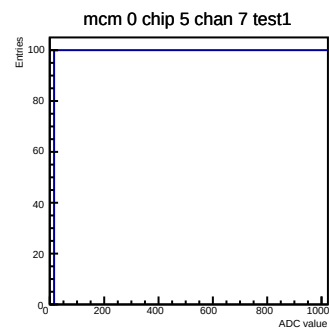
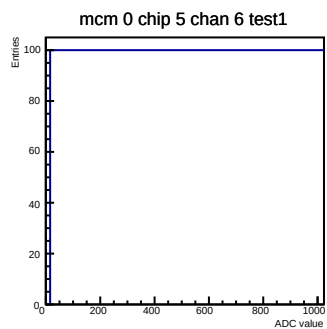
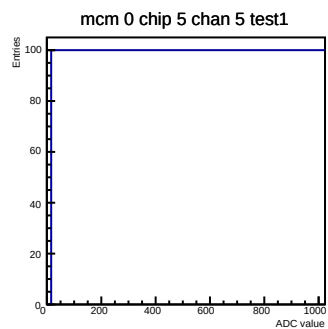
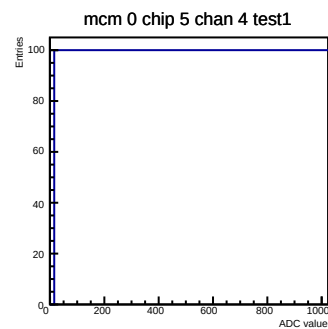
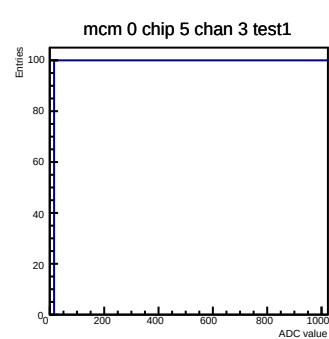
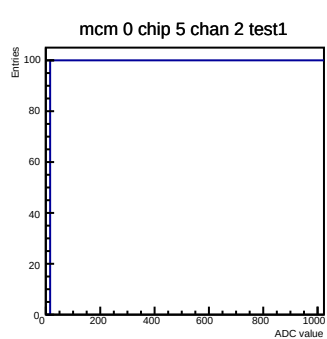
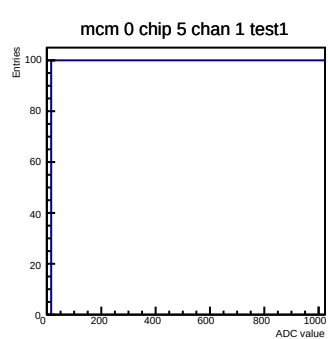
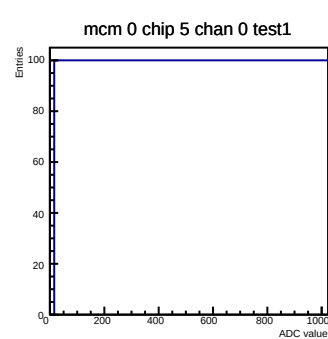
mcm 0 chip 3 chan 14 test1



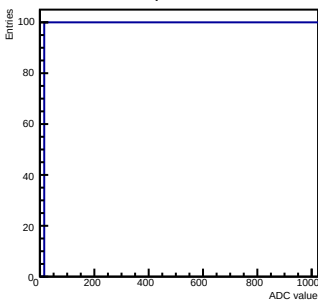
mcm 0 chip 3 chan 15 test1



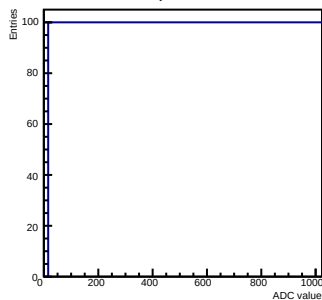




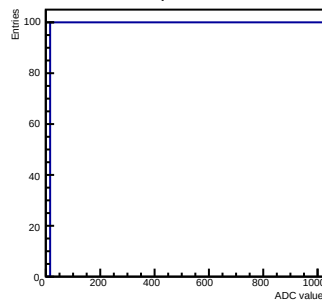
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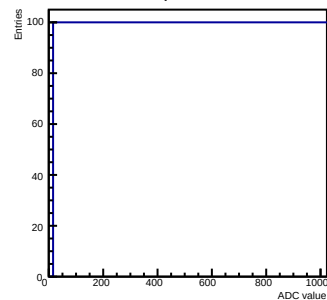
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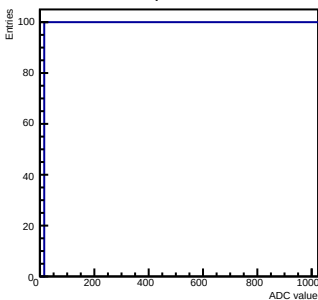
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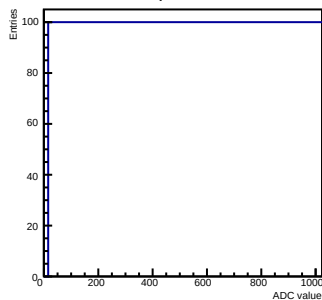
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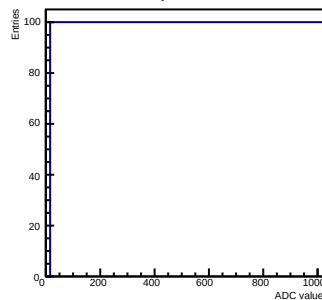
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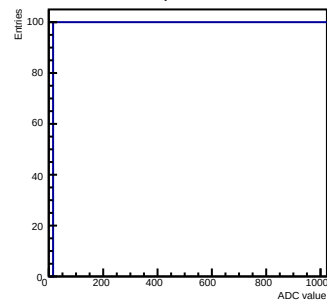
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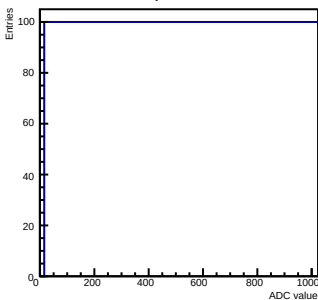
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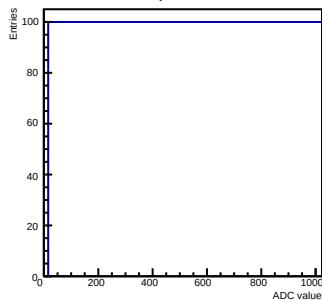
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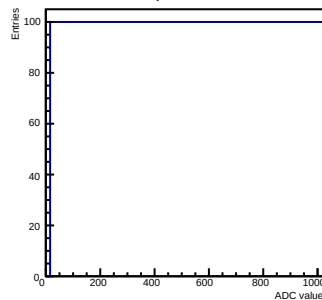
mcm 0 chip 6 chan 8 test1



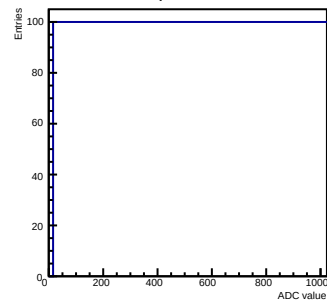
mcm 0 chip 6 chan 9 test1



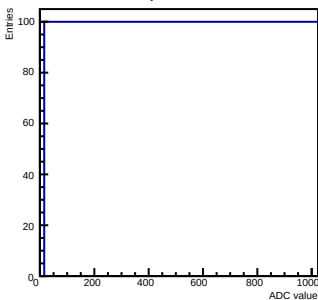
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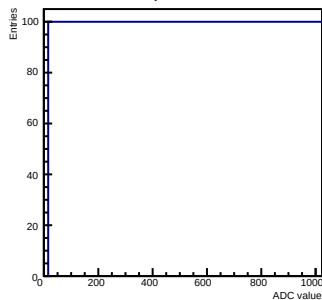
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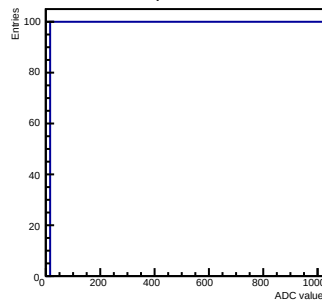
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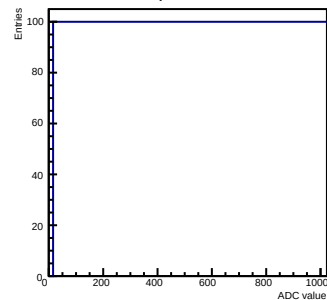
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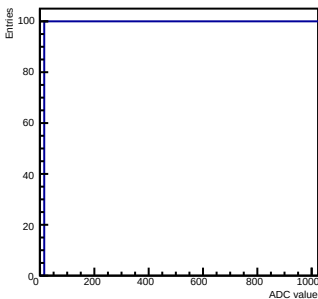
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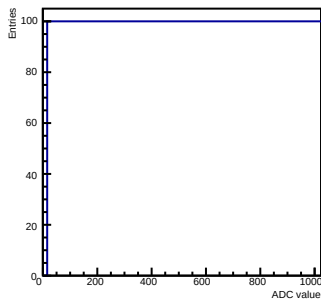
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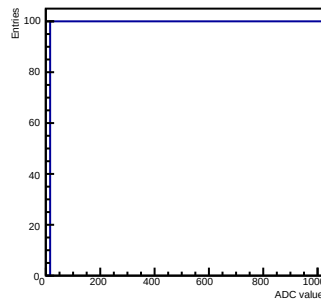
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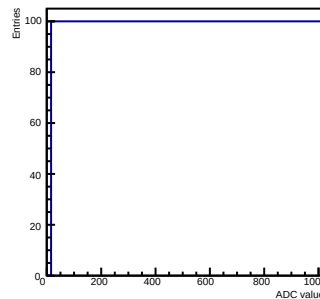
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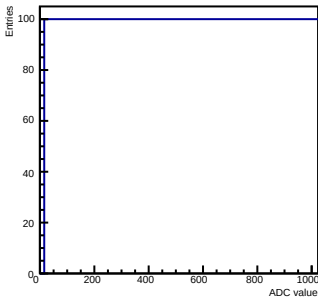
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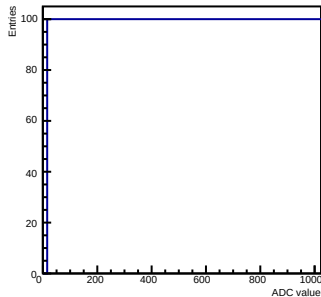
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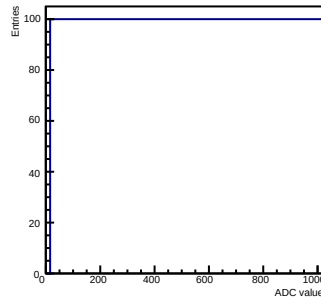
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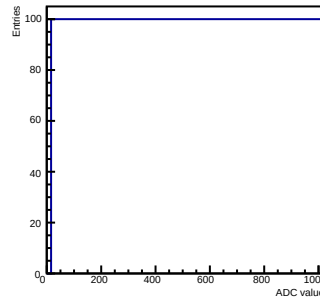
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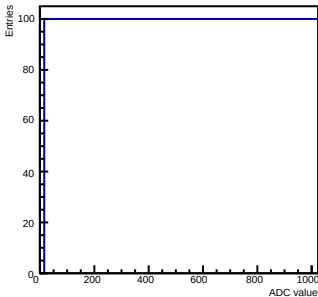
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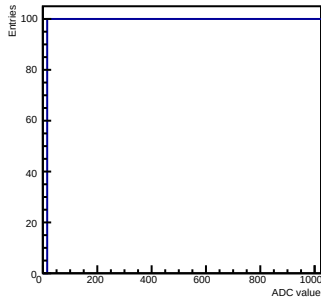
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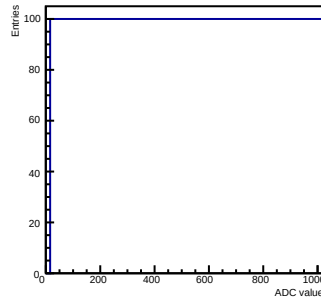
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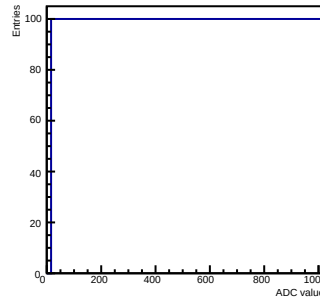
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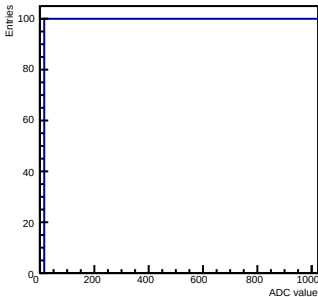
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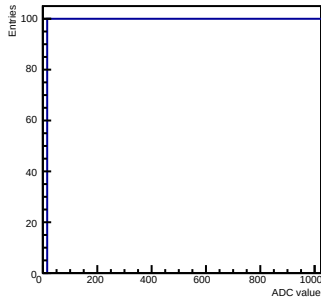
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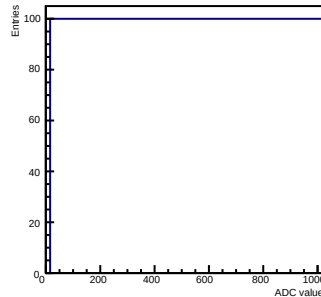
mcm 0 chip 7 chan 12 test1



mcm 0 chip 7 chan 13 test1



mcm 0 chip 7 chan 14 test1



mcm 0 chip 7 chan 15 test1

