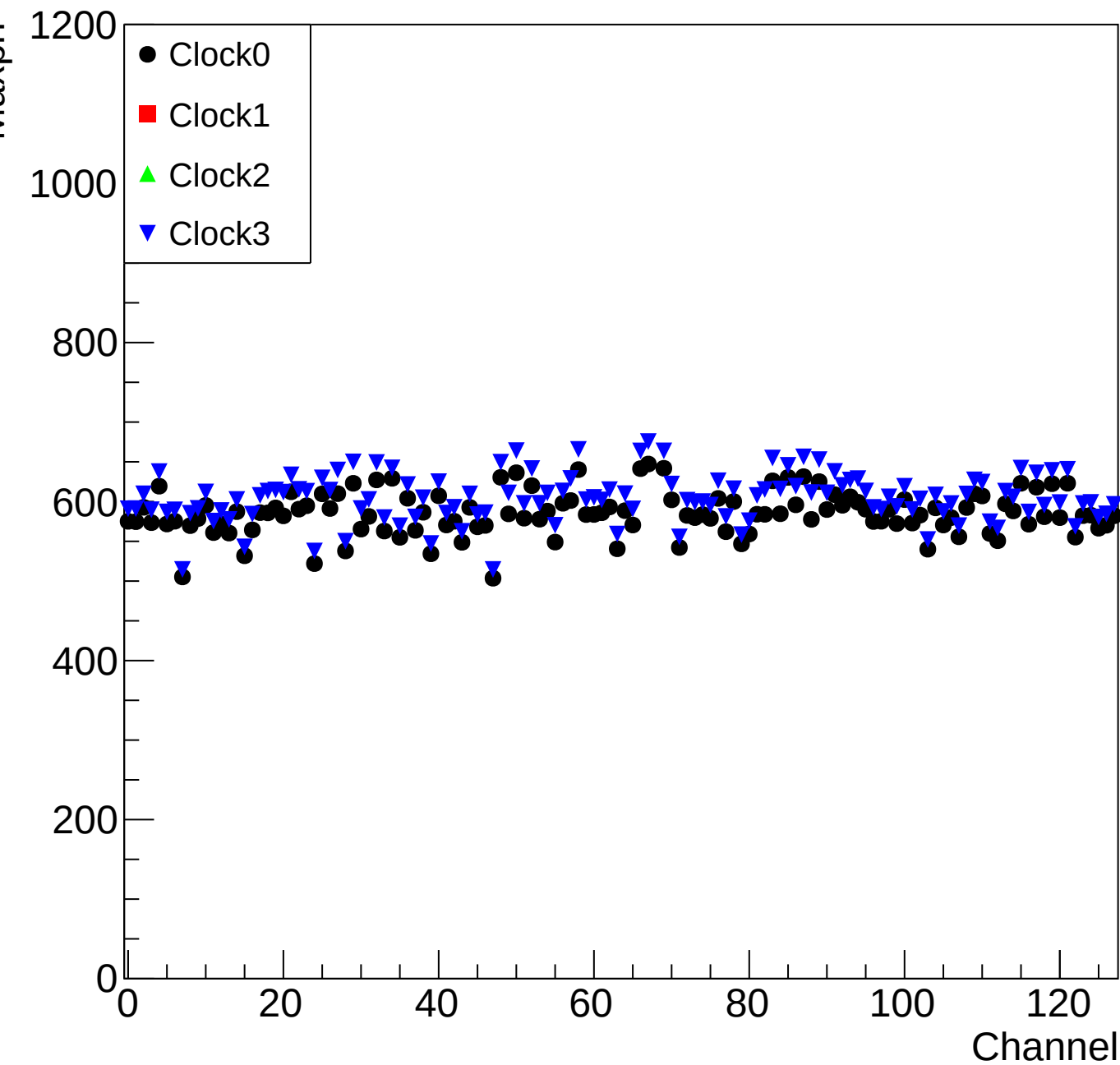


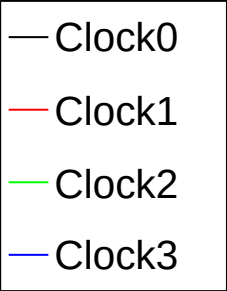
Maxph clocks 0-3



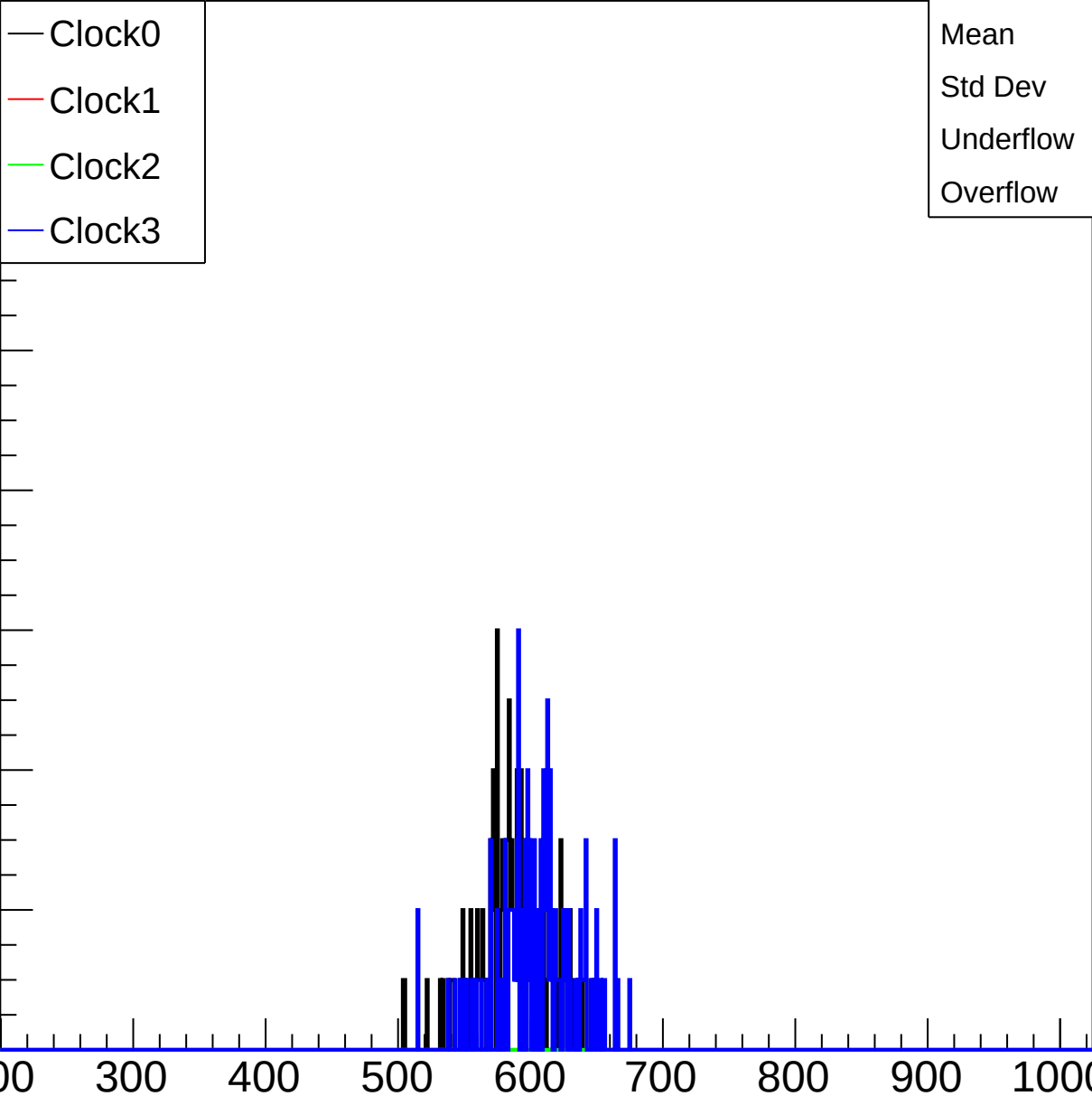
Maxph all clocks 0-3

Entries

14
12
10
8
6
4
2
0

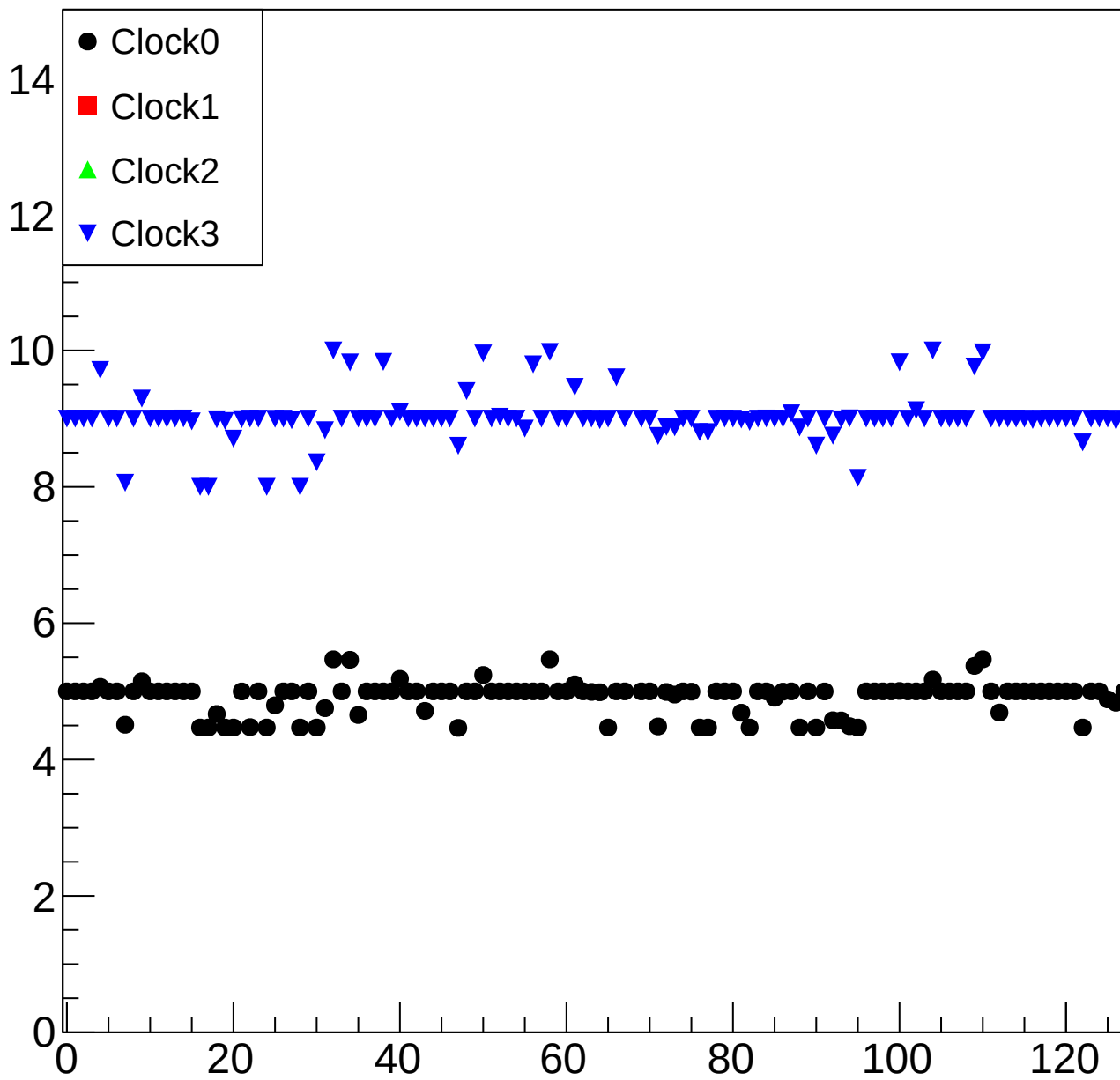


Entries	128
Mean	585.1
Std Dev	27.04
Underflow	0
Overflow	0



ADC

Clocks width 0-3



Clocks width all 0-3

