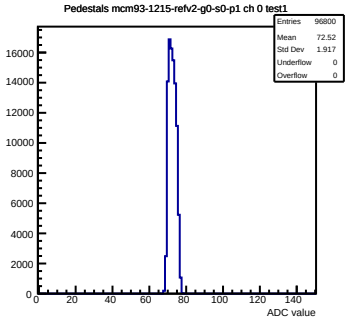
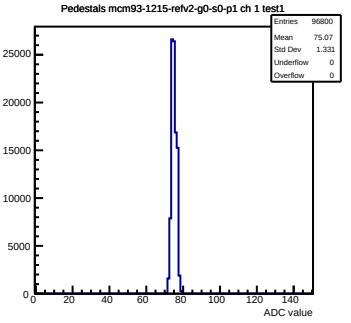
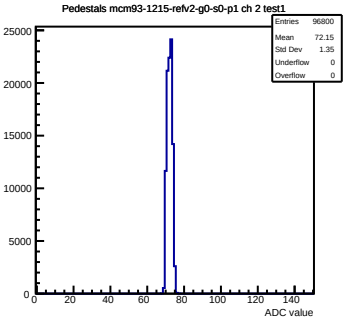
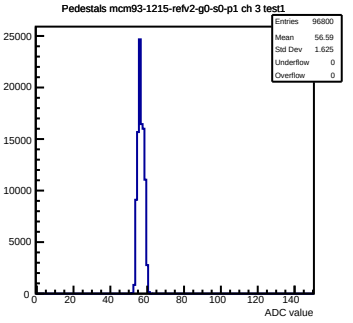
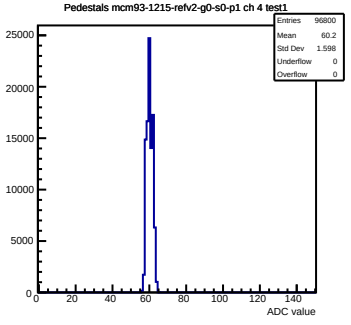
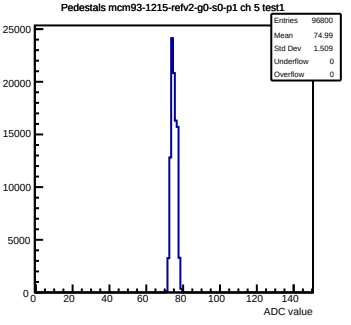

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 0 test1
Entries 96800
Mean 72.52
Std Dev 1.917
Underflow 0
Overflow 0
ADC value

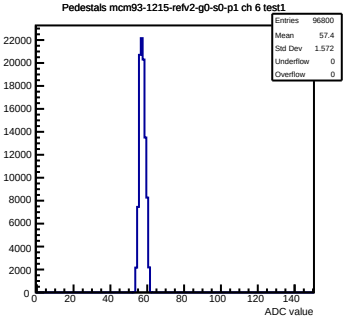

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 1 test1
Entries 96800
Mean 75.07
Std Dev 1.331
Underflow 0
Overflow 0
ADC value

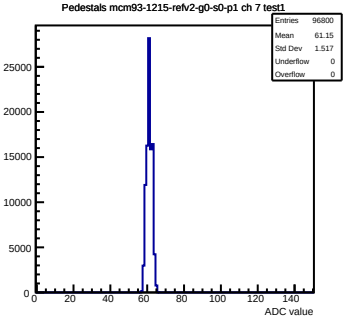

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 2 test1
Entries 96800
Mean 72.15
Std Dev 1.35
Underflow 0
Overflow 0
ADC value

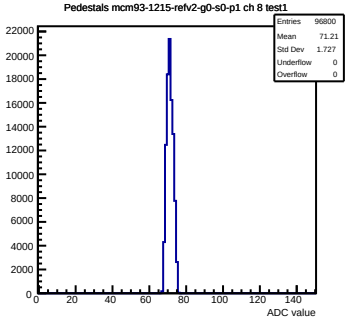

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 3 test1
Entries 96800
Mean 56.59
Std Dev 1.625
Underflow 0
Overflow 0
ADC value

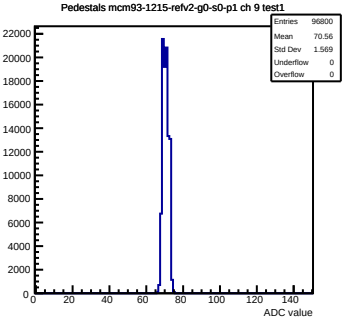

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 4 test1
Entries 96800
Mean 60.2
Std Dev 1.598
Underflow 0
Overflow 0
ADC value

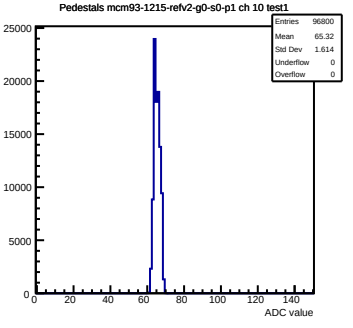

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 5 test1
Entries 96800
Mean 74.99
Std Dev 1.509
Underflow 0
Overflow 0
ADC value

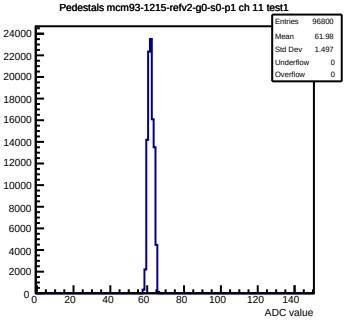

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 6 test1
Entries 96800
Mean 57.4
Std Dev 1.572
Underflow 0
Overflow 0
ADC value

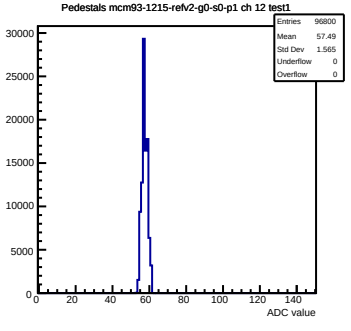

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 7 test1
Entries 96800
Mean 61.15
Std Dev 1.517
Underflow 0
Overflow 0
ADC value

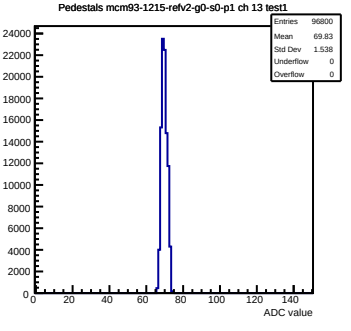

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 8 test1
Entries 96800
Mean 71.21
Std Dev 1.727
Underflow 0
Overflow 0
ADC value

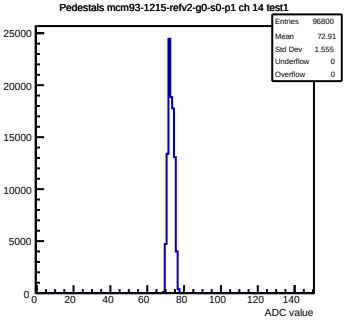

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 9 test1
Entries 96800
Mean 70.56
Std Dev 1.569
Underflow 0
Overflow 0
ADC value

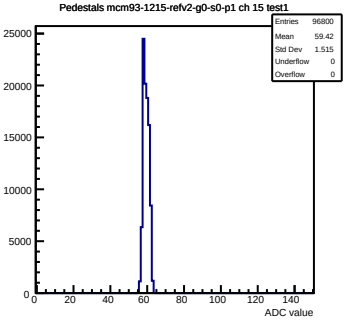

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 10 test1
Entries 96800
Mean 65.32
Std Dev 1.614
Underflow 0
Overflow 0
ADC value

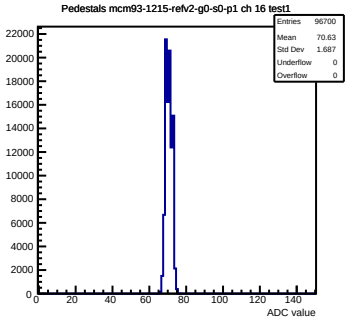

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 11 test1
Entries 96800
Mean 61.98
Std Dev 1.497
Underflow 0
Overflow 0
ADC value

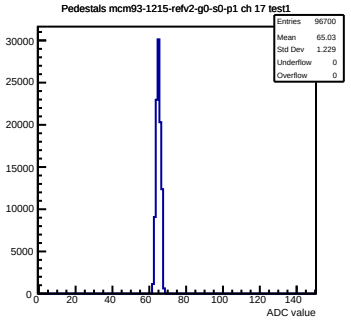

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 12 test1
Entries 96800
Mean 57.49
Std Dev 1.565
Underflow 0
Overflow 0
ADC value

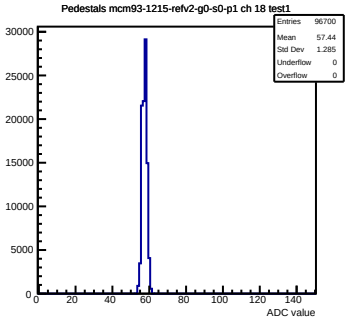

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 13 test1
Entries 96800
Mean 69.83
Std Dev 1.538
Underflow 0
Overflow 0
ADC value

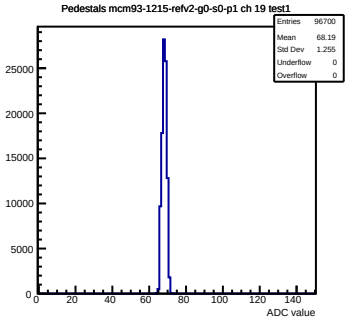

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 14 test1
Entries 96800
Mean 72.91
Std Dev 1.555
Underflow 0
Overflow 0
ADC value

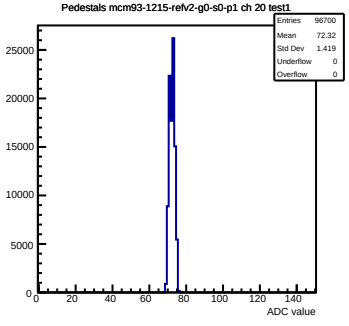

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 15 test1
Entries 96800
Mean 59.42
Std Dev 1.515
Underflow 0
Overflow 0
ADC value

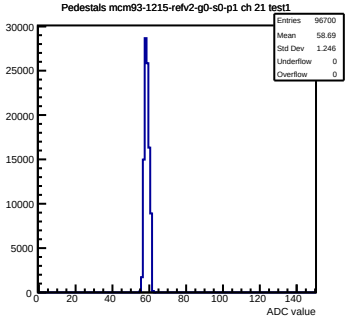

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 16 test1
Entries 96700
Mean 70.63
Std Dev 1.687
Underflow 0
Overflow 0
ADC value

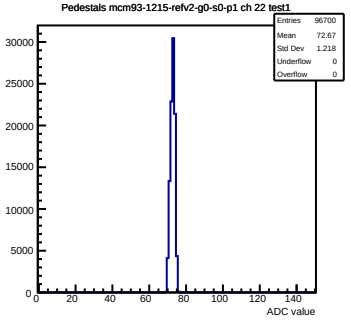

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 17 test1
Entries 96700
Mean 65.03
Std Dev 1.229
Underflow 0
Overflow 0
ADC value

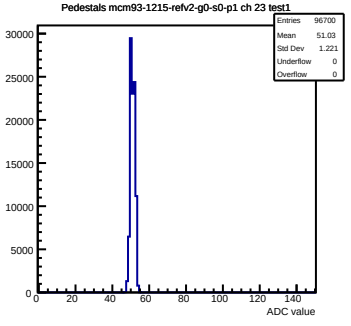

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 18 test1
Entries 96700
Mean 57.44
Std Dev 1.285
Underflow 0
Overflow 0
ADC value

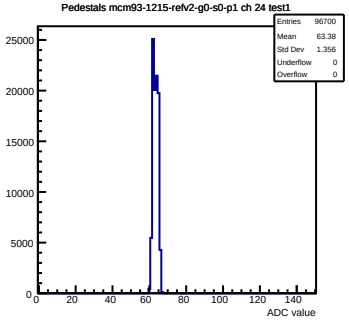

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 19 test1
Entries 96700
Mean 68.19
Std Dev 1.255
Underflow 0
Overflow 0
ADC value

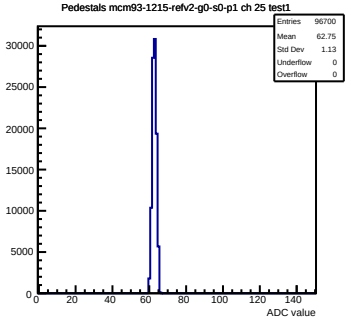

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 20 test1
Entries 96700
Mean 72.32
Std Dev 1.419
Underflow 0
Overflow 0
ADC value

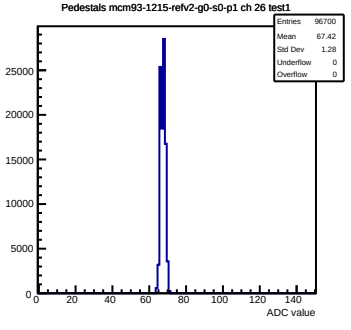

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 21 test1
Entries 96700
Mean 58.69
Std Dev 1.246
Underflow 0
Overflow 0
ADC value

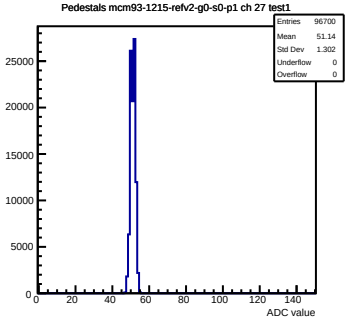

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 22 test1
Entries 96700
Mean 72.67
Std Dev 1.218
Underflow 0
Overflow 0
ADC value

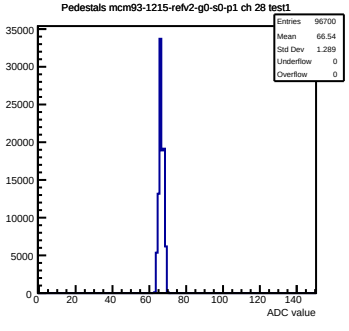

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 23 test1
Entries 96700
Mean 51.03
Std Dev 1.221
Underflow 0
Overflow 0
ADC value

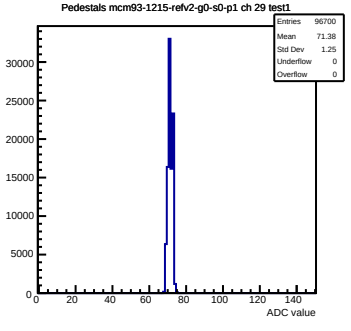

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 24 test1
Entries 96700
Mean 63.38
Std Dev 1.356
Underflow 0
Overflow 0
ADC value

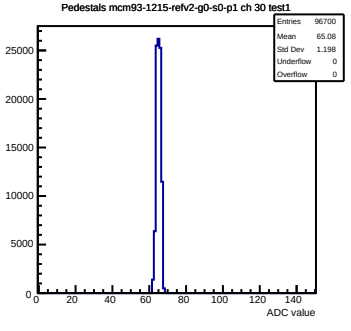

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 25 test1
Entries 96700
Mean 62.75
Std Dev 1.13
Underflow 0
Overflow 0
ADC value

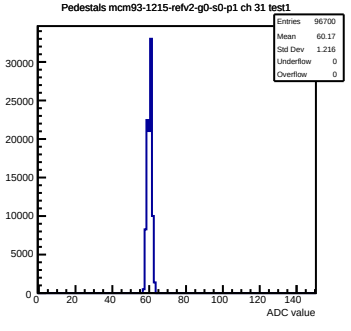

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 26 test1
Entries 96700
Mean 67.42
Std Dev 1.28
Underflow 0
Overflow 0
ADC value

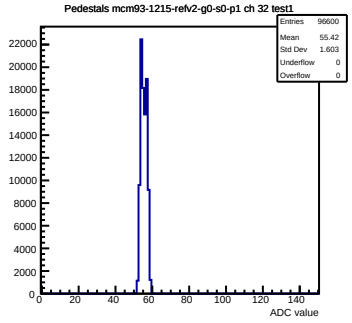

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 27 test1
Entries 96700
Mean 51.14
Std Dev 1.302
Underflow 0
Overflow 0
ADC value

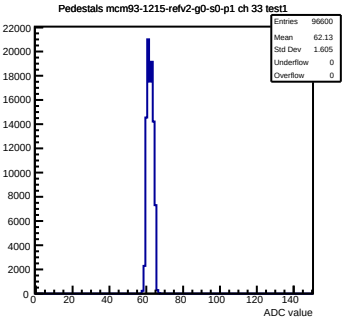

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 28 test1
Entries 96700
Mean 66.54
Std Dev 1.289
Underflow 0
Overflow 0
ADC value

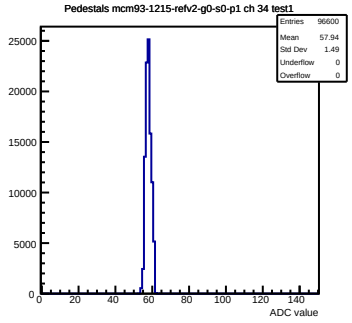

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 29 test1
Entries 96700
Mean 71.38
Std Dev 1.25
Underflow 0
Overflow 0
ADC value

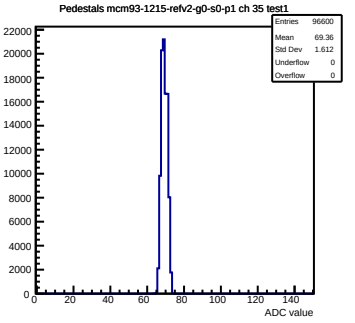

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 30 test1
Entries 96700
Mean 65.08
Std Dev 1.198
Underflow 0
Overflow 0
ADC value

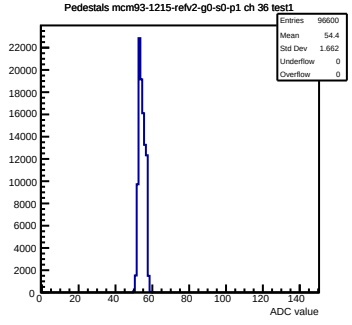

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 31 test1
Entries 96700
Mean 60.17
Std Dev 1.216
Underflow 0
Overflow 0
ADC value

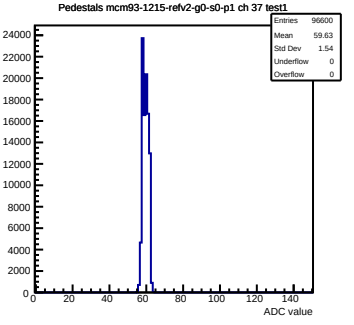

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 32 test1
Entries 96600
Mean 55.42
Std Dev 1.603
Underflow 0
Overflow 0
ADC value

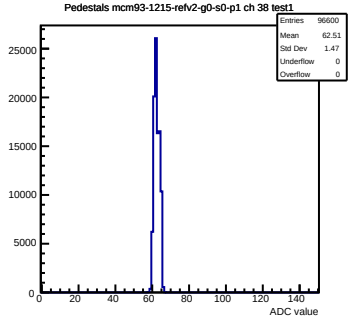

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 33 test1
Entries 96600
Mean 62.13
Std Dev 1.605
Underflow 0
Overflow 0
ADC value

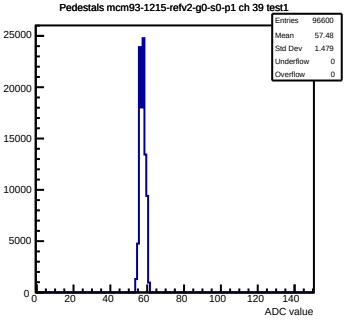

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 34 test1
Entries 96600
Mean 57.94
Std Dev 1.49
Underflow 0
Overflow 0
ADC value

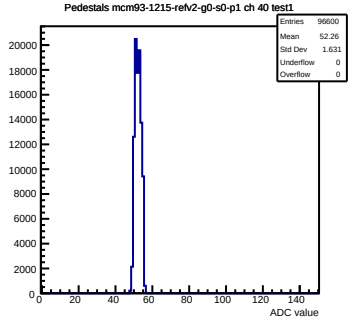

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 35 test1
Entries 96600
Mean 69.36
Std Dev 1.612
Underflow 0
Overflow 0
ADC value

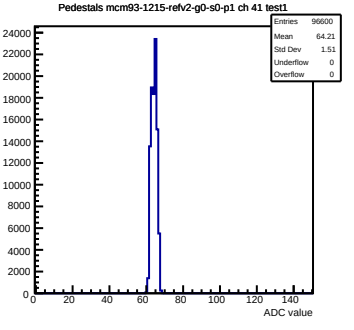

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 36 test1
Entries 96600
Mean 54.4
Std Dev 1.662
Underflow 0
Overflow 0
ADC value

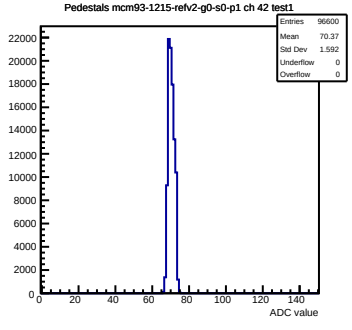

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 37 test1
Entries 96600
Mean 59.63
Std Dev 1.54
Underflow 0
Overflow 0
ADC value

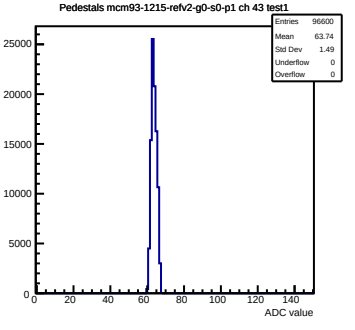

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 38 test1
Entries 96600
Mean 62.51
Std Dev 1.47
Underflow 0
Overflow 0
ADC value

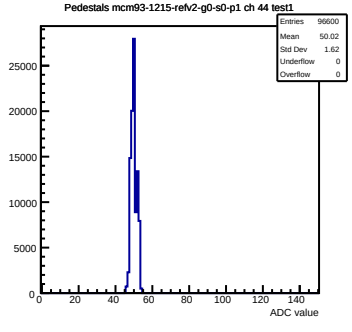

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 39 test1
Entries 96600
Mean 57.48
Std Dev 1.479
Underflow 0
Overflow 0
ADC value

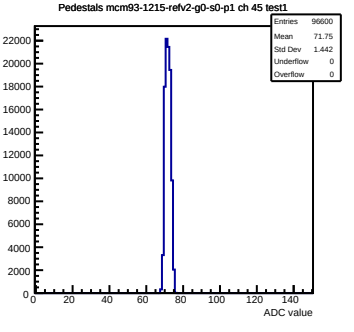

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 40 test1
Entries 96600
Mean 52.26
Std Dev 1.631
Underflow 0
Overflow 0
ADC value

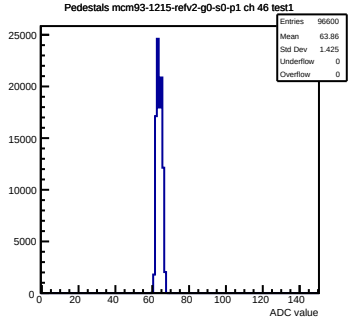

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 41 test1
Entries 96600
Mean 64.21
Std Dev 1.51
Underflow 0
Overflow 0
ADC value

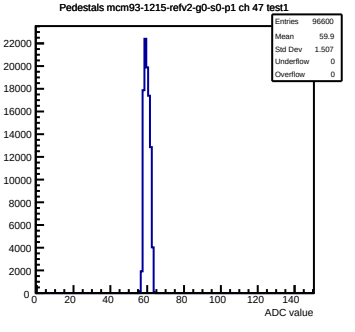

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 42 test1
Entries 96600
Mean 70.37
Std Dev 1.592
Underflow 0
Overflow 0
ADC value

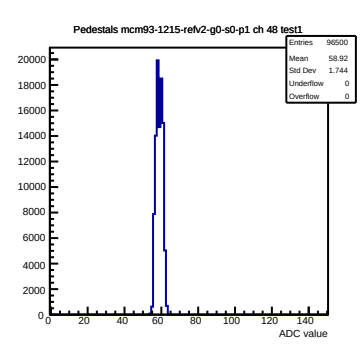

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 43 test1
Entries 96600
Mean 63.74
Std Dev 1.49
Underflow 0
Overflow 0
ADC value

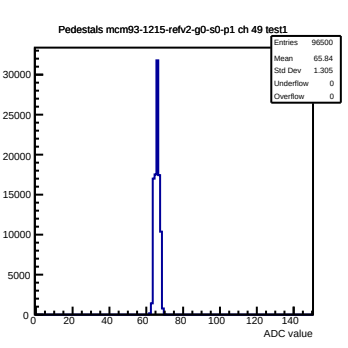

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 44 test1
Entries 96600
Mean 50.02
Std Dev 1.62
Underflow 0
Overflow 0
ADC value

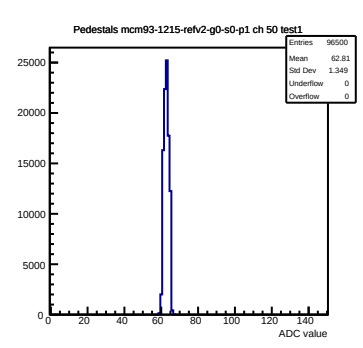

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 45 test1
Entries 96600
Mean 71.75
Std Dev 1.442
Underflow 0
Overflow 0
ADC value

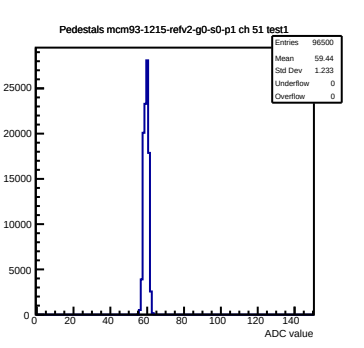

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 46 test1
Entries 96600
Mean 63.86
Std Dev 1.425
Underflow 0
Overflow 0
ADC value

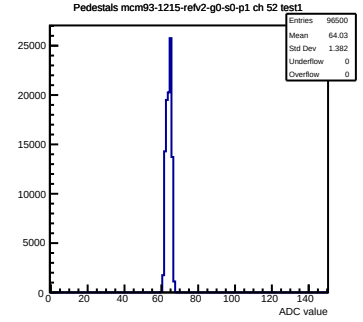

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 47 test1
Entries 96600
Mean 59.9
Std Dev 1.507
Underflow 0
Overflow 0
ADC value

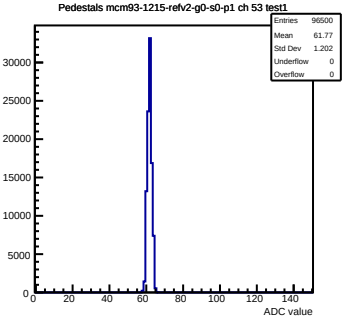

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 48 test1
Entries 96500
Mean 58.92
Std Dev 1.744
Underflow 0
Overflow 0
ADC value

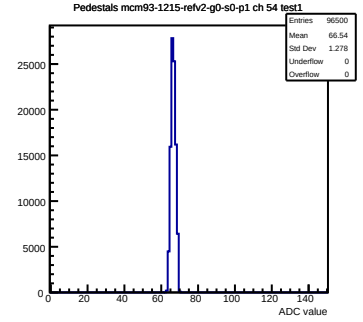

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 49 test1
Entries 96500
Mean 65.84
Std Dev 1.305
Underflow 0
Overflow 0
ADC value

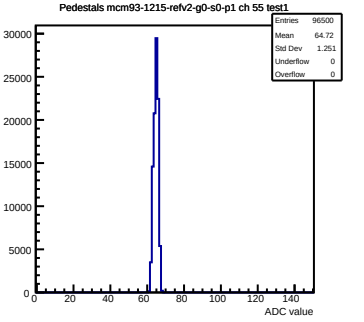

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 50 test1
Entries 96500
Mean 62.81
Std Dev 1.349
Underflow 0
Overflow 0
ADC value

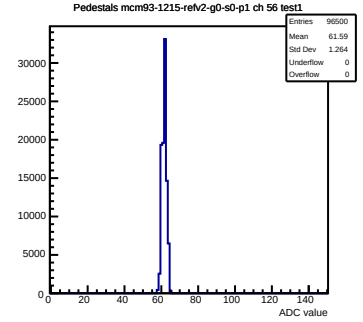

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 51 test1
Entries 96500
Mean 59.44
Std Dev 1.233
Underflow 0
Overflow 0
ADC value

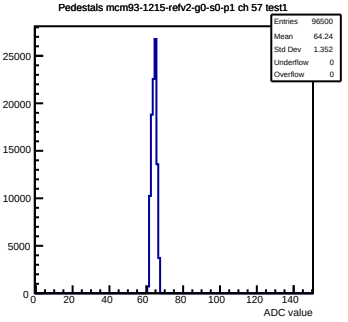

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 52 test1
Entries 96500
Mean 64.03
Std Dev 1.382
Underflow 0
Overflow 0
ADC value

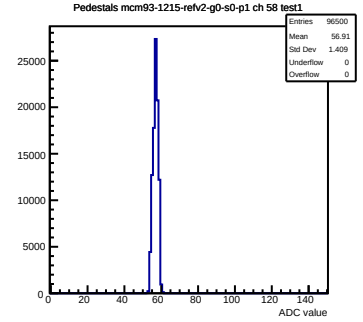

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 53 test1
Entries 96500
Mean 61.77
Std Dev 1.202
Underflow 0
Overflow 0
ADC value

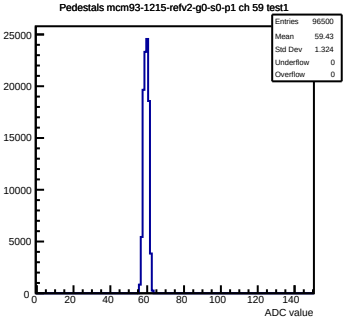

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 54 test1
Entries 96500
Mean 66.54
Std Dev 1.278
Underflow 0
Overflow 0
ADC value

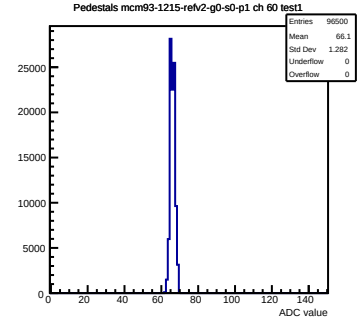

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 55 test1
Entries 96500
Mean 64.72
Std Dev 1.251
Underflow 0
Overflow 0
ADC value

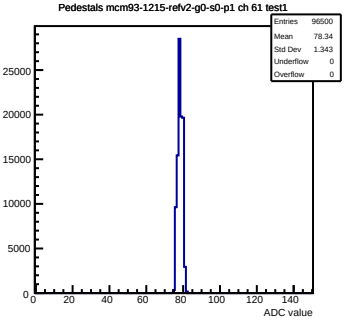

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 56 test1
Entries 96500
Mean 61.59
Std Dev 1.264
Underflow 0
Overflow 0
ADC value

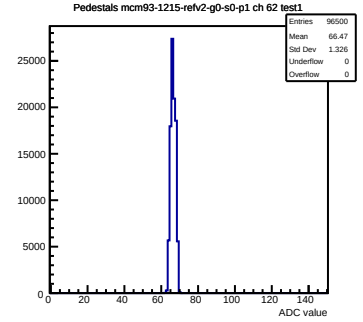

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 57 test1
Entries 96500
Mean 64.24
Std Dev 1.352
Underflow 0
Overflow 0
ADC value

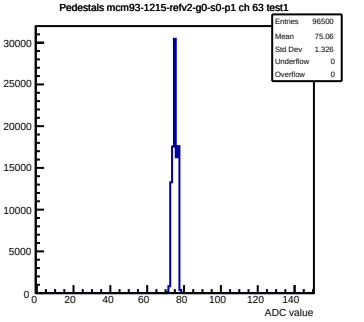

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 58 test1
Entries 96500
Mean 56.91
Std Dev 1.409
Underflow 0
Overflow 0
ADC value

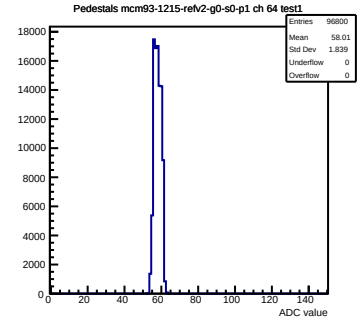

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 59 test1
Entries 96500
Mean 59.43
Std Dev 1.324
Underflow 0
Overflow 0
ADC value

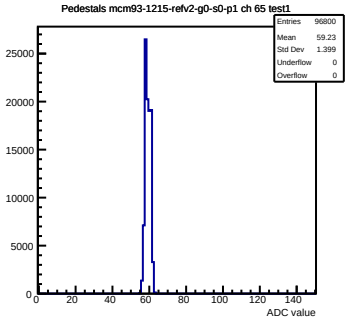

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 60 test1
Entries 96500
Mean 66.1
Std Dev 1.282
Underflow 0
Overflow 0
ADC value

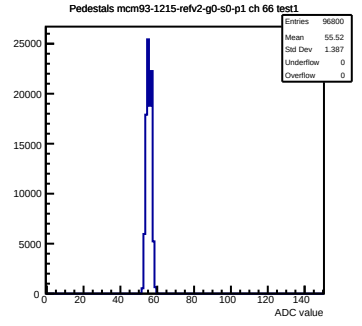

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 61 test1
Entries 96500
Mean 78.34
Std Dev 1.343
Underflow 0
Overflow 0
ADC value

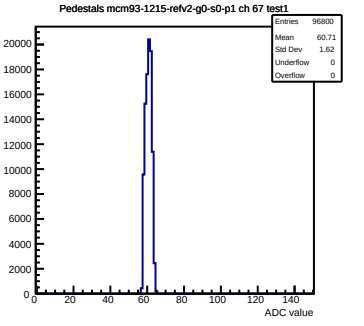

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 62 test1
Entries 96500
Mean 66.47
Std Dev 1.326
Underflow 0
Overflow 0
ADC value

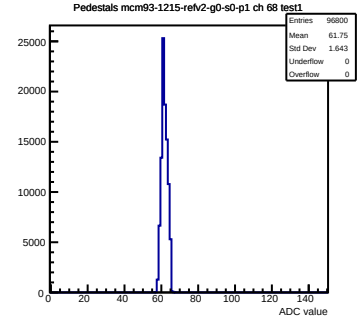

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 63 test1
Entries 96500
Mean 75.06
Std Dev 1.326
Underflow 0
Overflow 0
ADC value

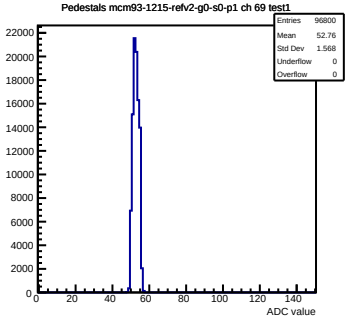

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 64 test1
Entries 96800
Mean 58.01
Std Dev 1.839
Underflow 0
Overflow 0
ADC value

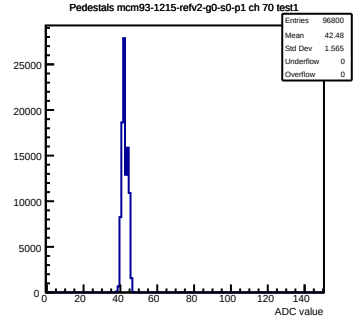

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 65 test1
Entries 96800
Mean 59.23
Std Dev 1.399
Underflow 0
Overflow 0
ADC value

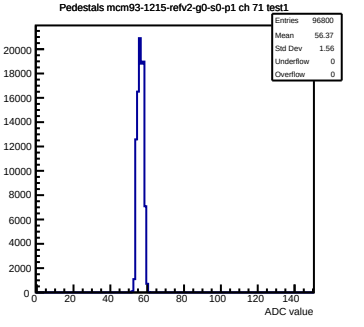

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 66 test1
Entries 96800
Mean 55.52
Std Dev 1.387
Underflow 0
Overflow 0
ADC value

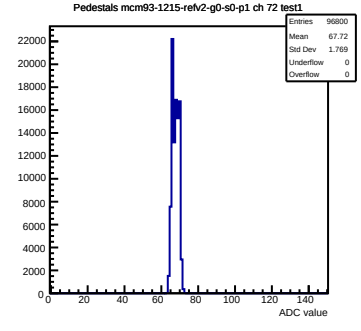

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 67 test1
Entries 96800
Mean 60.71
Std Dev 1.62
Underflow 0
Overflow 0
ADC value

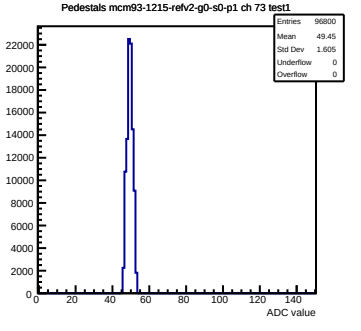

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 68 test1
Entries 96800
Mean 61.75
Std Dev 1.643
Underflow 0
Overflow 0
ADC value

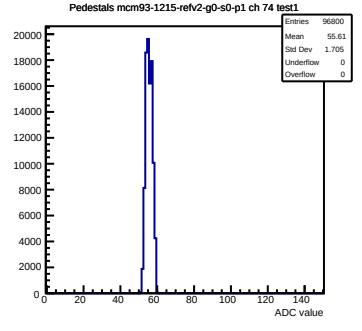

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 69 test1
Entries 96800
Mean 52.76
Std Dev 1.568
Underflow 0
Overflow 0
ADC value

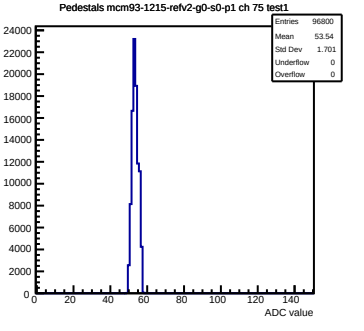

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 70 test1
Entries 96800
Mean 42.48
Std Dev 1.565
Underflow 0
Overflow 0
ADC value

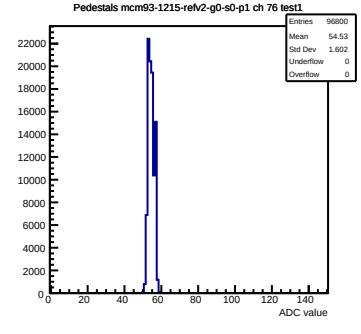

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 71 test1
Entries 96800
Mean 56.37
Std Dev 1.56
Underflow 0
Overflow 0
ADC value

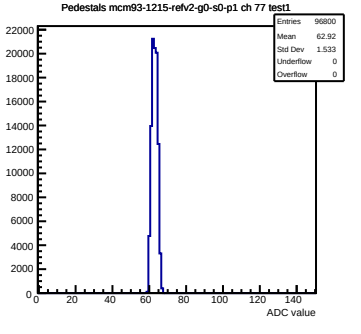

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 72 test1
Entries 96800
Mean 67.72
Std Dev 1.769
Underflow 0
Overflow 0
ADC value

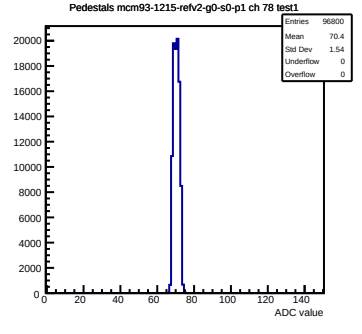

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 73 test1
Entries 96800
Mean 49.45
Std Dev 1.605
Underflow 0
Overflow 0
ADC value

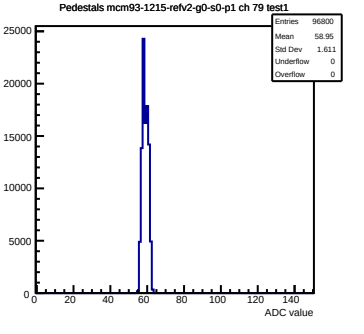

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 74 test1
Entries 96800
Mean 55.61
Std Dev 1.705
Underflow 0
Overflow 0
ADC value

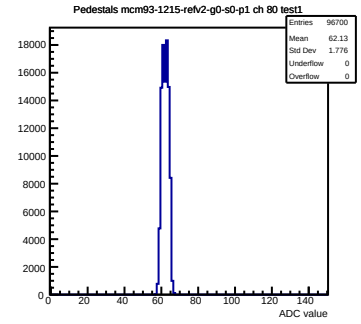

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 75 test1
Entries 96800
Mean 53.54
Std Dev 1.701
Underflow 0
Overflow 0
ADC value

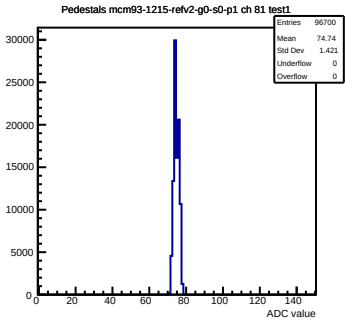

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 76 test1
Entries 96800
Mean 54.53
Std Dev 1.602
Underflow 0
Overflow 0
ADC value

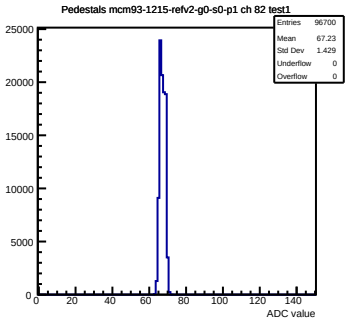

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 77 test1
Entries 96800
Mean 62.92
Std Dev 1.533
Underflow 0
Overflow 0
ADC value

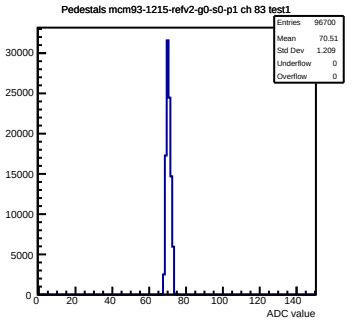

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 78 test1
Entries 96800
Mean 70.4
Std Dev 1.54
Underflow 0
Overflow 0
ADC value

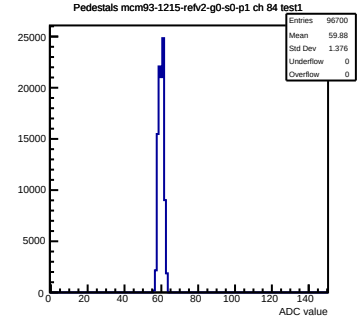

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 79 test1
Entries 96800
Mean 58.95
Std Dev 1.611
Underflow 0
Overflow 0
ADC value

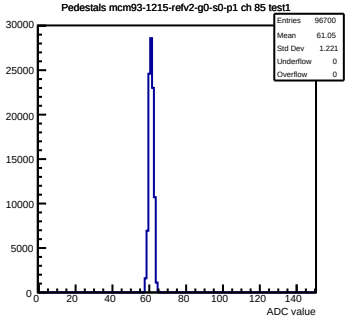

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 80 test1
Entries 96700
Mean 62.13
Std Dev 1.776
Underflow 0
Overflow 0
ADC value

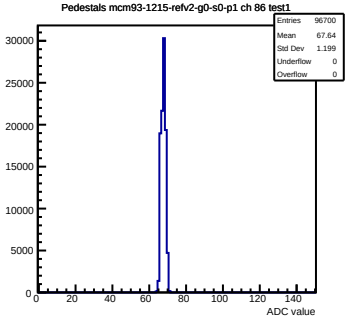

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 81 test1
Entries 96700
Mean 74.74
Std Dev 1.421
Underflow 0
Overflow 0
ADC value

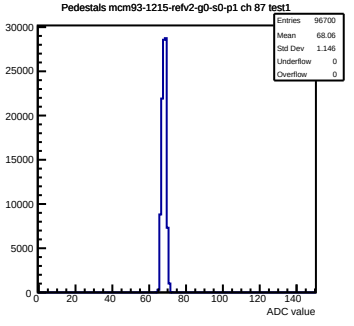

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 82 test1
Entries 96700
Mean 67.23
Std Dev 1.429
Underflow 0
Overflow 0
ADC value

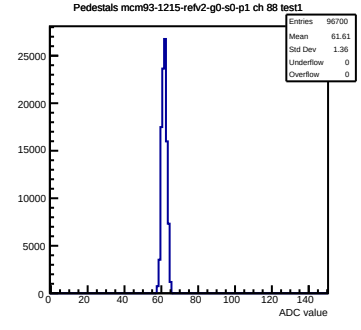

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 83 test1
Entries 96700
Mean 70.51
Std Dev 1.209
Underflow 0
Overflow 0
ADC value

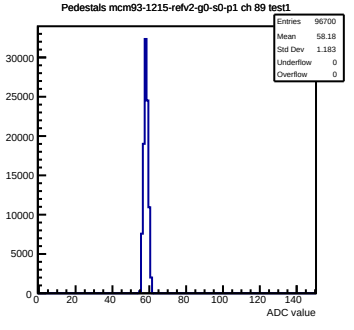

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 84 test1
Entries 96700
Mean 59.88
Std Dev 1.376
Underflow 0
Overflow 0
ADC value

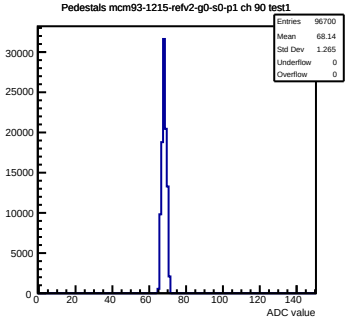

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 85 test1
Entries 96700
Mean 61.05
Std Dev 1.221
Underflow 0
Overflow 0
ADC value

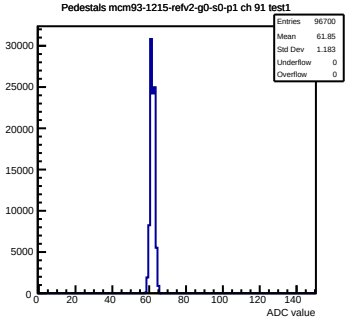

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 86 test1
Entries 96700
Mean 67.64
Std Dev 1.199
Underflow 0
Overflow 0
ADC value

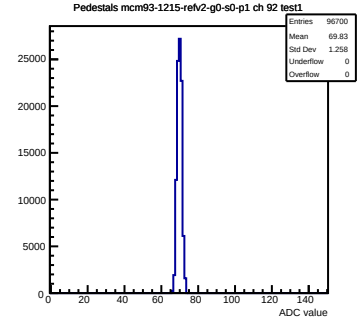

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 87 test1
Entries 96700
Mean 68.06
Std Dev 1.146
Underflow 0
Overflow 0
ADC value

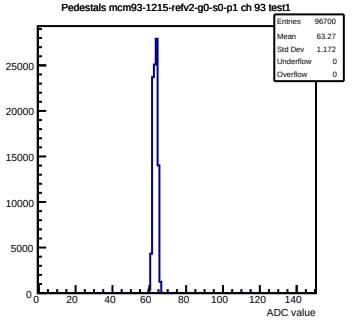

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 88 test1
Entries 96700
Mean 61.61
Std Dev 1.36
Underflow 0
Overflow 0
ADC value

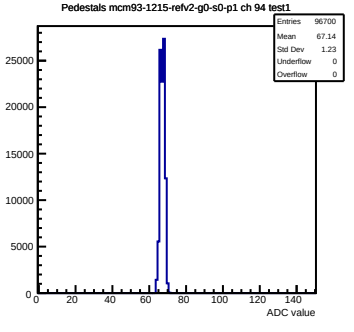

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 89 test1
Entries 96700
Mean 58.18
Std Dev 1.183
Underflow 0
Overflow 0
ADC value

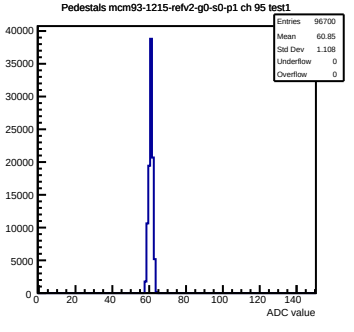

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 90 test1
Entries 96700
Mean 68.14
Std Dev 1.265
Underflow 0
Overflow 0
ADC value

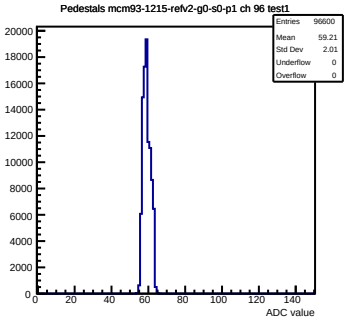

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 91 test1
Entries 96700
Mean 61.85
Std Dev 1.183
Underflow 0
Overflow 0
ADC value

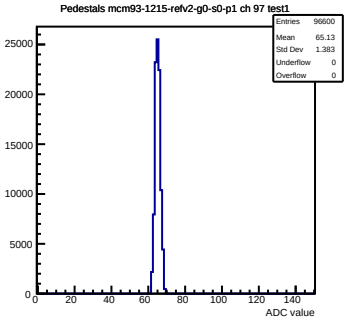

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 92 test1
Entries 96700
Mean 69.83
Std Dev 1.258
Underflow 0
Overflow 0
ADC value

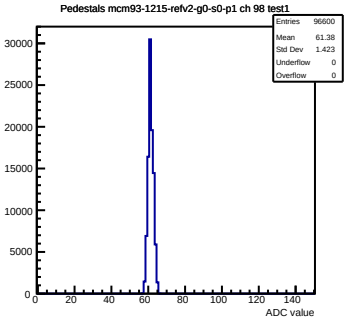

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 93 test1
Entries 96700
Mean 63.27
Std Dev 1.172
Underflow 0
Overflow 0
ADC value

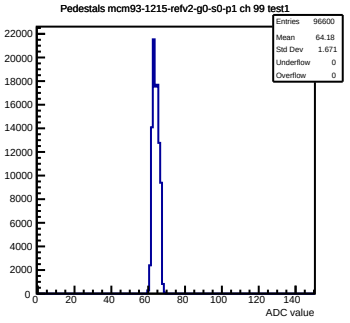

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 94 test1
Entries 96700
Mean 67.14
Std Dev 1.23
Underflow 0
Overflow 0
ADC value

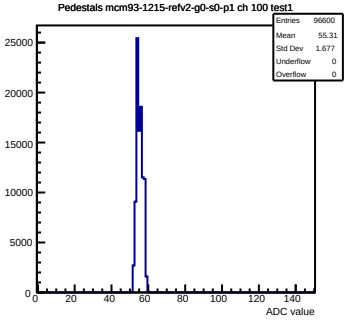

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 95 test1
Entries 96700
Mean 60.85
Std Dev 1.108
Underflow 0
Overflow 0
ADC value

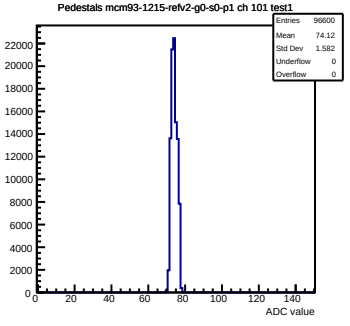

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 96 test1
Entries 96600
Mean 59.21
Std Dev 2.01
Underflow 0
Overflow 0
ADC value

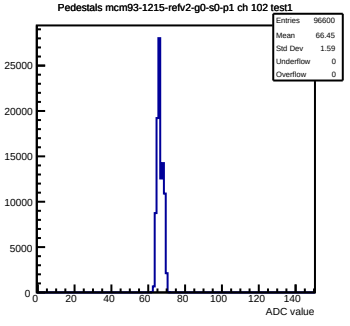

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 97 test1
Entries 96600
Mean 65.13
Std Dev 1.383
Underflow 0
Overflow 0
ADC value

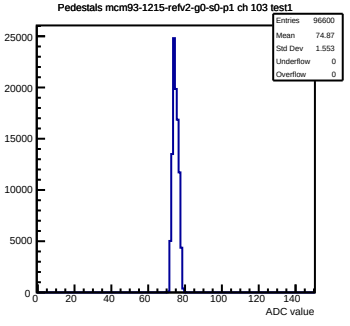

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 98 test1
Entries 96600
Mean 61.38
Std Dev 1.423
Underflow 0
Overflow 0
ADC value

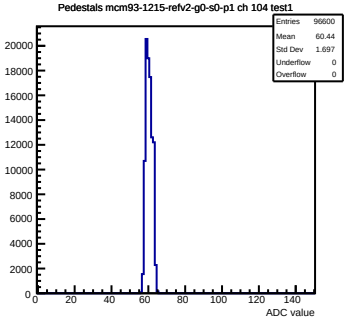

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 99 test1
Entries 96600
Mean 64.18
Std Dev 1.671
Underflow 0
Overflow 0
ADC value

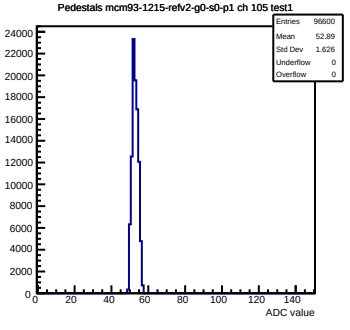

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 100 test1
Entries 96600
Mean 55.31
Std Dev 1.677
Underflow 0
Overflow 0
ADC value

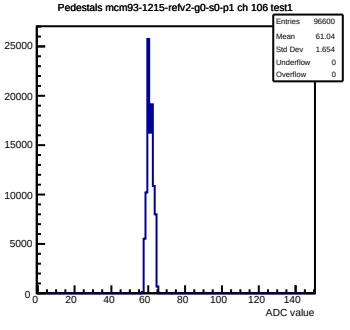

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 101 test1
Entries 96600
Mean 74.12
Std Dev 1.582
Underflow 0
Overflow 0
ADC value

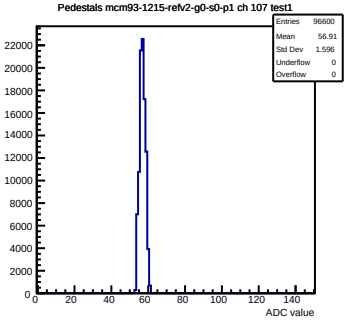

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 102 test1
Entries 96600
Mean 66.45
Std Dev 1.59
Underflow 0
Overflow 0
ADC value

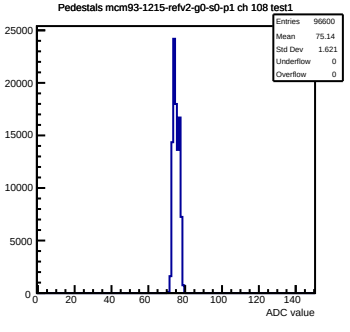

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 103 test1
Entries 96600
Mean 74.87
Std Dev 1.553
Underflow 0
Overflow 0
ADC value

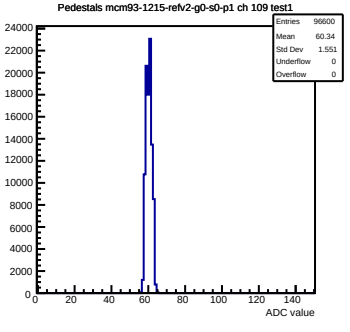

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 104 test1
Entries 96600
Mean 60.44
Std Dev 1.697
Underflow 0
Overflow 0
ADC value

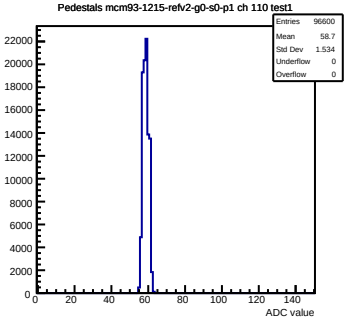

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 105 test1
Entries 96600
Mean 52.89
Std Dev 1.626
Underflow 0
Overflow 0
ADC value

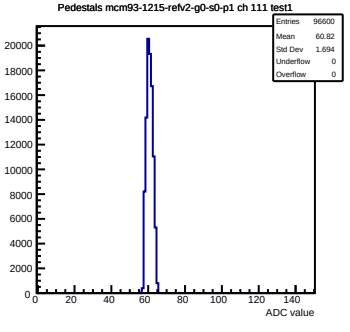

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 106 test1
Entries 96600
Mean 61.04
Std Dev 1.654
Underflow 0
Overflow 0
ADC value

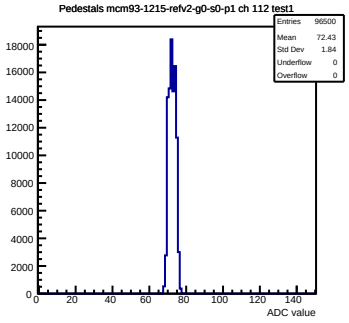

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 107 test1
Entries 96600
Mean 56.91
Std Dev 1.596
Underflow 0
Overflow 0
ADC value

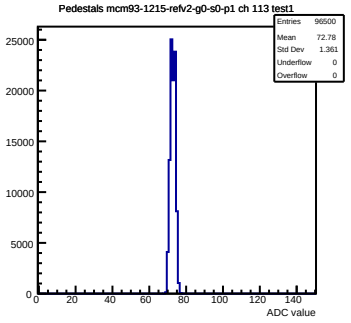

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 108 test1
Entries 96600
Mean 75.14
Std Dev 1.621
Underflow 0
Overflow 0
ADC value

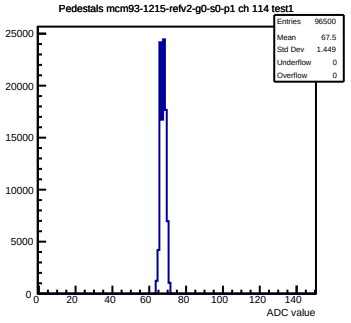

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 109 test1
Entries 96600
Mean 60.34
Std Dev 1.551
Underflow 0
Overflow 0
ADC value

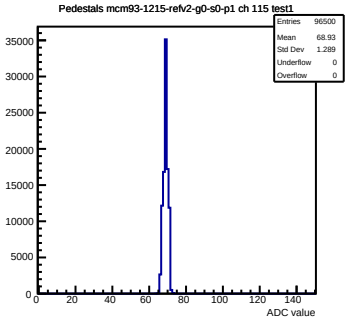

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 110 test1
Entries 96600
Mean 58.7
Std Dev 1.534
Underflow 0
Overflow 0
ADC value

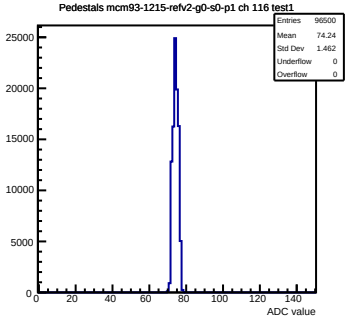

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 111 test1
Entries 96600
Mean 60.82
Std Dev 1.694
Underflow 0
Overflow 0
ADC value

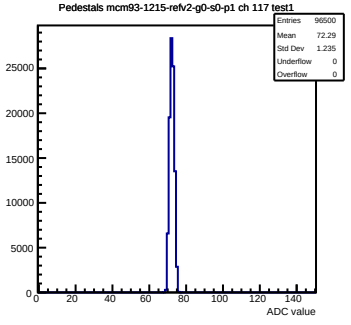

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 112 test1
Entries 96500
Mean 72.43
Std Dev 1.84
Underflow 0
Overflow 0
ADC value

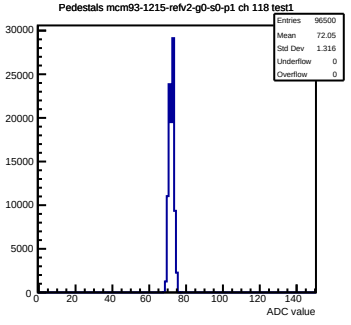

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 113 test1
Entries 96500
Mean 72.78
Std Dev 1.361
Underflow 0
Overflow 0
ADC value

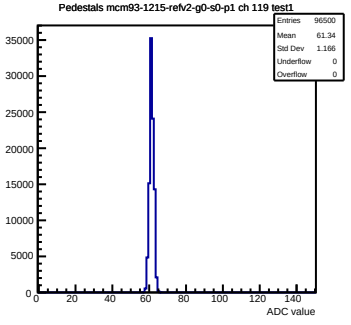

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 114 test1
Entries 96500
Mean 67.5
Std Dev 1.449
Underflow 0
Overflow 0
ADC value

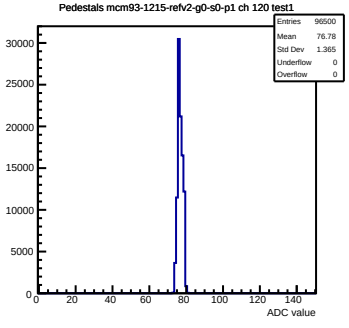

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 115 test1
Entries 96500
Mean 68.93
Std Dev 1.289
Underflow 0
Overflow 0
ADC value

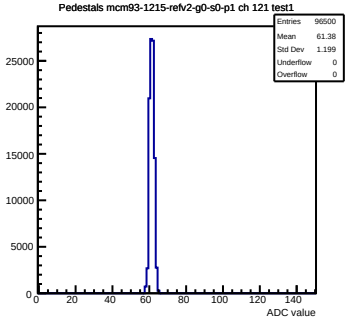

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 116 test1
Entries 96500
Mean 74.24
Std Dev 1.462
Underflow 0
Overflow 0
ADC value

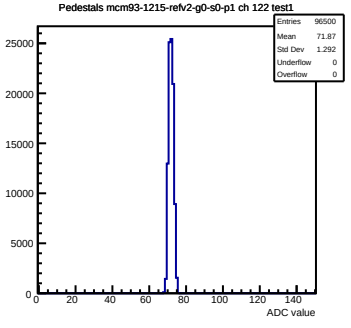

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 117 test1
Entries 96500
Mean 72.29
Std Dev 1.235
Underflow 0
Overflow 0
ADC value

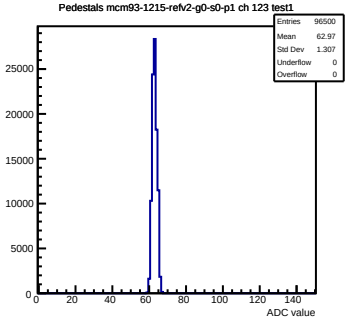

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 118 test1
Entries 96500
Mean 72.05
Std Dev 1.316
Underflow 0
Overflow 0
ADC value

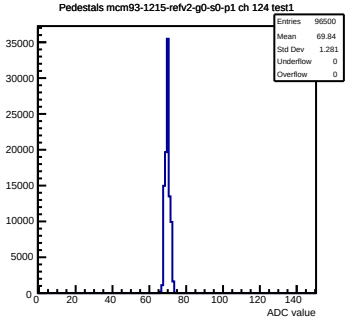

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 119 test1
Entries 96500
Mean 61.34
Std Dev 1.166
Underflow 0
Overflow 0
ADC value

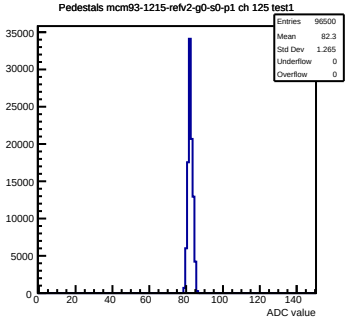

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 120 test1
Entries 96500
Mean 76.78
Std Dev 1.365
Underflow 0
Overflow 0
ADC value

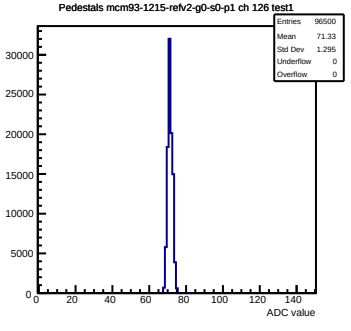

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 121 test1
Entries 96500
Mean 61.38
Std Dev 1.199
Underflow 0
Overflow 0
ADC value

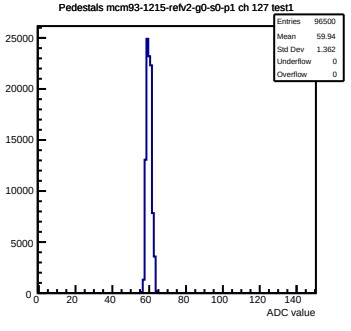

Pedestals mcm93-1215-refv2-g0-s0-p1 ch 122 test1
Entries 96500
Mean 71.87
Std Dev 1.292
Underflow 0
Overflow 0
ADC value


Pedestals mcm93-1215-refv2-g0-s0-p1 ch 123 test1
Entries 96500
Mean 62.97
Std Dev 1.307
Underflow 0
Overflow 0
ADC value


Pedestals mcm93-1215-refv2-g0-s0-p1 ch 124 test1
Entries 96500
Mean 69.84
Std Dev 1.281
Underflow 0
Overflow 0
ADC value


Pedestals mcm93-1215-refv2-g0-s0-p1 ch 125 test1
Entries 96500
Mean 82.3
Std Dev 1.265
Underflow 0
Overflow 0
ADC value


Pedestals mcm93-1215-refv2-g0-s0-p1 ch 126 test1
Entries 96500
Mean 71.33
Std Dev 1.295
Underflow 0
Overflow 0
ADC value


Pedestals mcm93-1215-refv2-g0-s0-p1 ch 127 test1
Entries 96500
Mean 59.94
Std Dev 1.362
Underflow 0
Overflow 0
ADC value