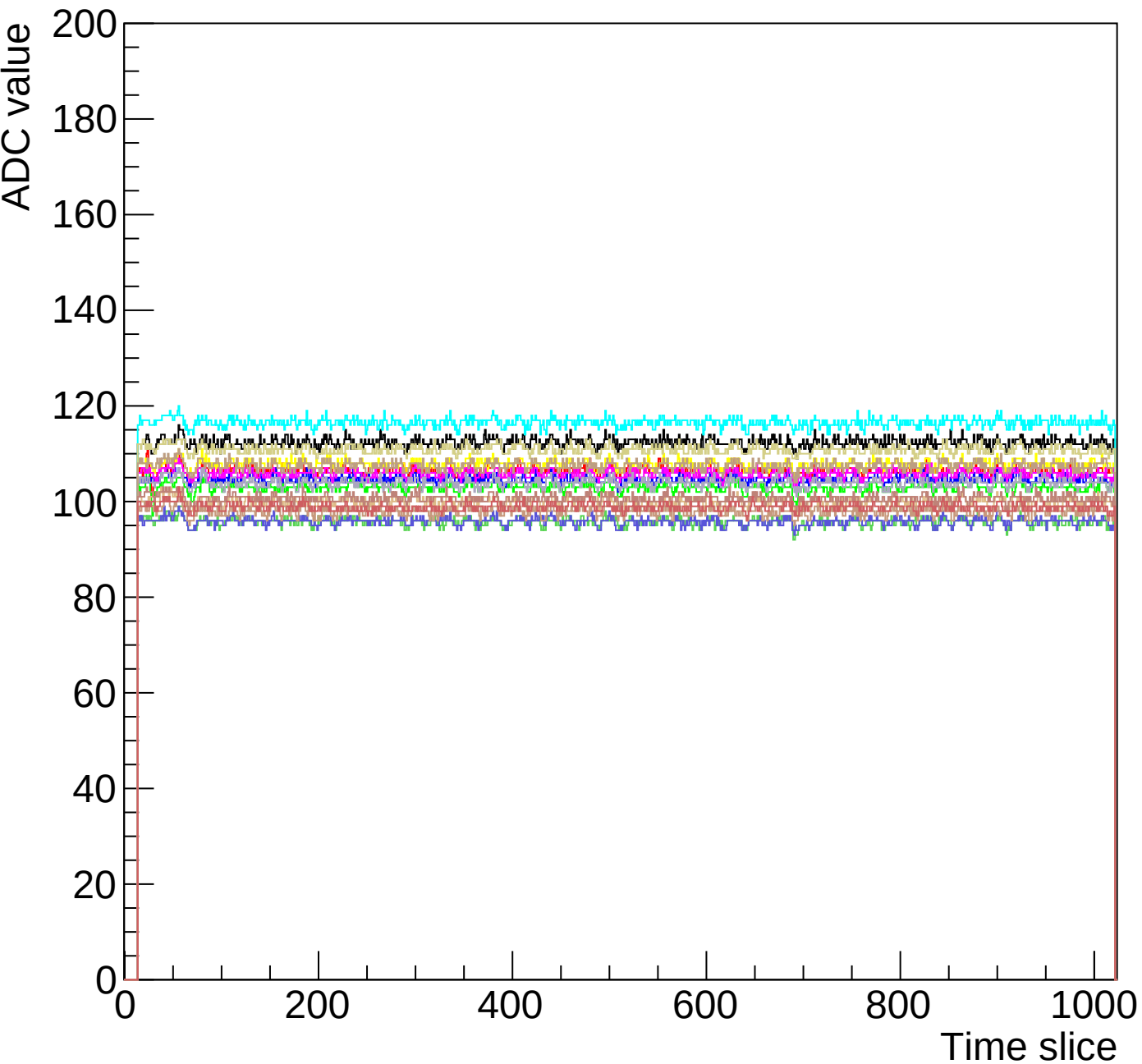
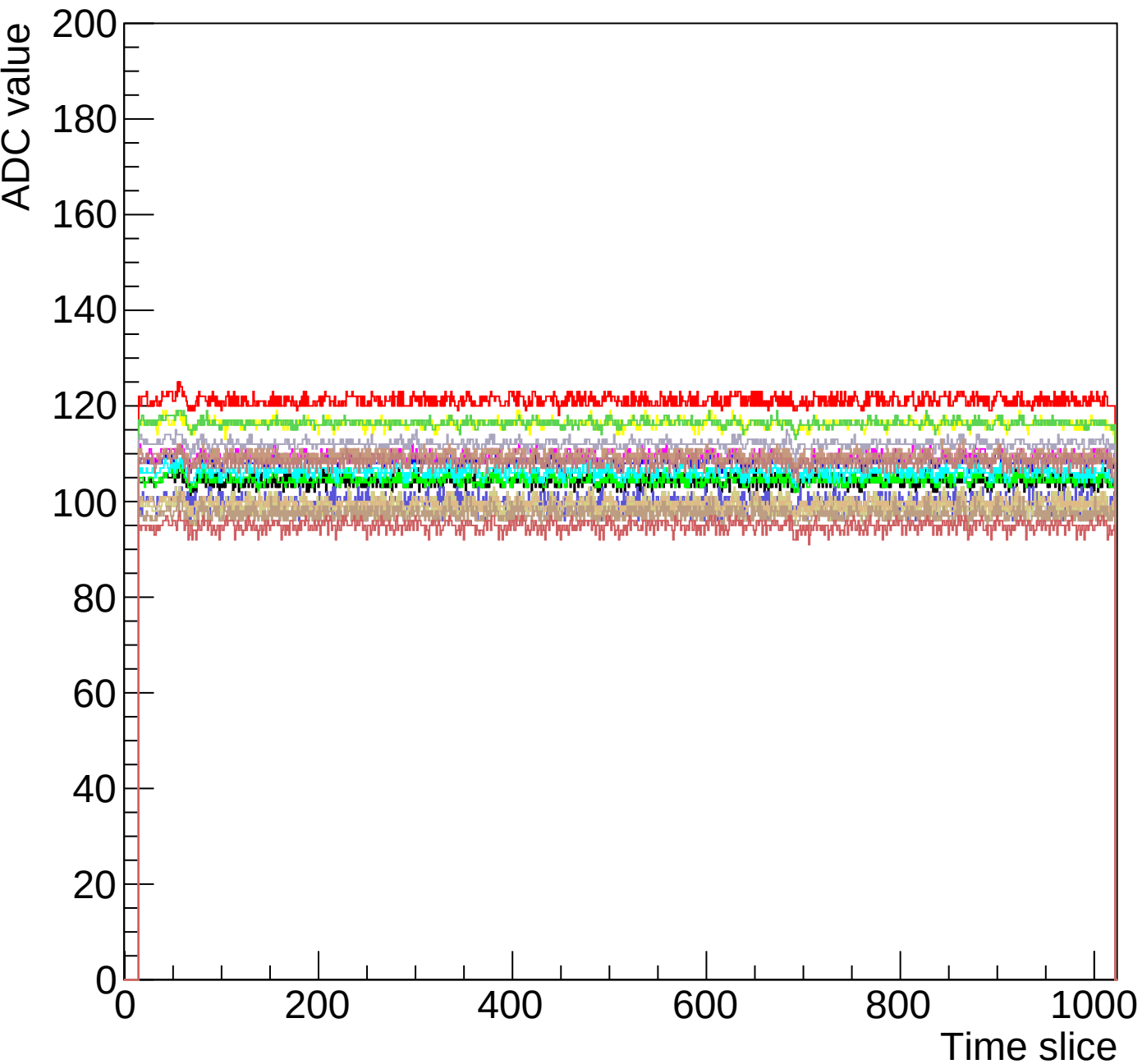


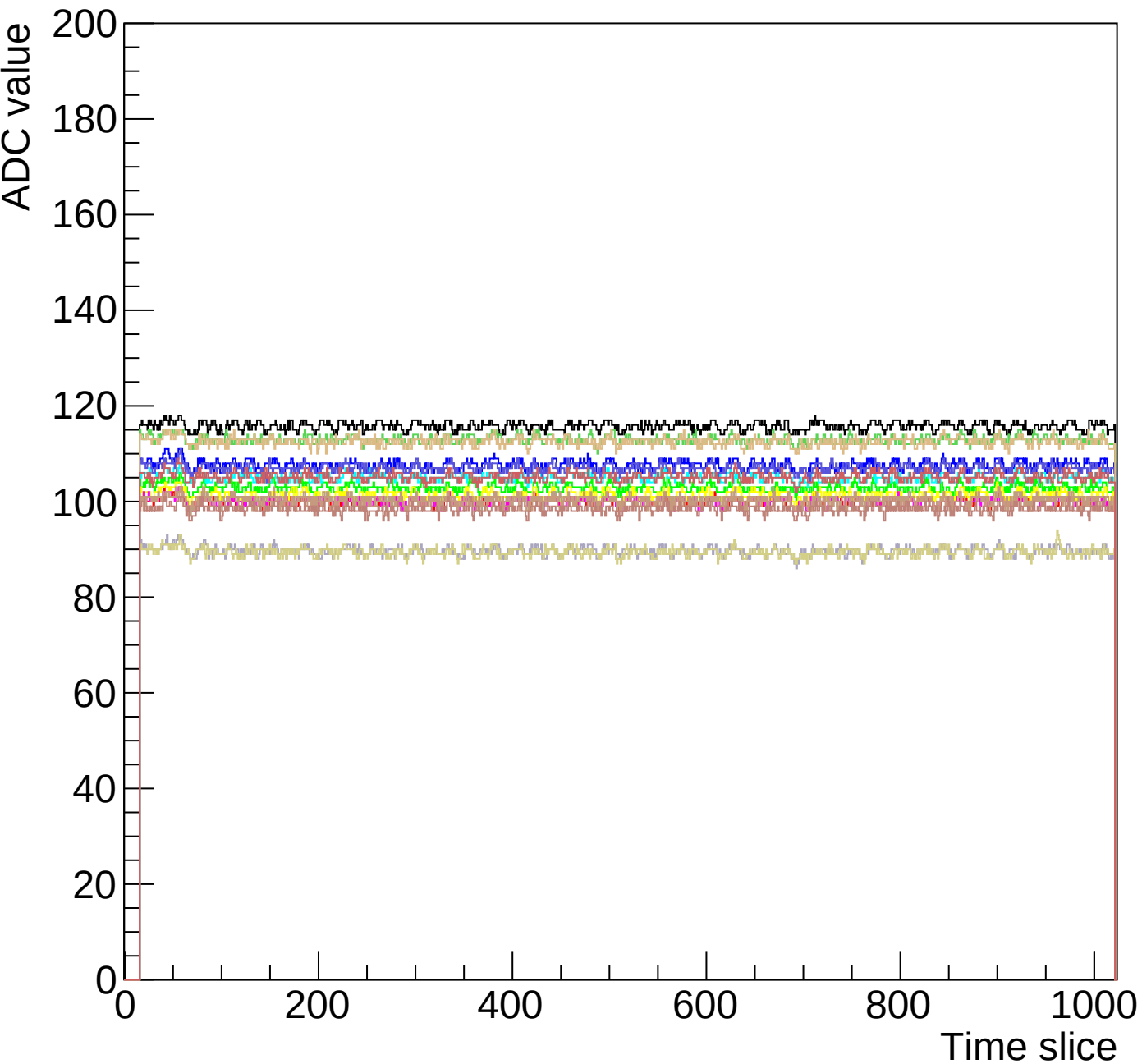
Run 0 event 188 mcm 0 chip 0 chamber



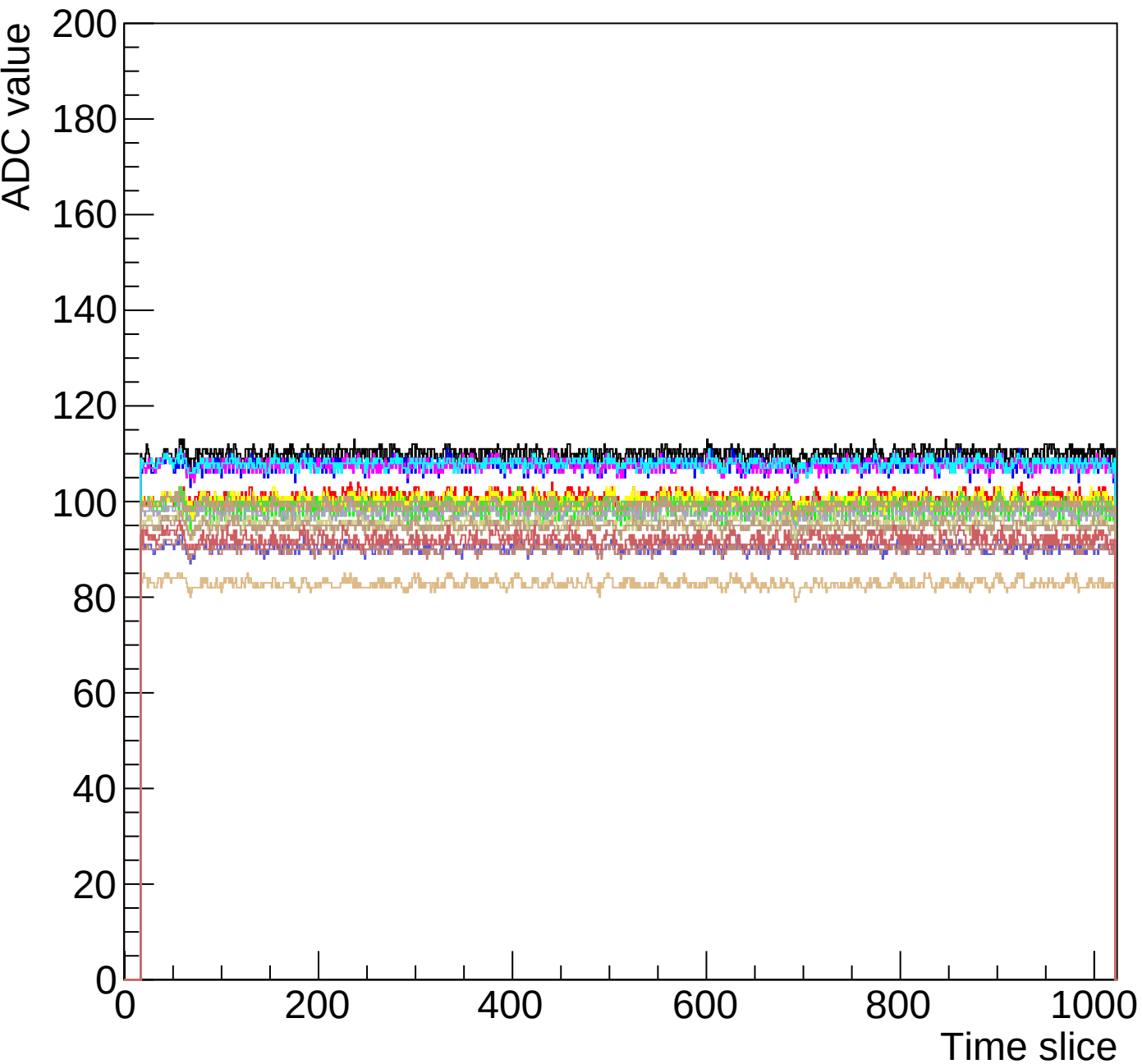
Run 0 event 188 mcm 0 chip 1 chamber



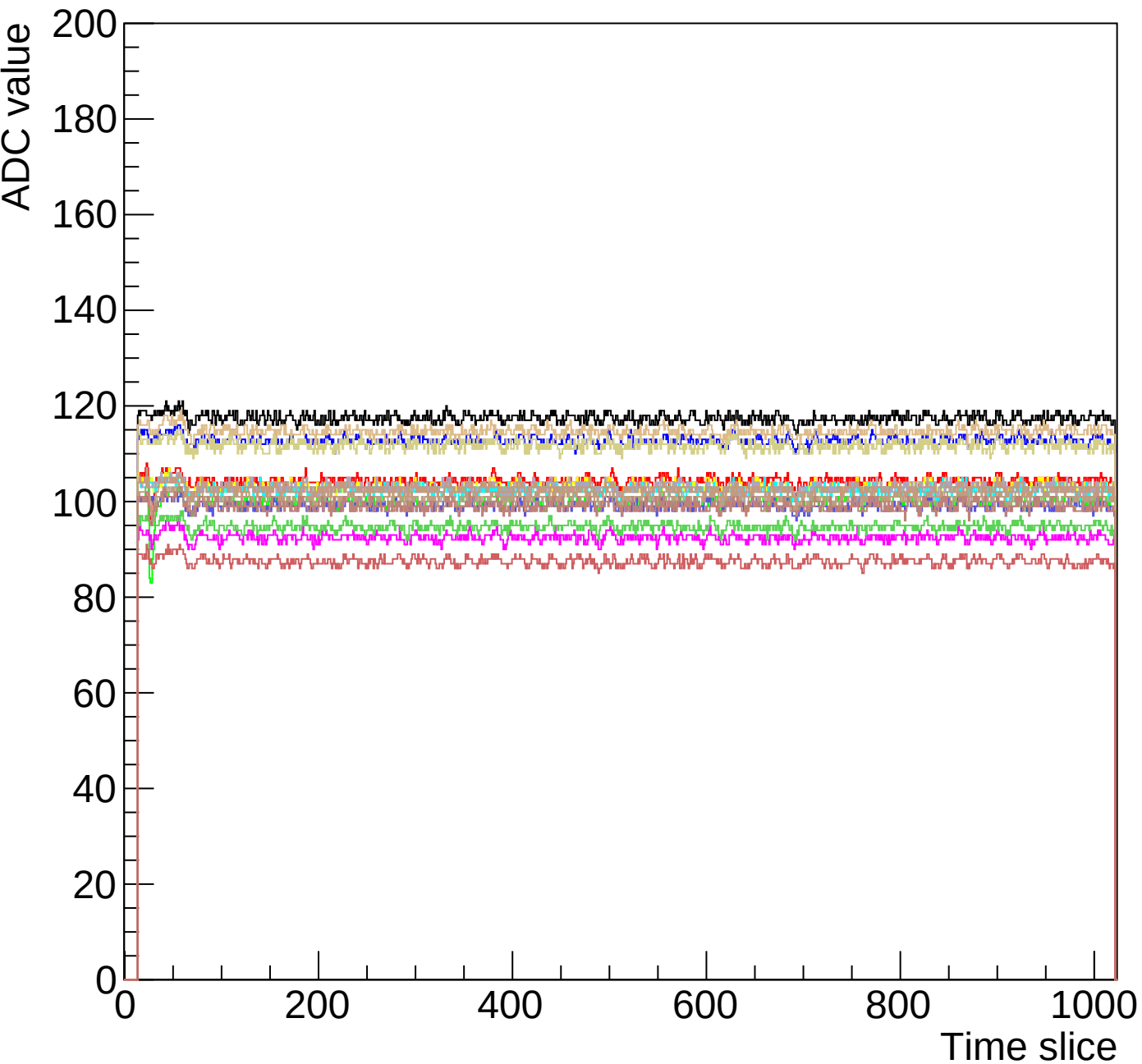
Run 0 event 188 mcm 0 chip 2 chamber



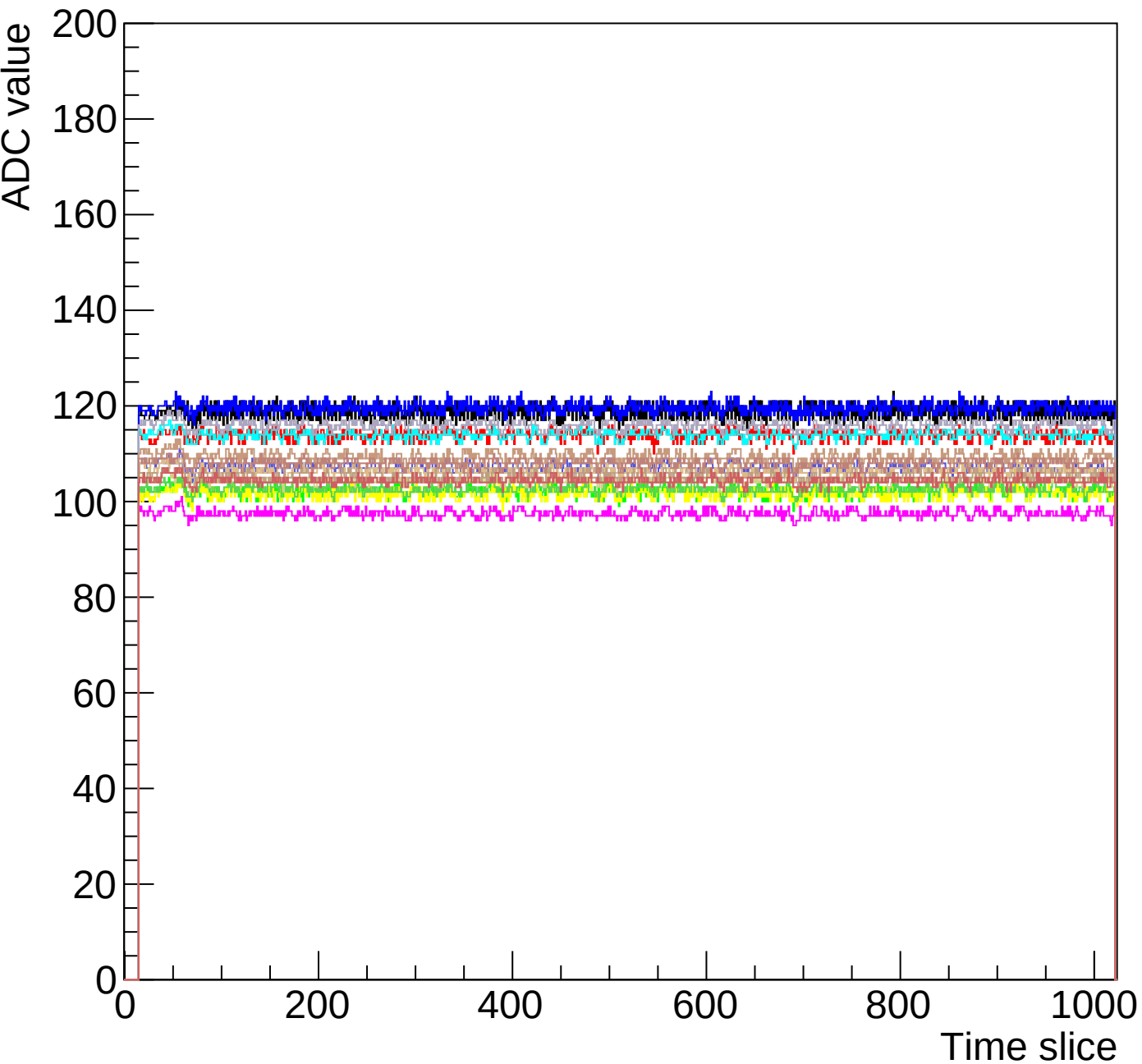
Run 0 event 188 mcm 0 chip 3 chamber



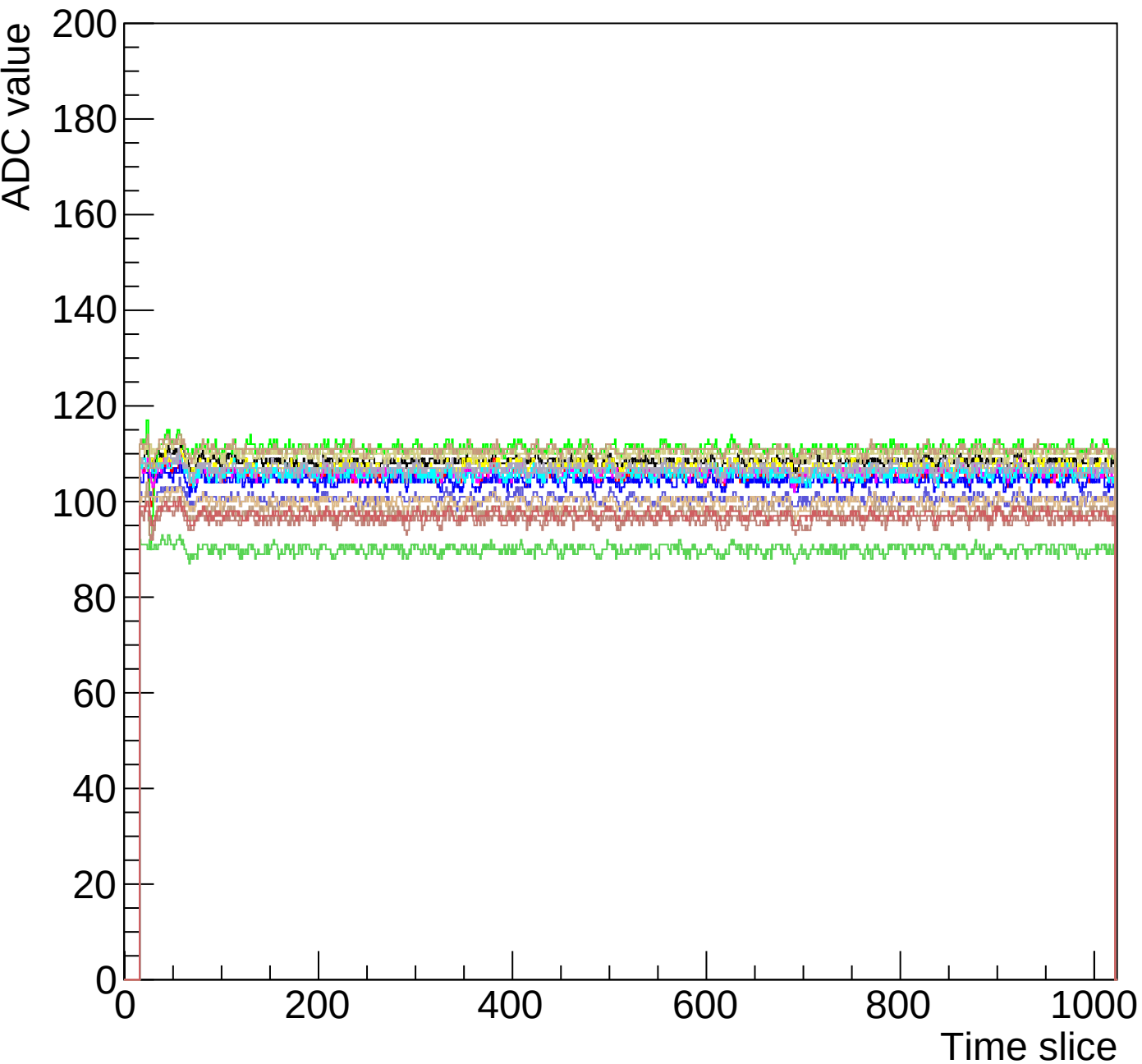
Run 0 event 188 mcm 0 chip 4 chamber



Run 0 event 188 mcm 0 chip 5 chamber



Run 0 event 188 mcm 0 chip 6 chamber



Run 0 event 188 mcm 0 chip 7 chamber

