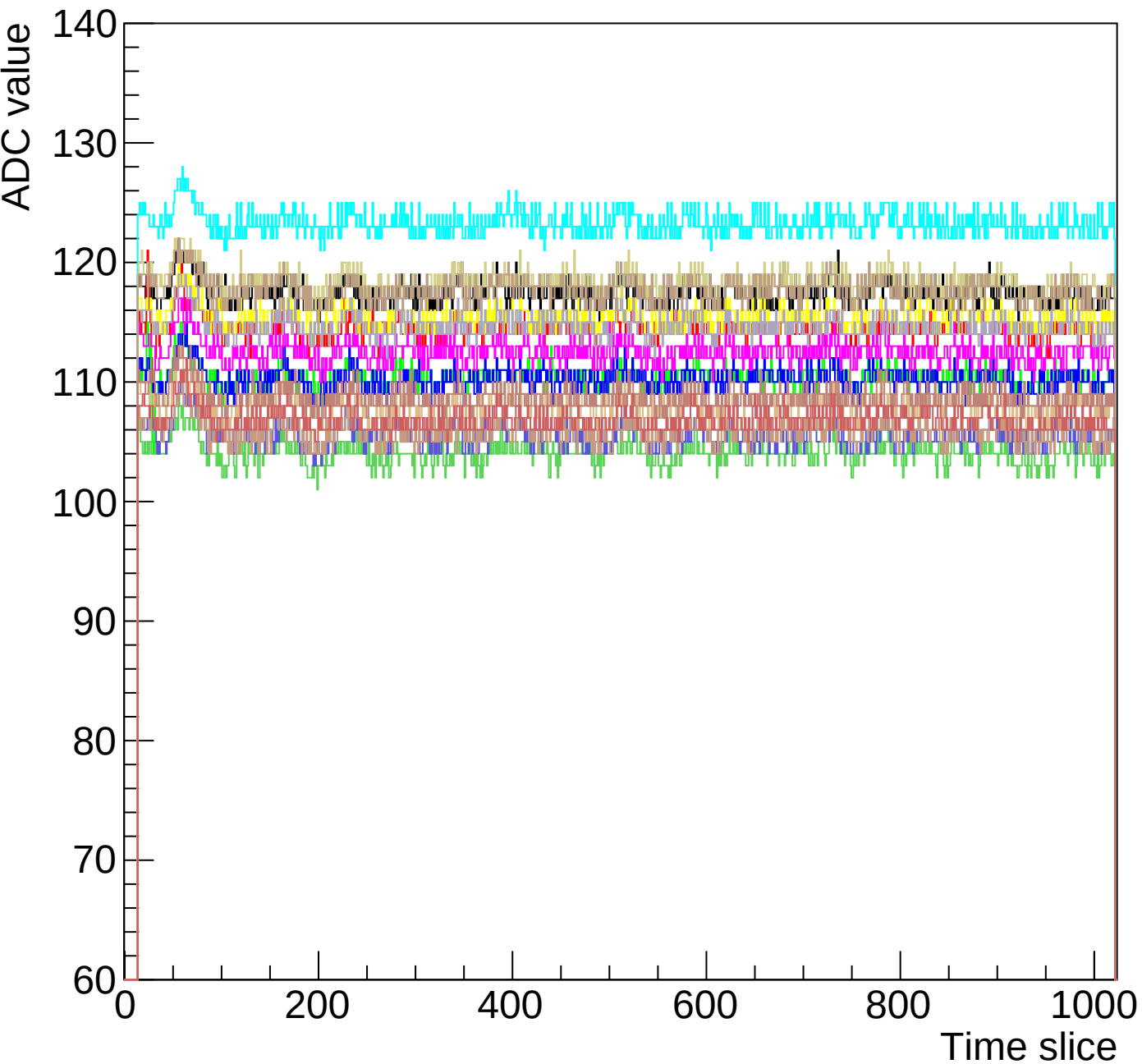
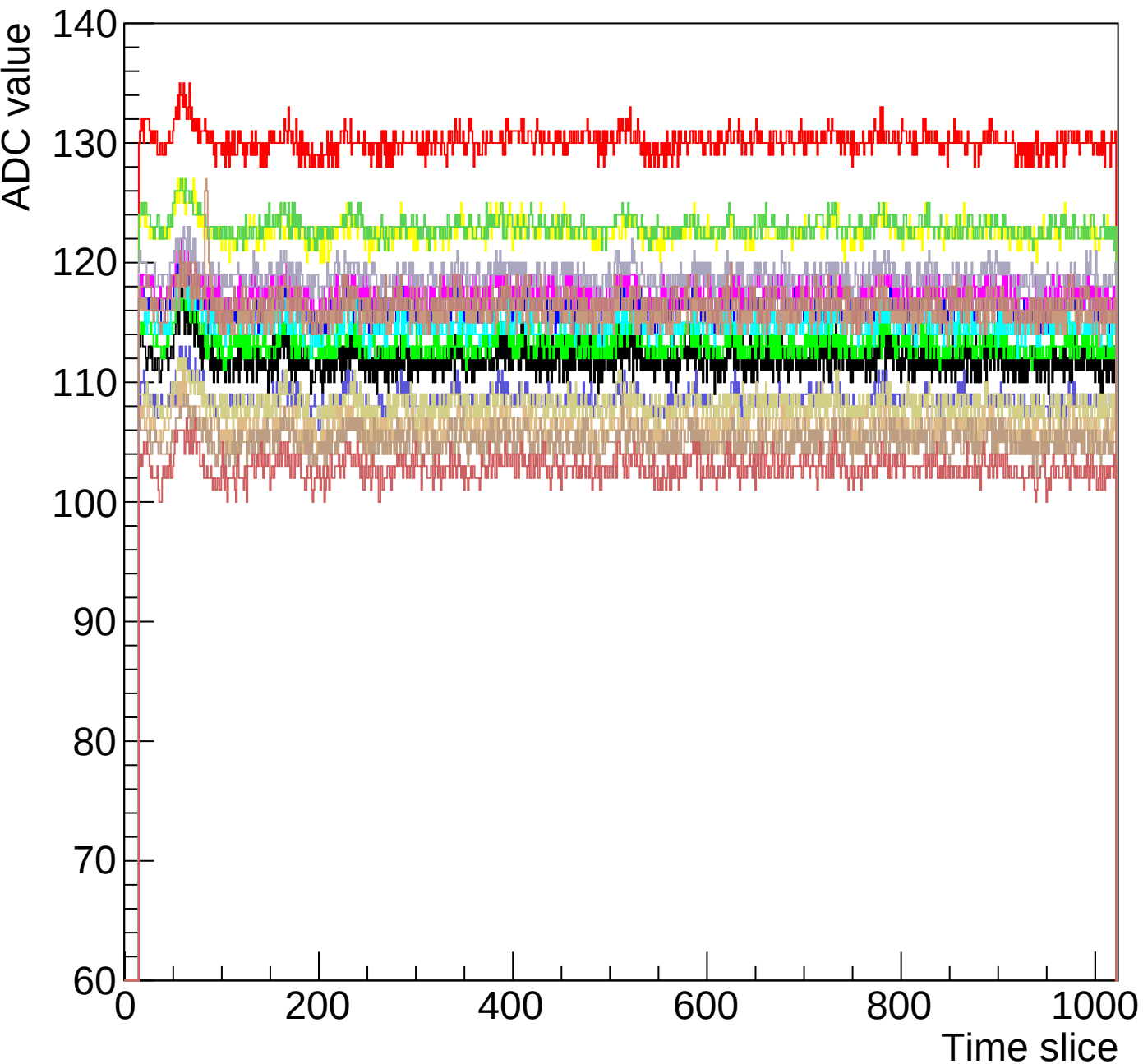


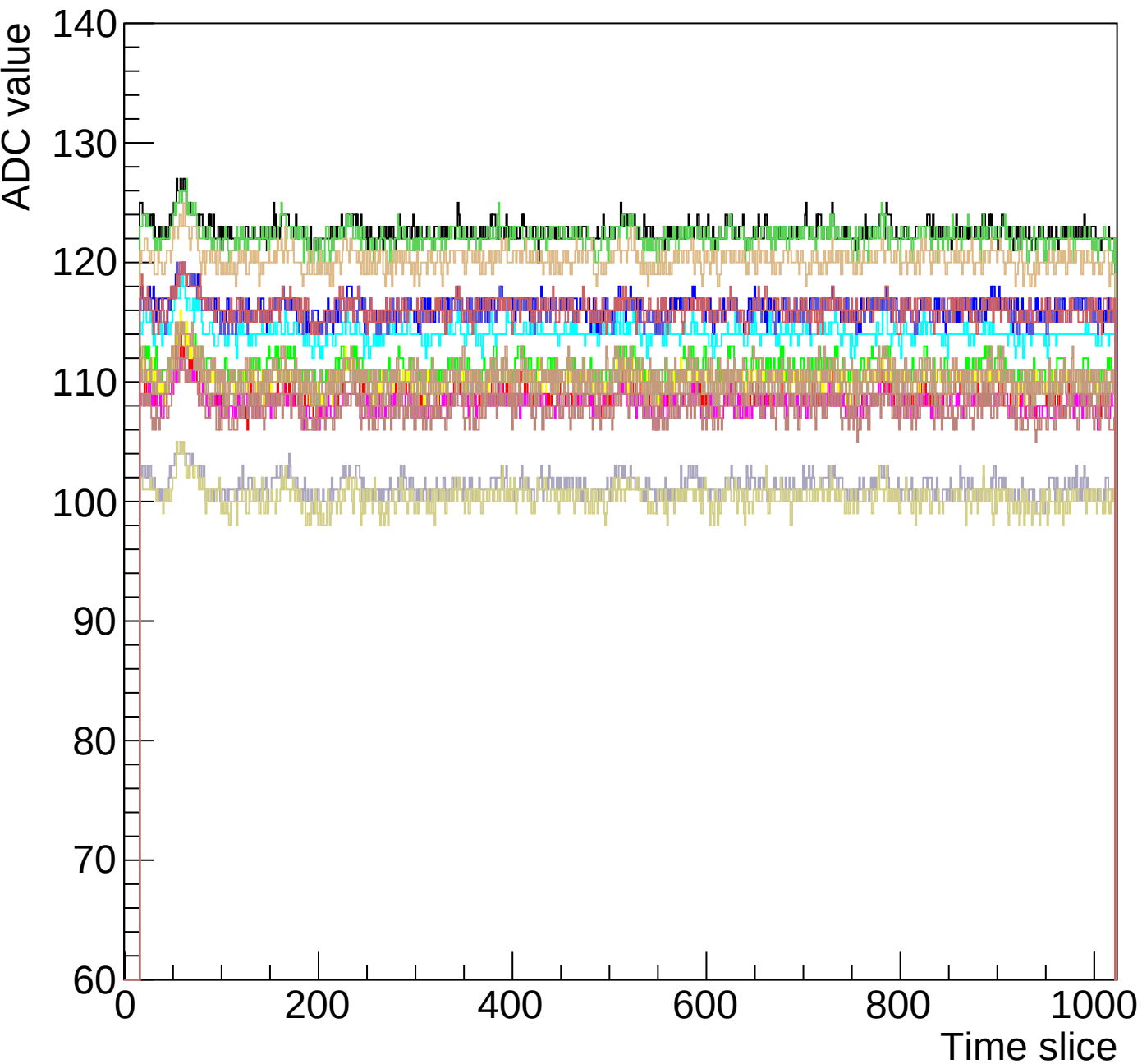
Run 0 event 7 mcm 0 chip 0 chamber-fec01-debug



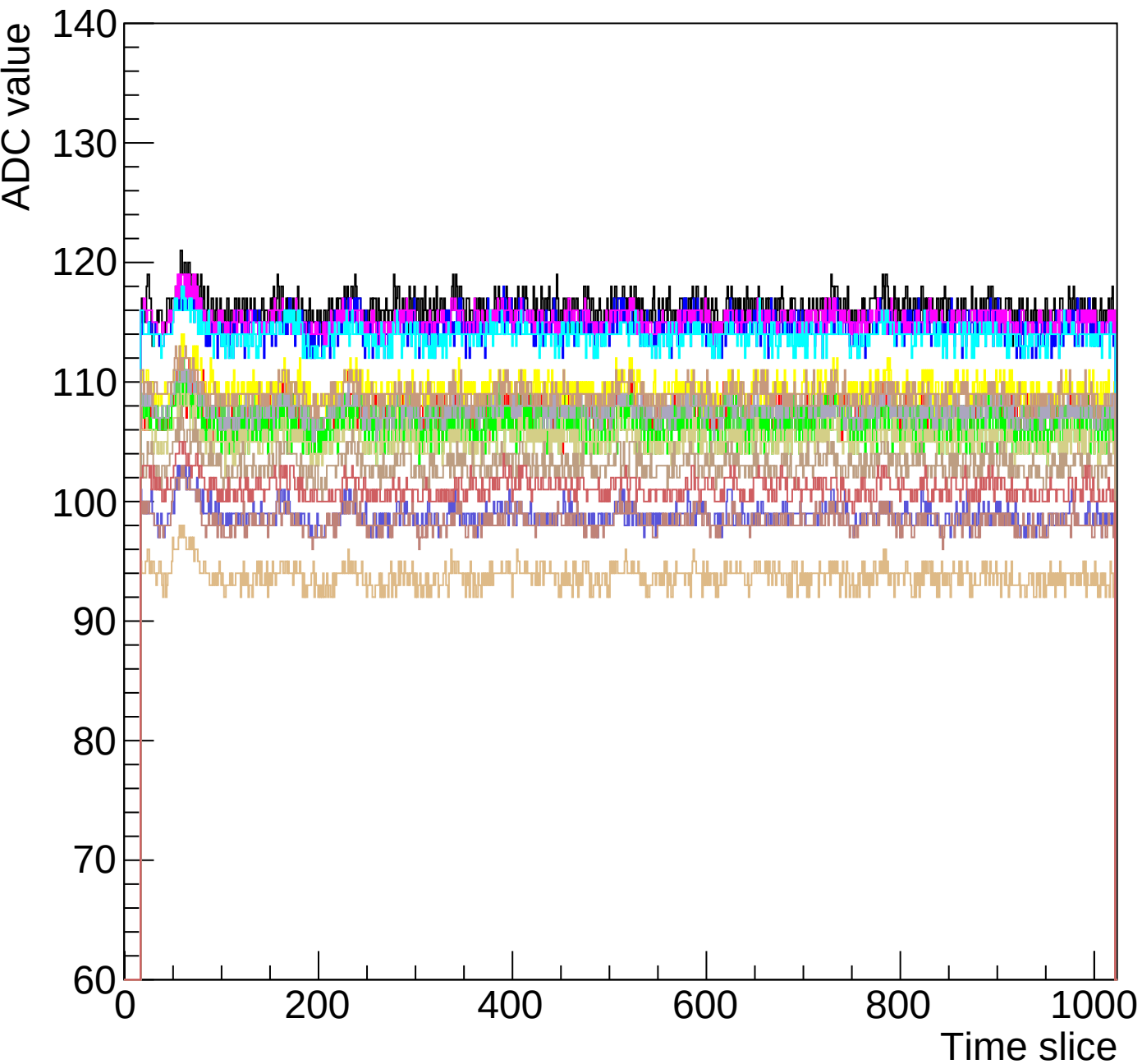
Run 0 event 7 mcm 0 chip 1 chamber-fec01-debug



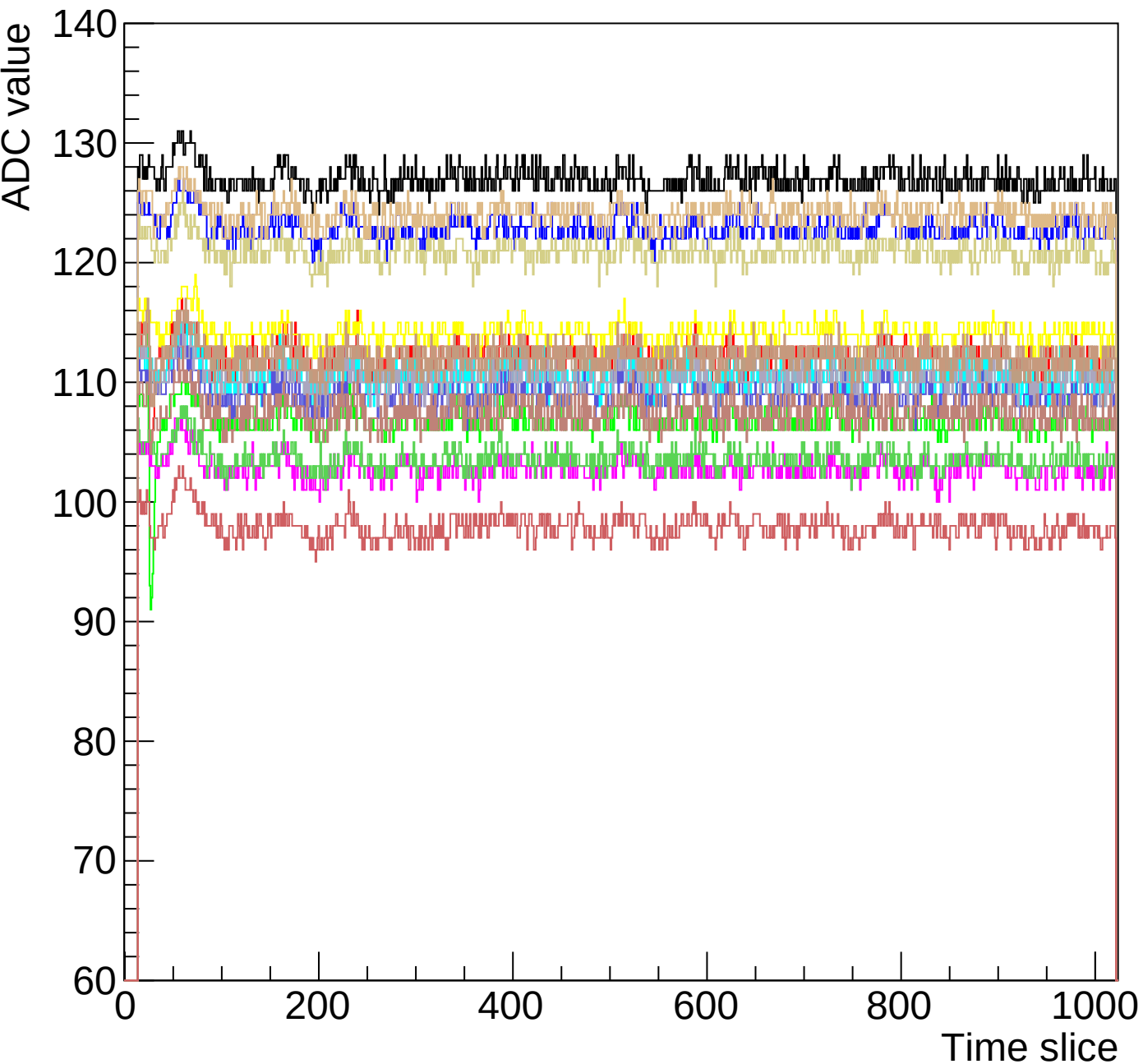
Run 0 event 7 mcm 0 chip 2 chamber-fec01-debug



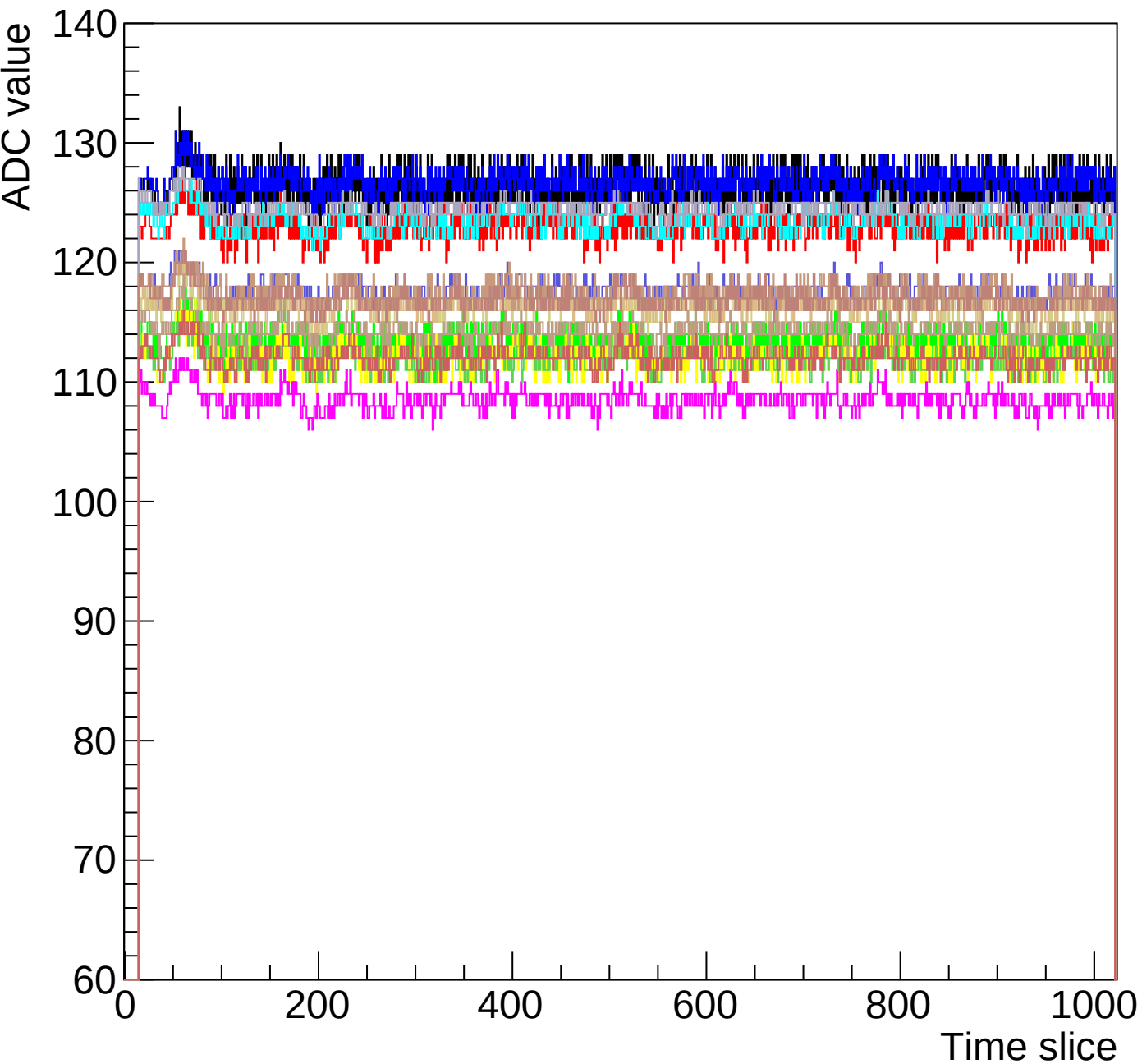
Run 0 event 7 mcm 0 chip 3 chamber-fec01-debug



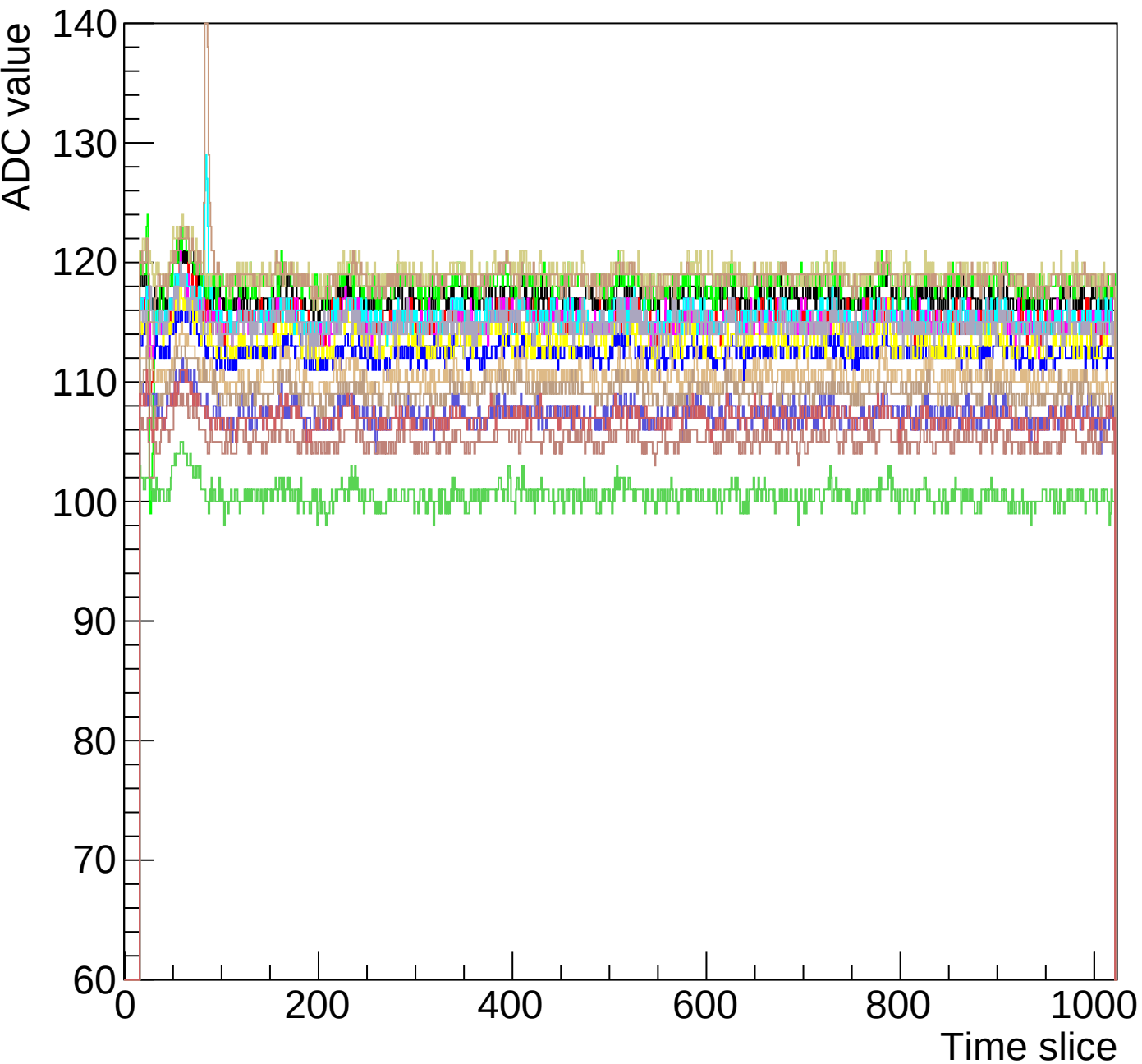
Run 0 event 7 mcm 0 chip 4 chamber-fec01-debug



Run 0 event 7 mcm 0 chip 5 chamber-fec01-debug



Run 0 event 7 mcm 0 chip 6 chamber-fec01-debug



Run 0 event 7 mcm 0 chip 7 chamber-fec01-debug

