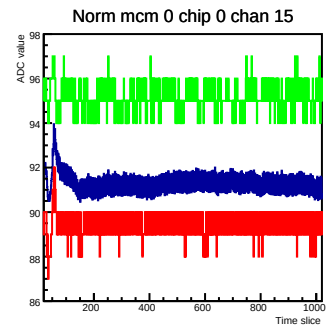
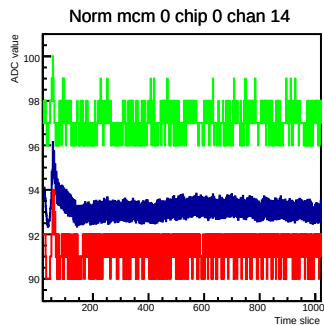
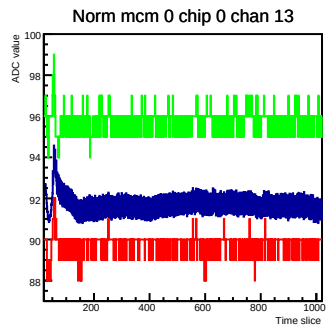
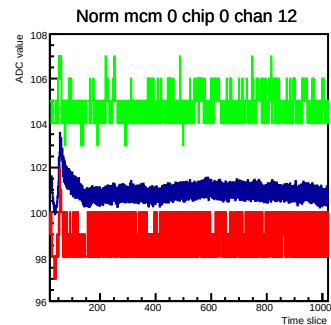
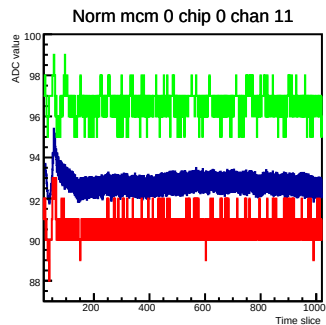
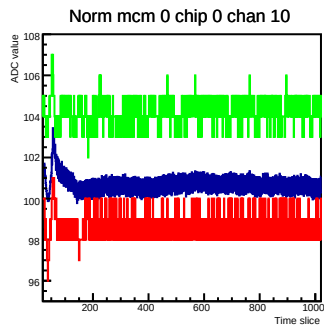
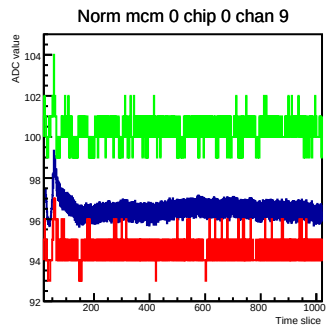
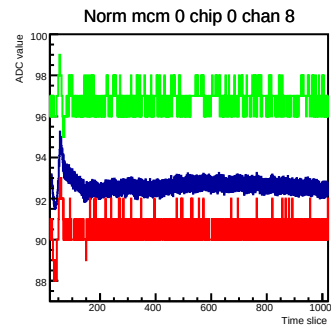
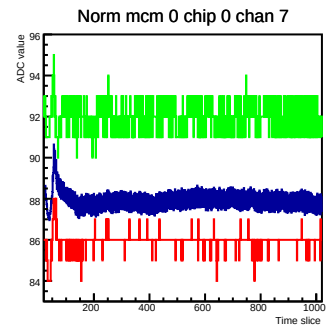
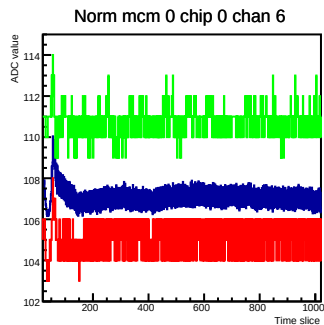
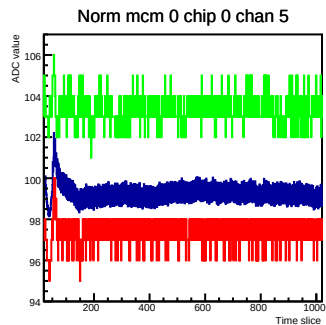
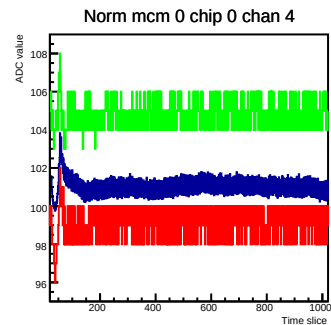
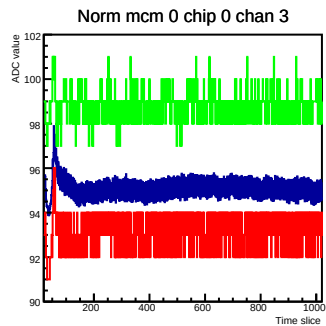
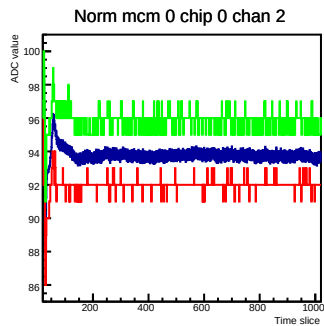
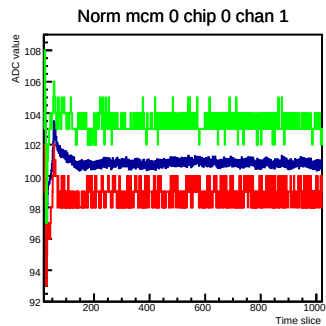
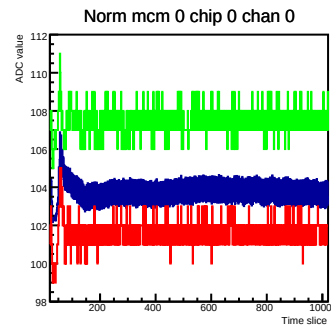
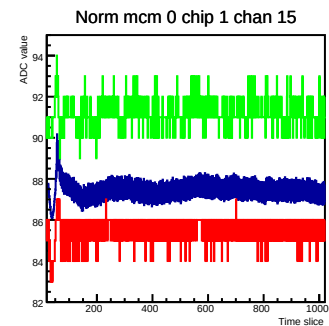
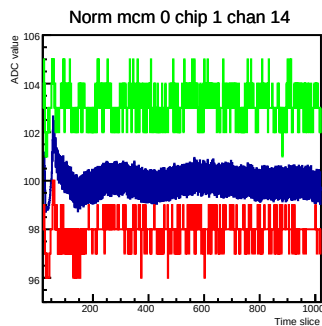
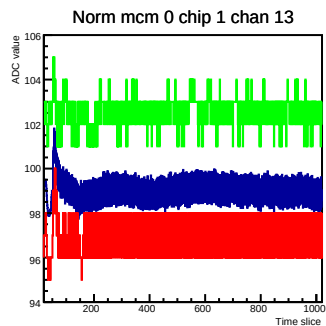
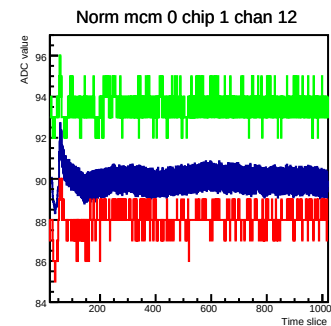
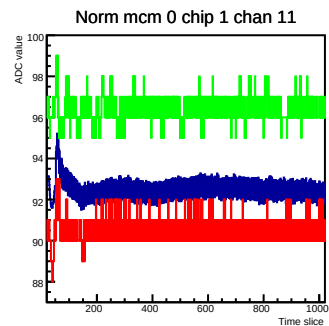
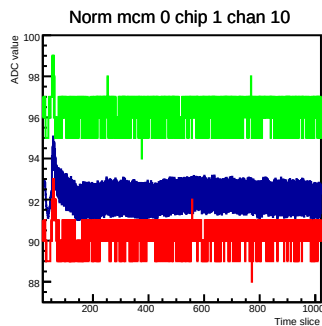
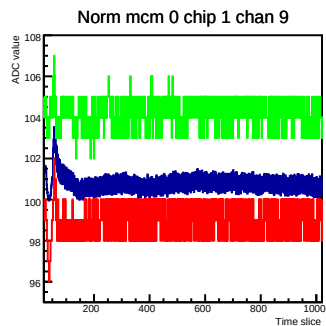
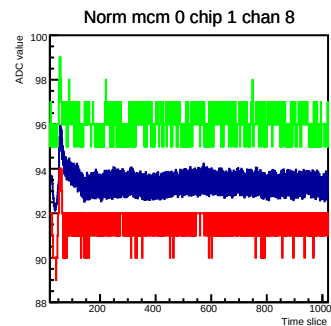
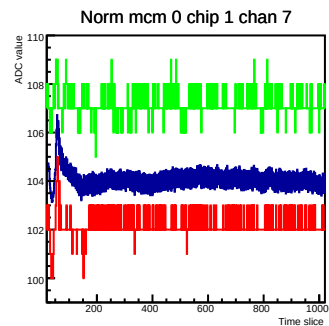
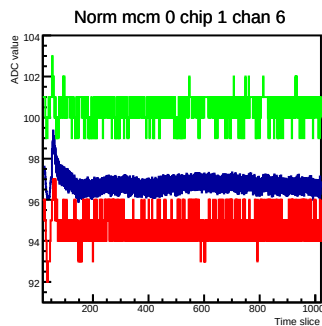
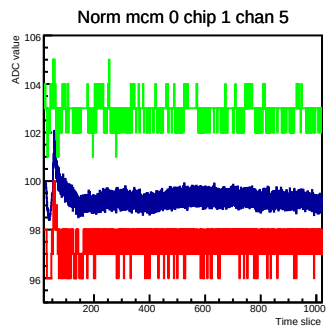
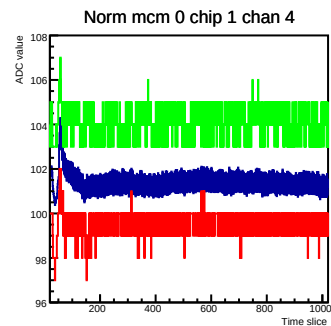
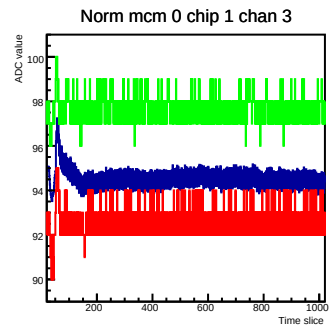
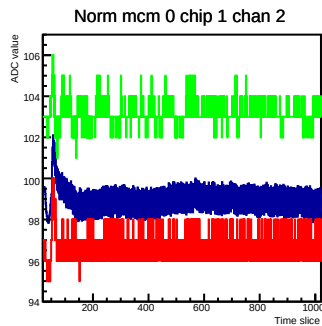
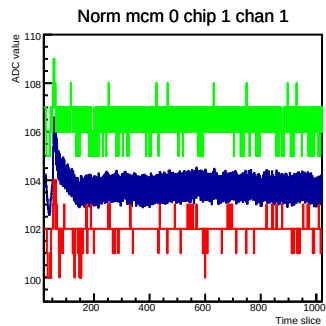
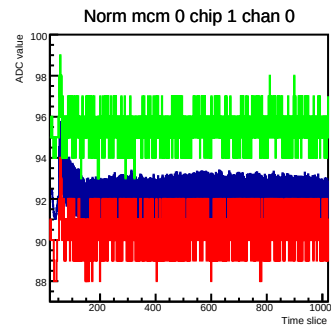
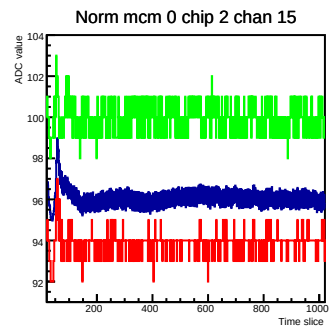
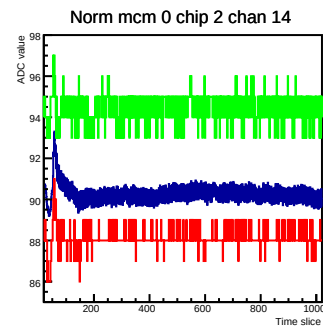
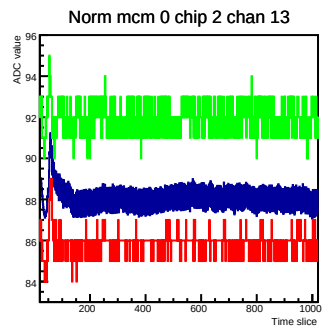
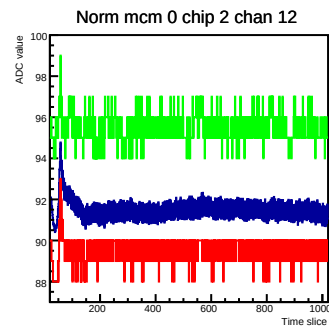
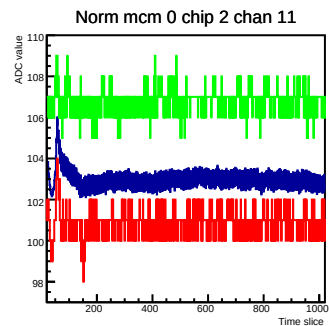
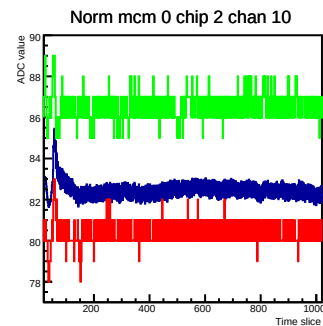
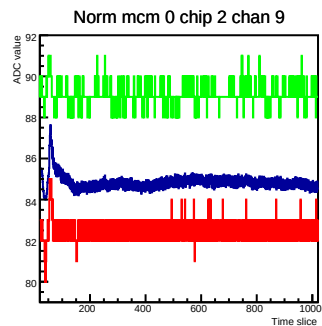
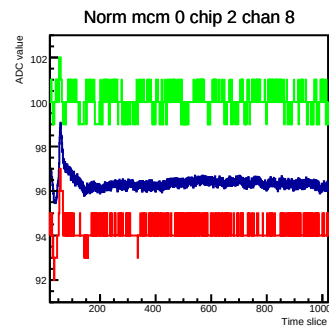
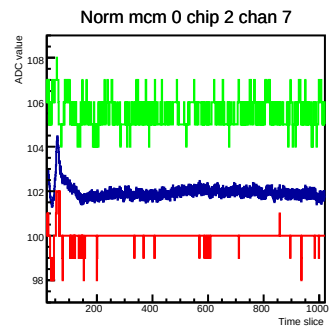
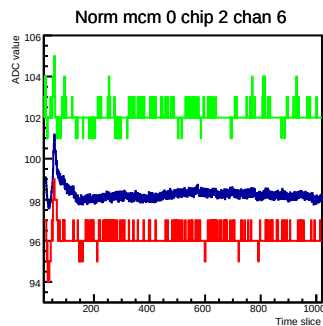
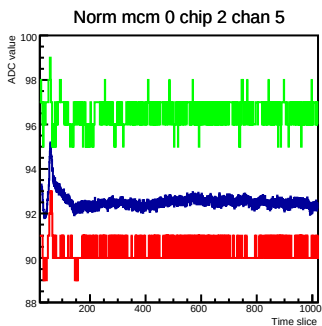
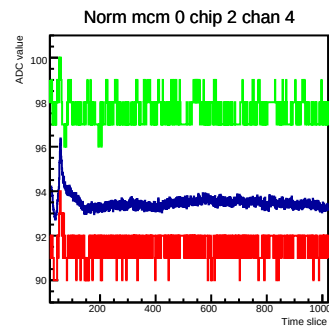
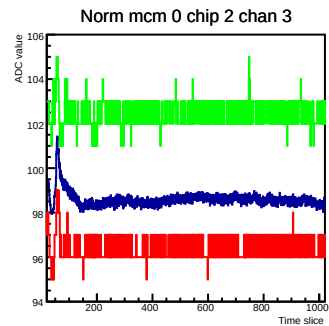
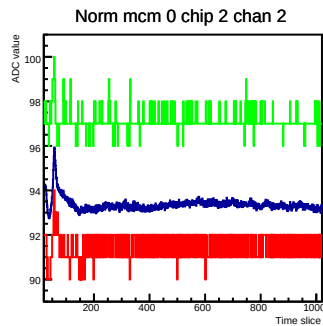
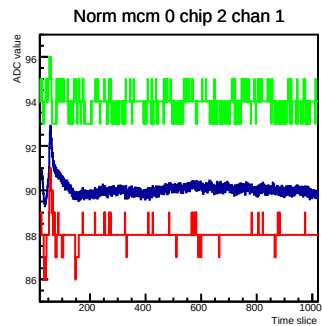
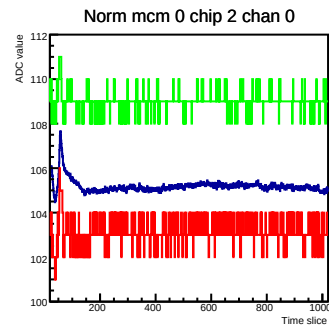
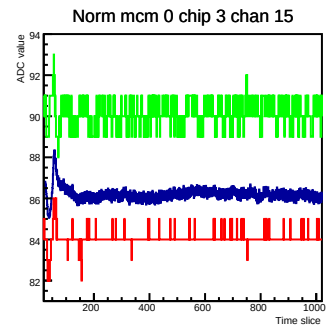
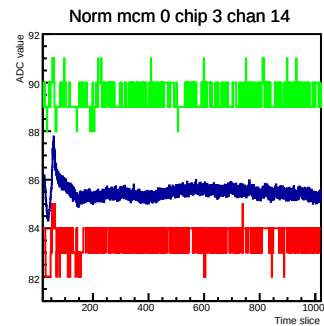
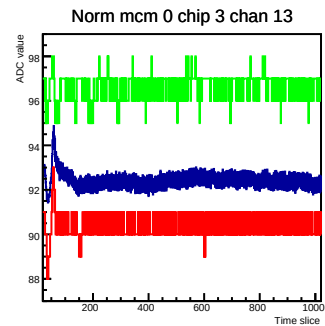
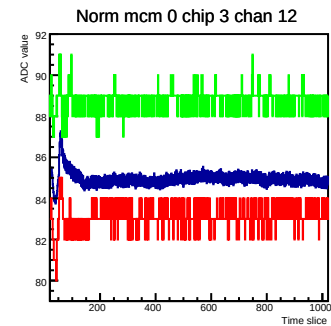
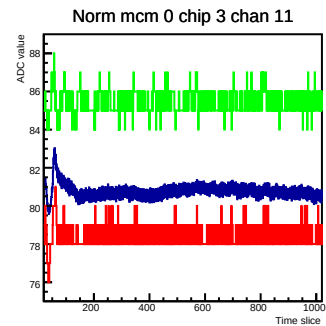
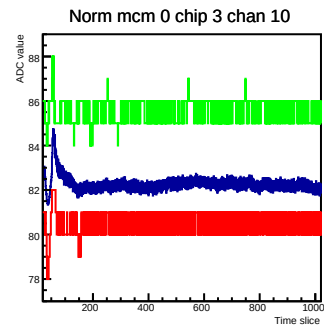
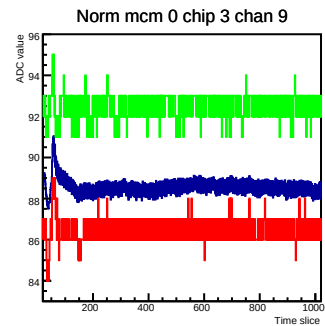
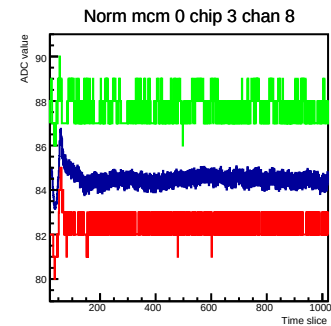
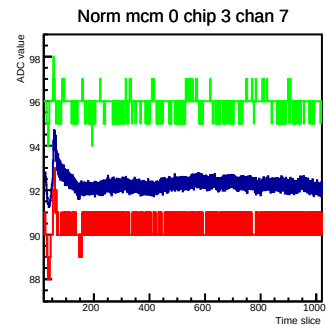
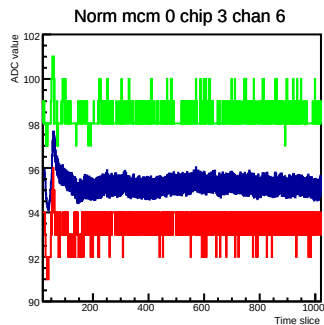
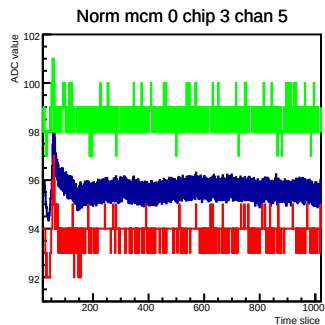
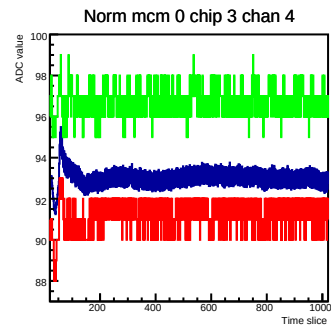
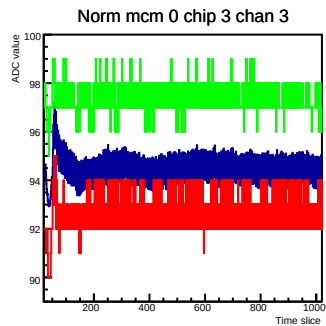
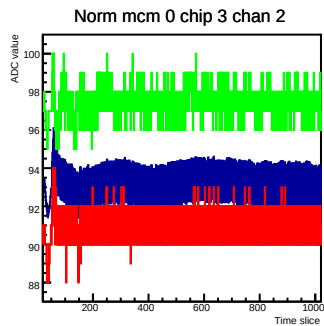
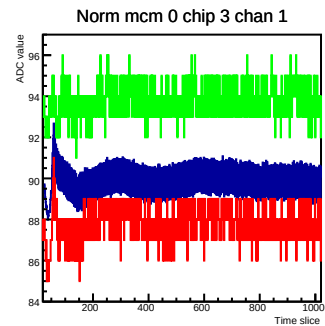
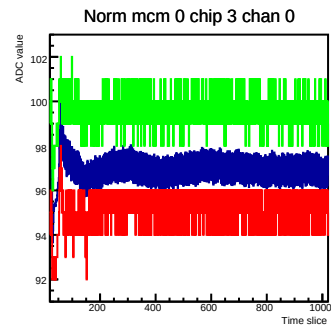
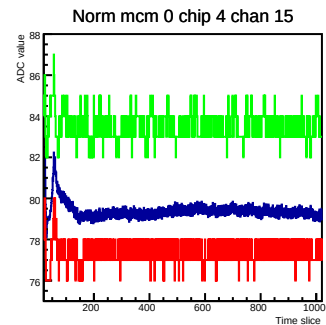
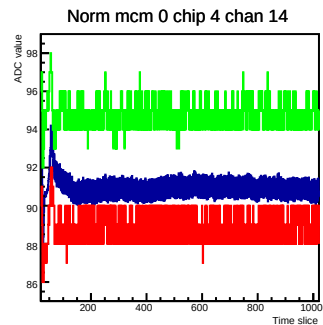
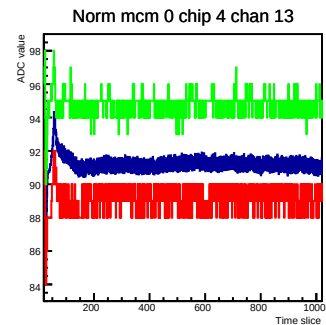
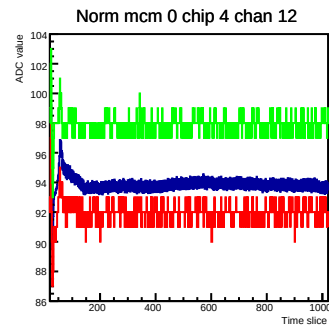
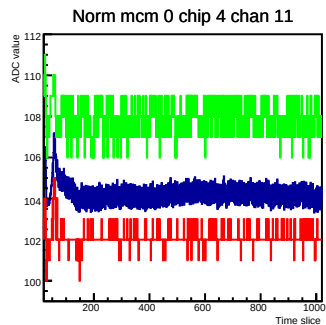
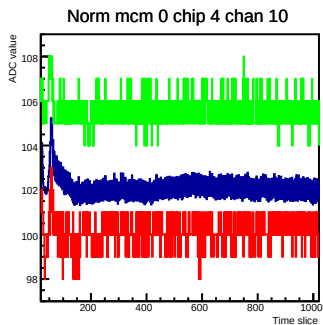
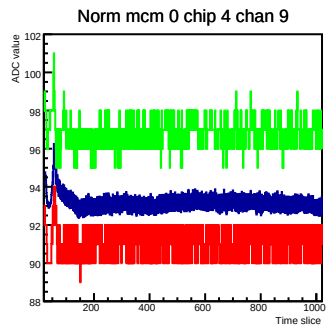
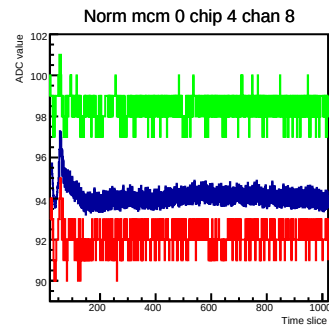
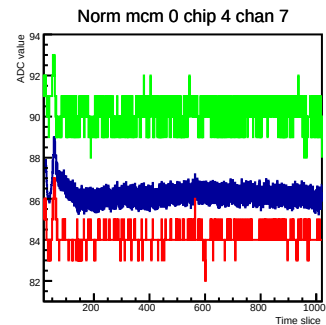
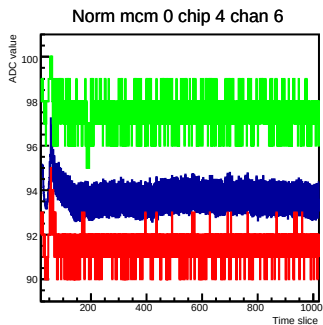
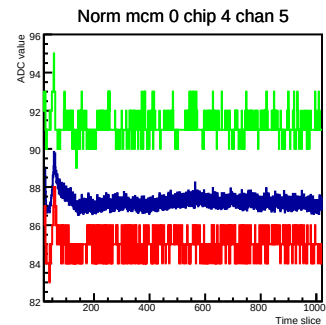
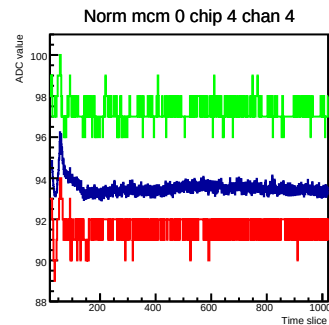
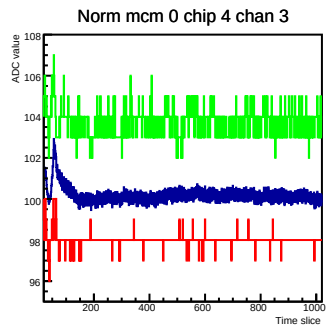
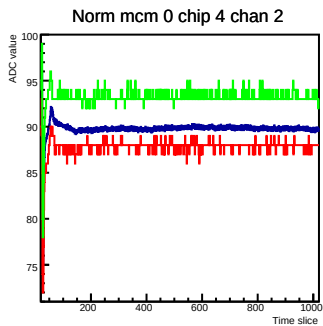
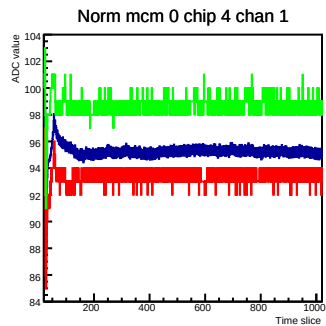
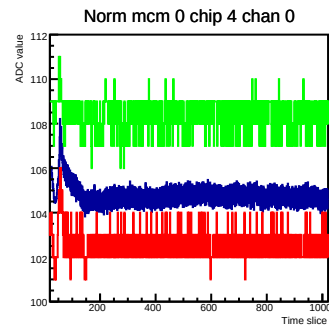


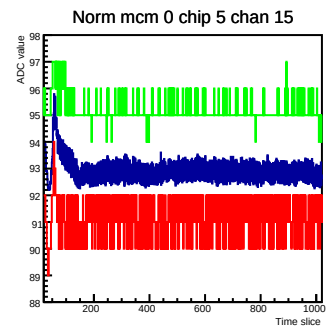
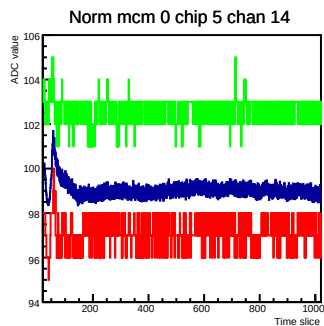
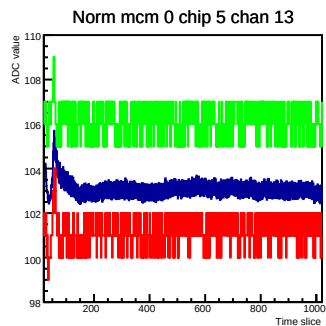
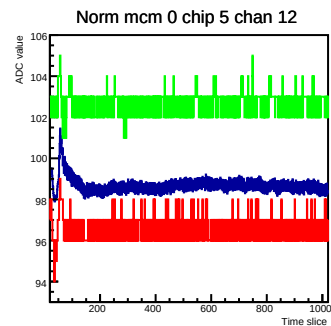
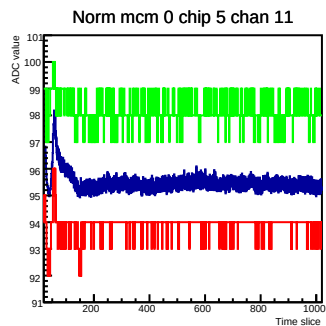
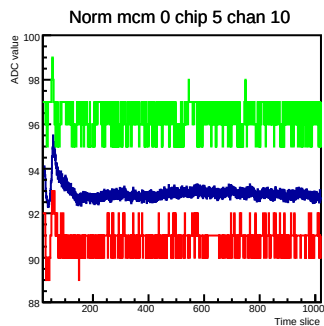
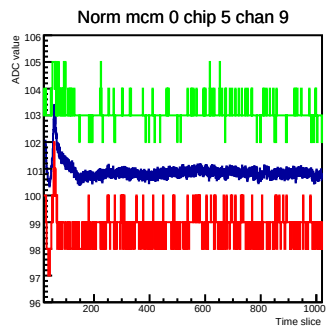
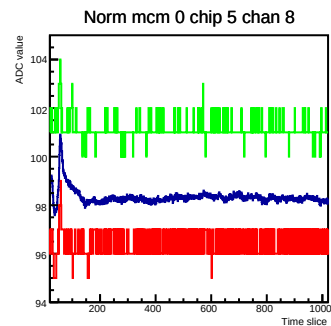
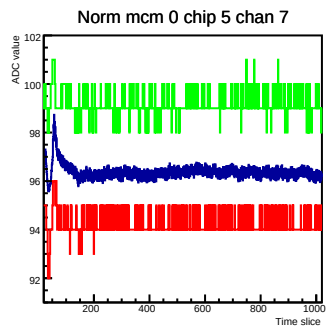
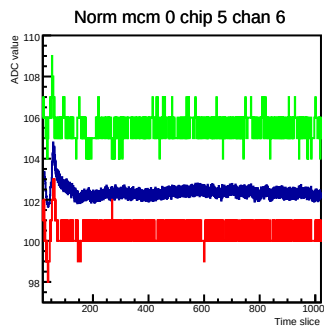
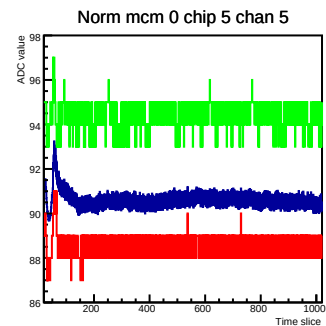
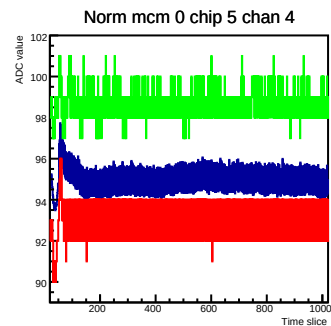
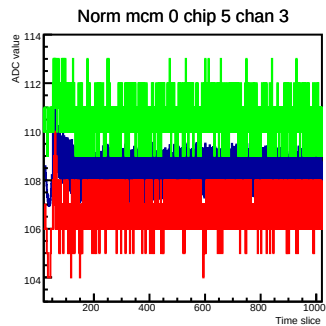
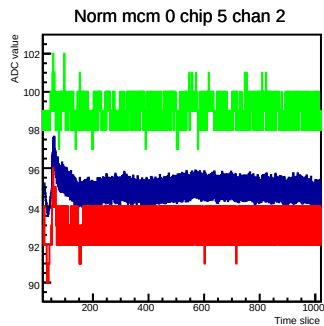
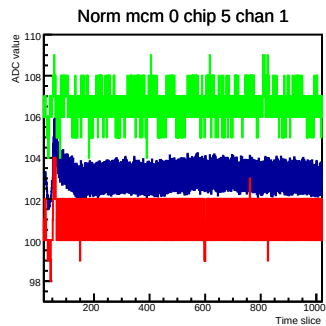
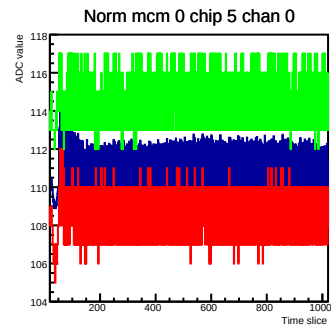




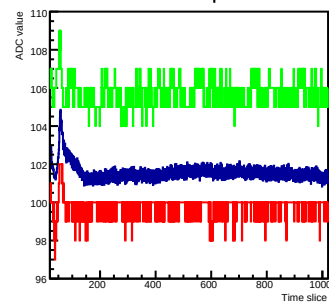




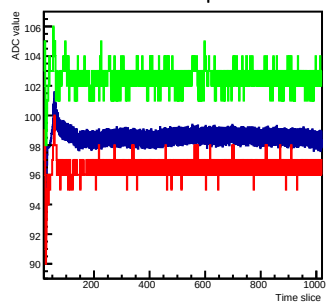




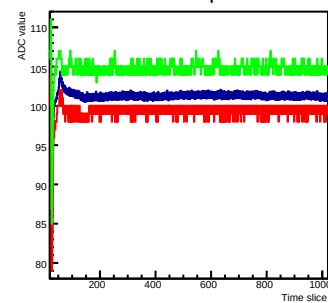
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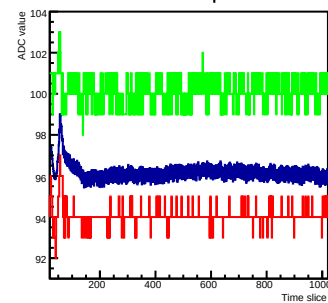
Norm mcm 0 chip 6 chan 1



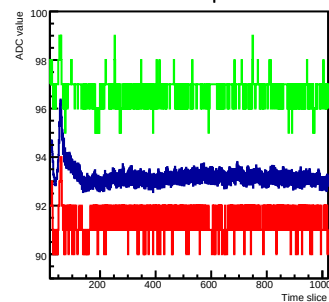
Norm mcm 0 chip 6 chan 2



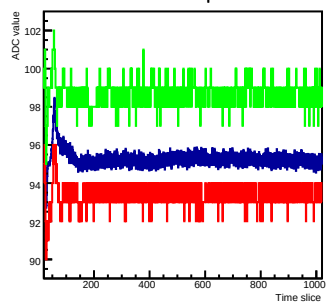
Norm mcm 0 chip 6 chan 3



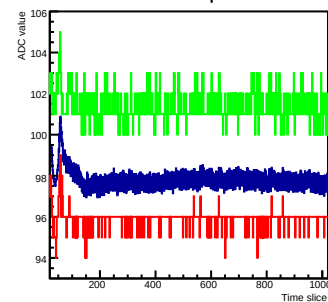
Norm mcm 0 chip 6 chan 4



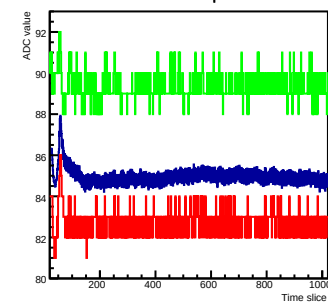
Norm mcm 0 chip 6 chan 5



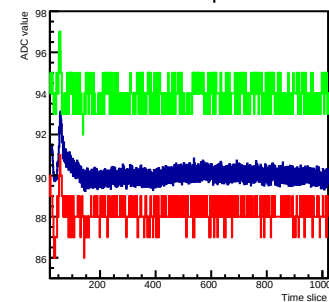
Norm mcm 0 chip 6 chan 6



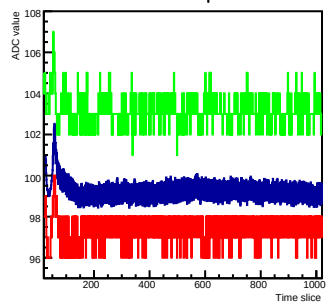
Norm mcm 0 chip 6 chan 7



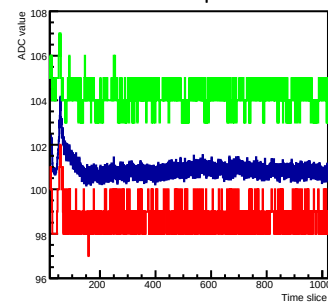
Norm mcm 0 chip 6 chan 8



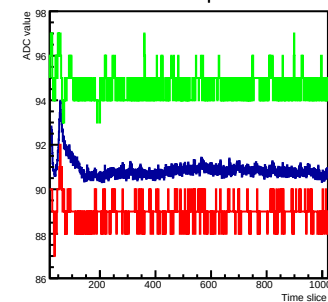
Norm mcm 0 chip 6 chan 9



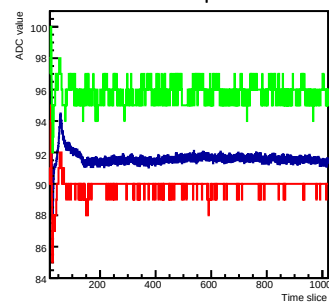
Norm mcm 0 chip 6 chan 10



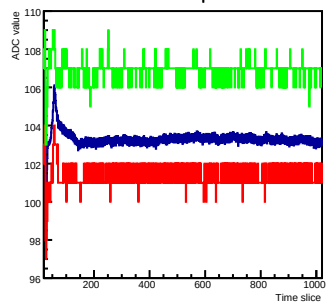
Norm mcm 0 chip 6 chan 11



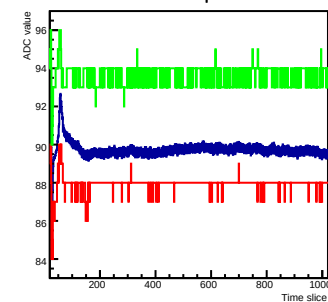
Norm mcm 0 chip 6 chan 12



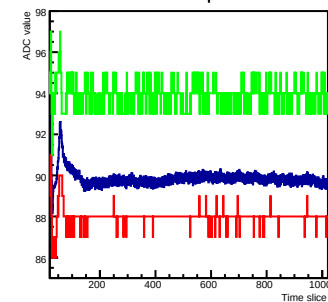
Norm mcm 0 chip 6 chan 13

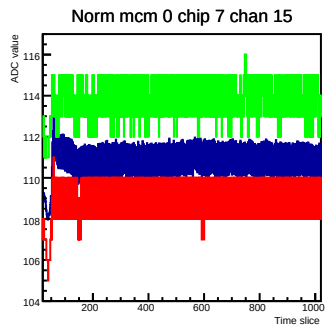
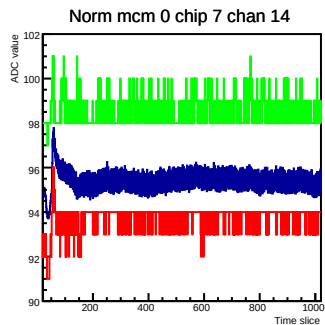
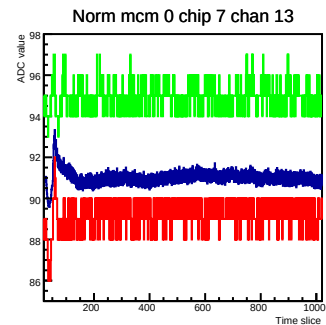
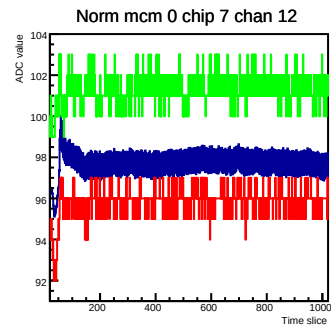
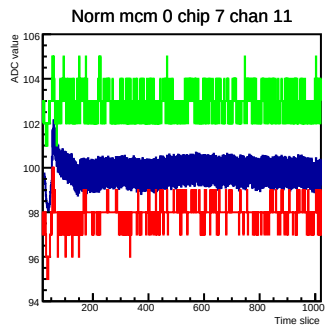
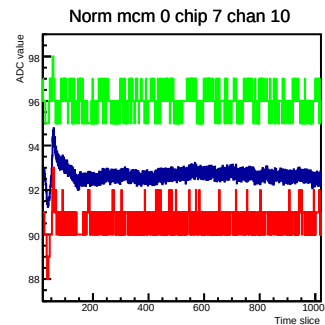
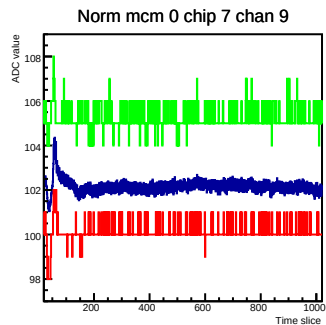
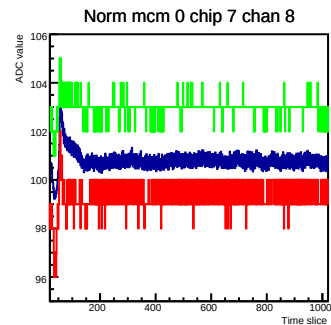
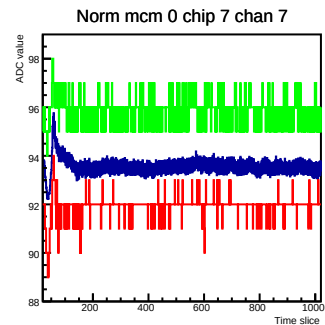
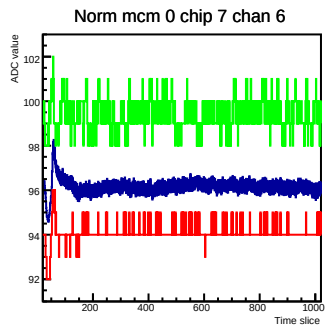
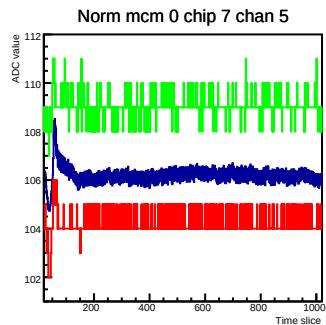
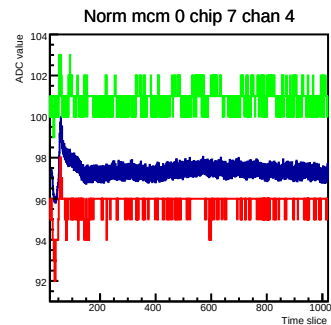
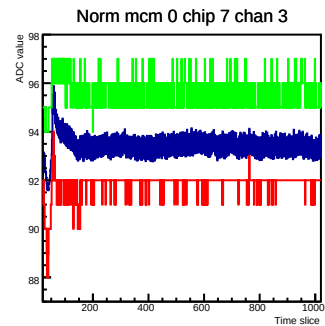
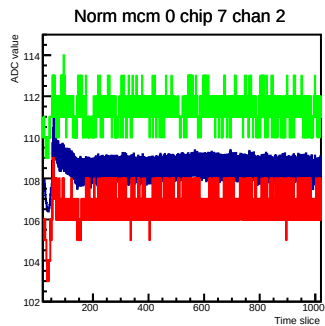
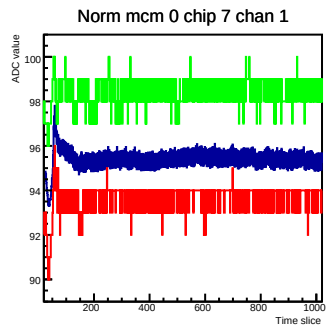
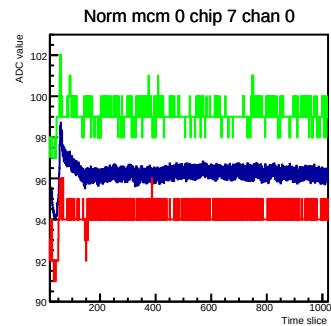


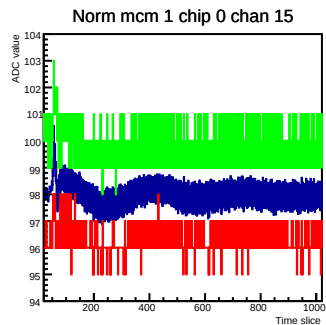
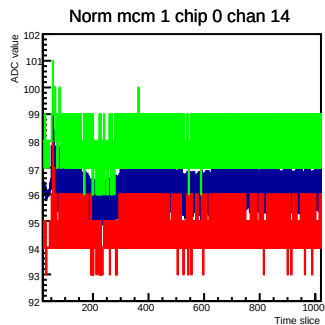
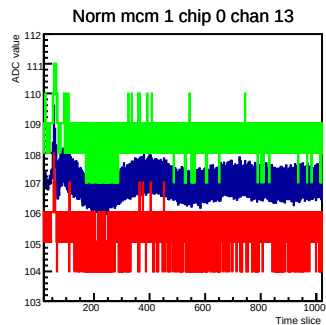
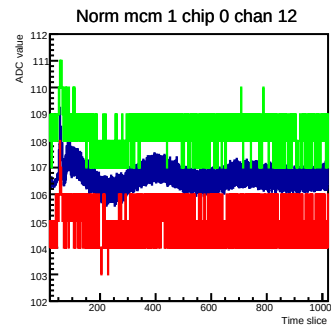
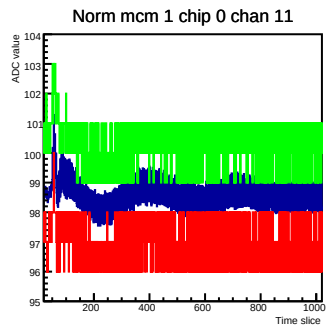
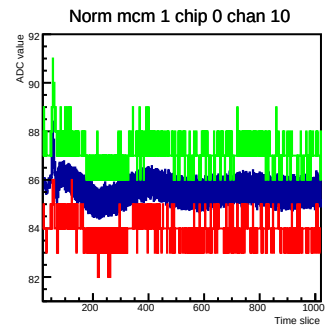
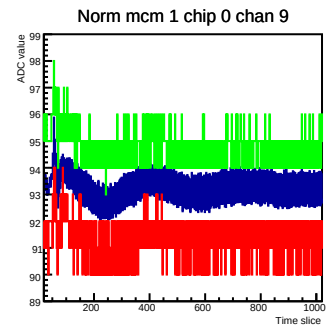
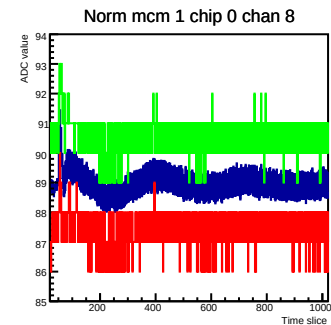
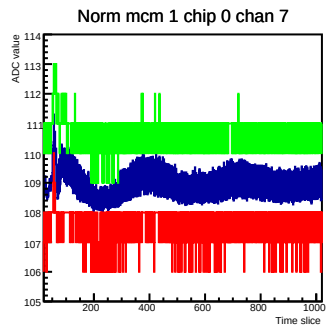
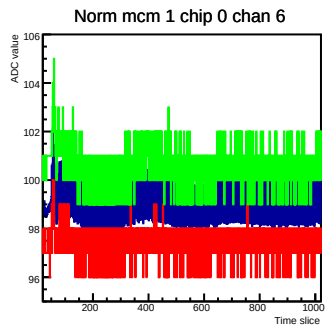
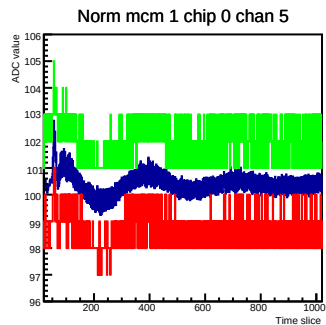
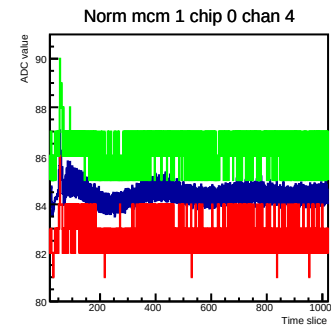
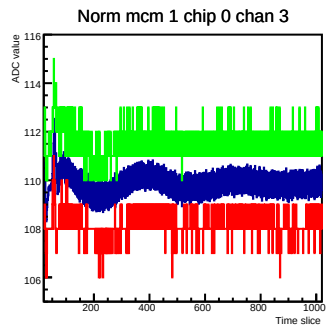
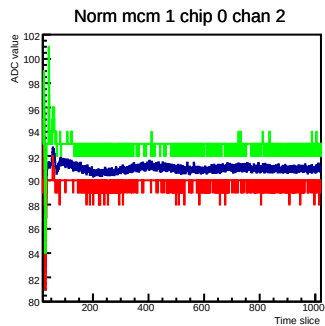
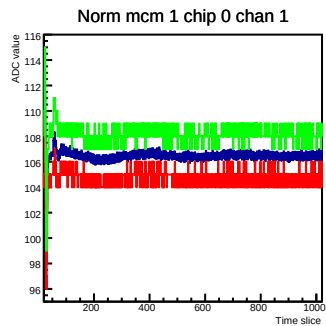
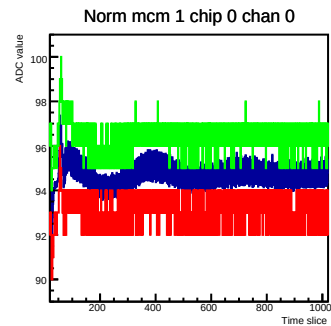
Norm mcm 0 chip 6 chan 14



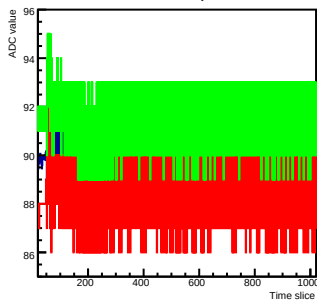
Norm mcm 0 chip 6 chan 15



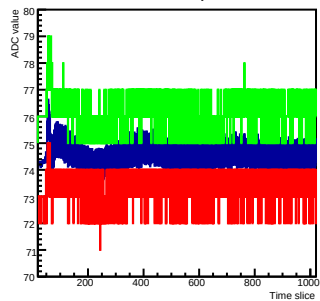




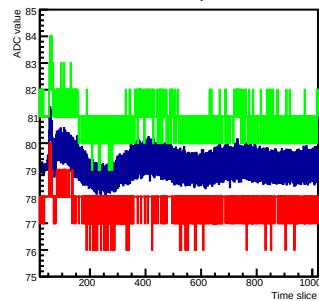
Norm mcm 1 chip 1 chan 0



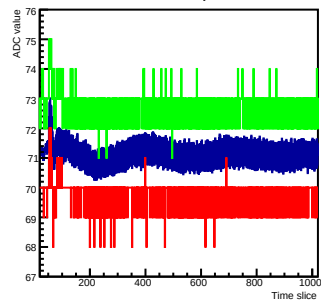
Norm mcm 1 chip 1 chan 1



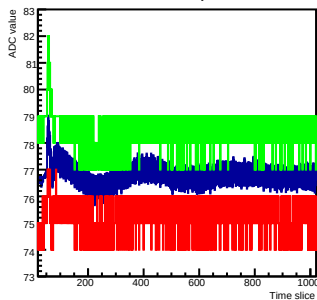
Norm mcm 1 chip 1 chan 2



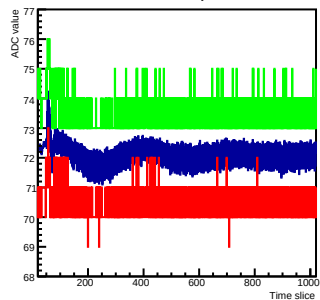
Norm mcm 1 chip 1 chan 3



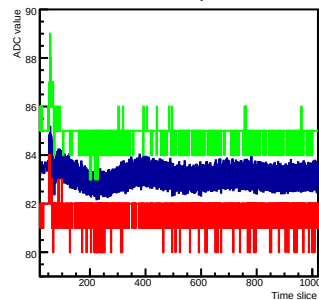
Norm mcm 1 chip 1 chan 4



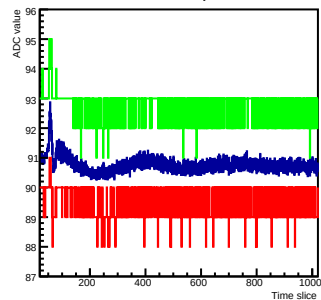
Norm mcm 1 chip 1 chan 5



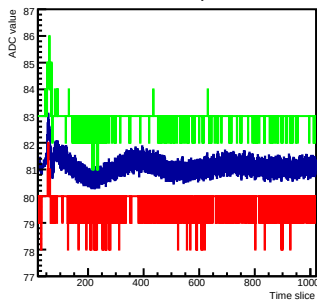
Norm mcm 1 chip 1 chan 6



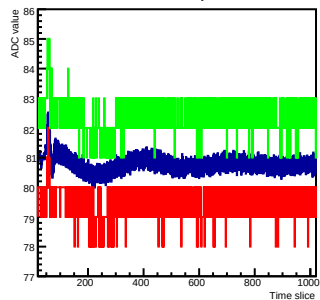
Norm mcm 1 chip 1 chan 7



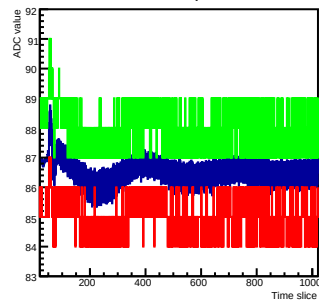
Norm mcm 1 chip 1 chan 8



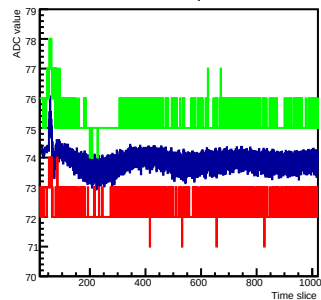
Norm mcm 1 chip 1 chan 9



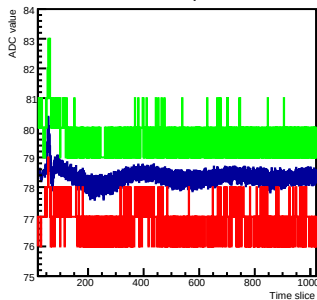
Norm mcm 1 chip 1 chan 10



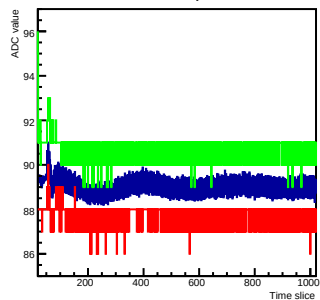
Norm mcm 1 chip 1 chan 11



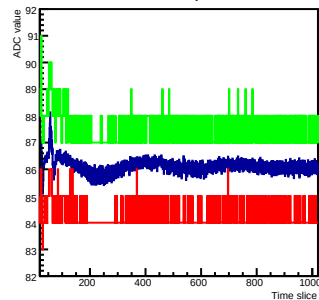
Norm mcm 1 chip 1 chan 12



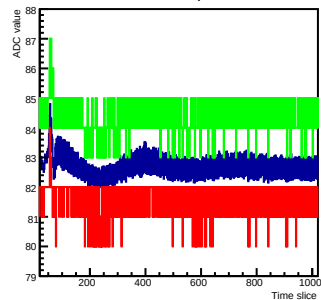
Norm mcm 1 chip 1 chan 13



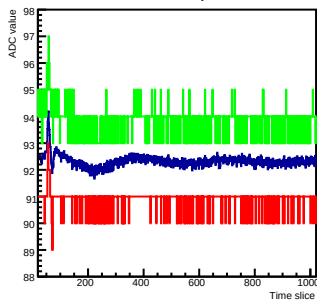
Norm mcm 1 chip 1 chan 14



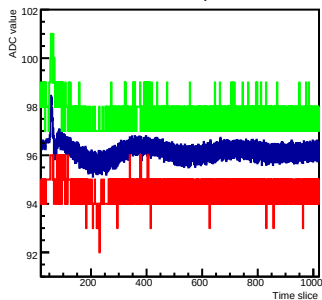
Norm mcm 1 chip 1 chan 15



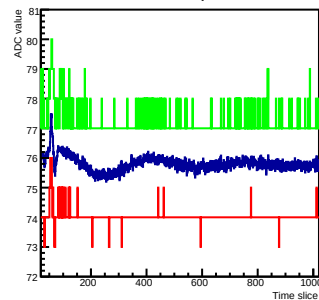
Norm mcm 1 chip 2 chan 0



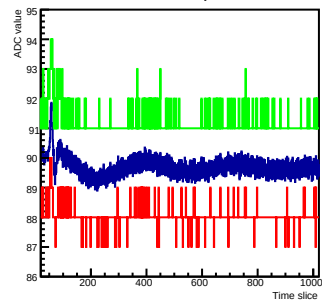
Norm mcm 1 chip 2 chan 1



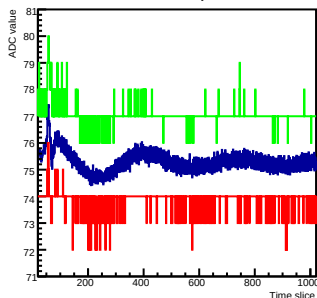
Norm mcm 1 chip 2 chan 2



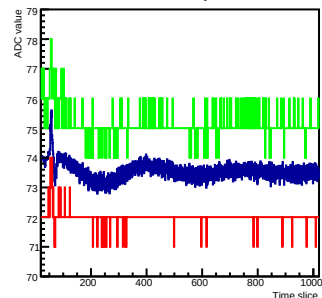
Norm mcm 1 chip 2 chan 3



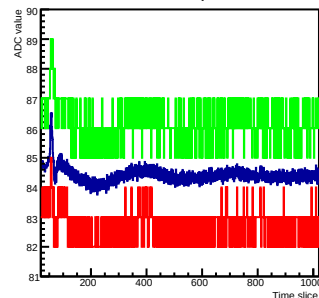
Norm mcm 1 chip 2 chan 4



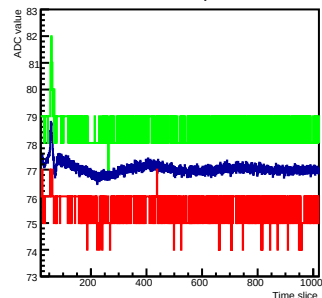
Norm mcm 1 chip 2 chan 5



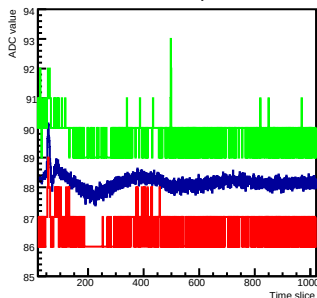
Norm mcm 1 chip 2 chan 6



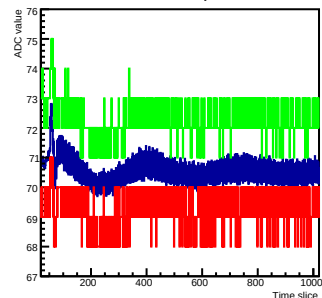
Norm mcm 1 chip 2 chan 7



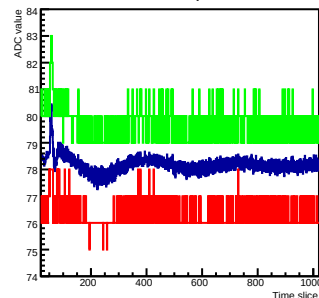
Norm mcm 1 chip 2 chan 8



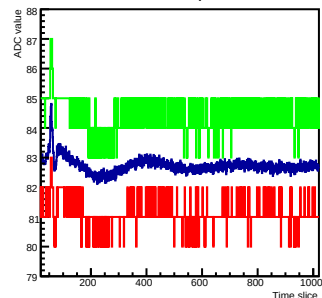
Norm mcm 1 chip 2 chan 9



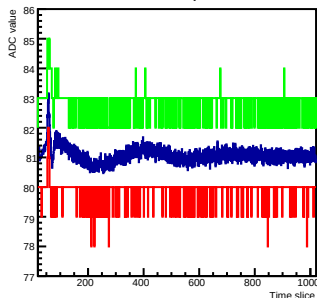
Norm mcm 1 chip 2 chan 10



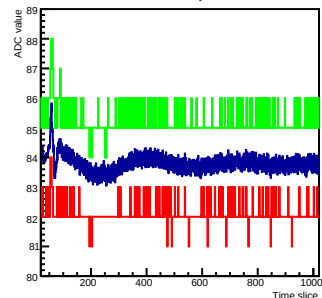
Norm mcm 1 chip 2 chan 11



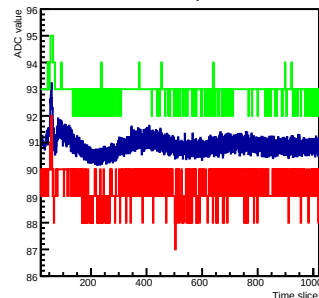
Norm mcm 1 chip 2 chan 12



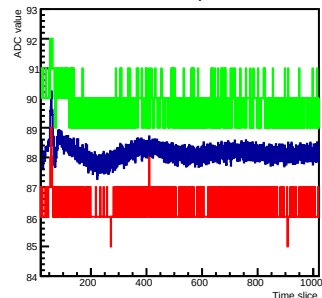
Norm mcm 1 chip 2 chan 13



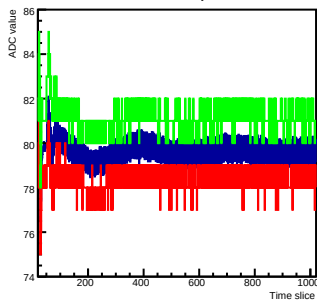
Norm mcm 1 chip 2 chan 14



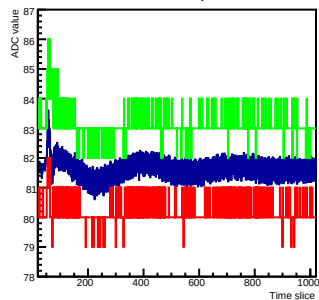
Norm mcm 1 chip 2 chan 15



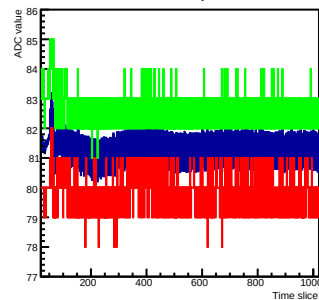
Norm mcm 1 chip 3 chan 0



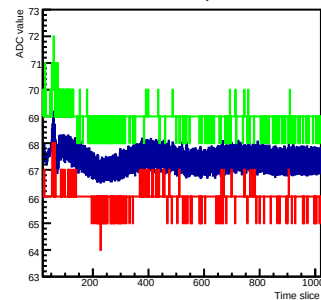
Norm mcm 1 chip 3 chan 1



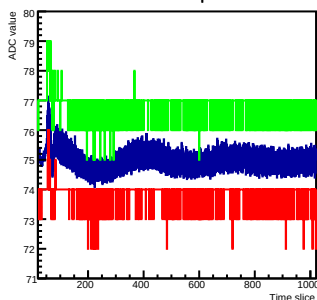
Norm mcm 1 chip 3 chan 2



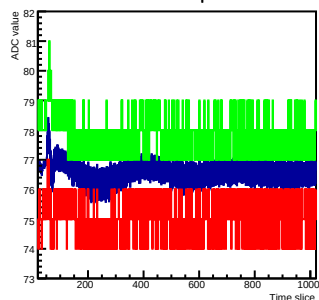
Norm mcm 1 chip 3 chan 3



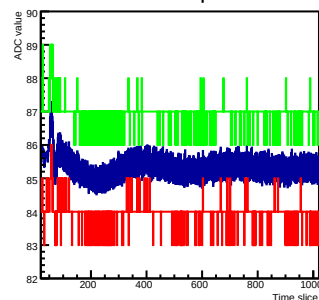
Norm mcm 1 chip 3 chan 4



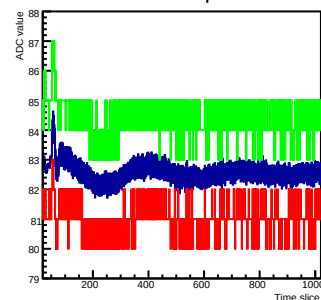
Norm mcm 1 chip 3 chan 5



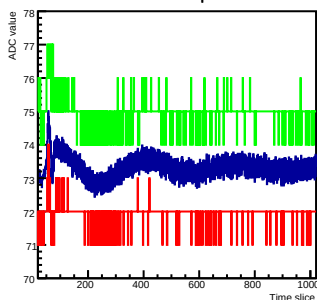
Norm mcm 1 chip 3 chan 6



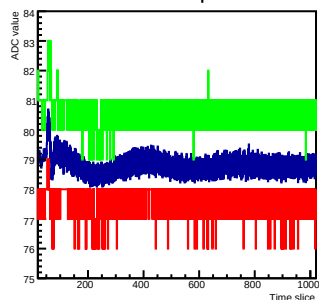
Norm mcm 1 chip 3 chan 7



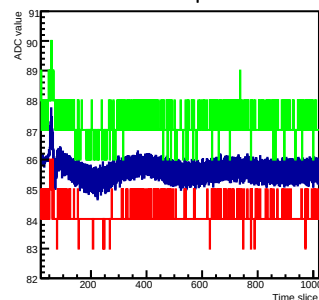
Norm mcm 1 chip 3 chan 8



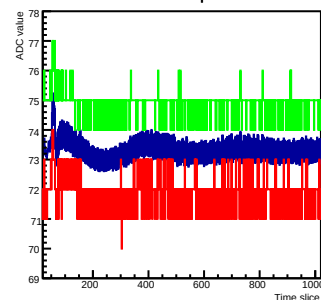
Norm mcm 1 chip 3 chan 9



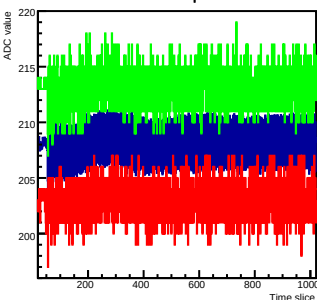
Norm mcm 1 chip 3 chan 10



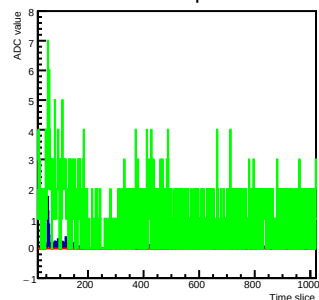
Norm mcm 1 chip 3 chan 11



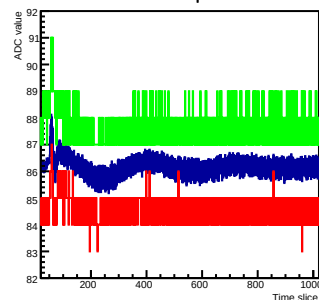
Norm mcm 1 chip 3 chan 12



Norm mcm 1 chip 3 chan 13



Norm mcm 1 chip 3 chan 14



Norm mcm 1 chip 3 chan 15

