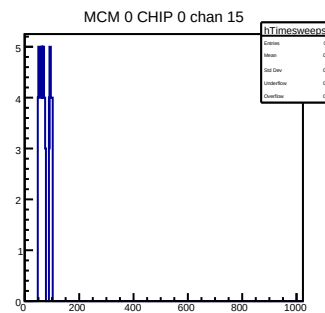
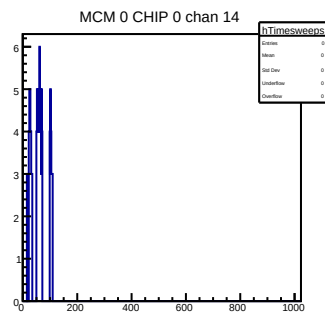
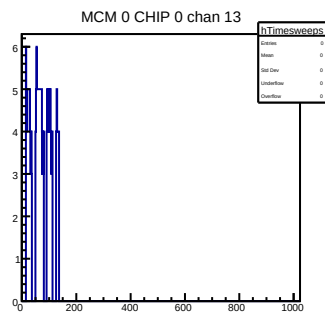
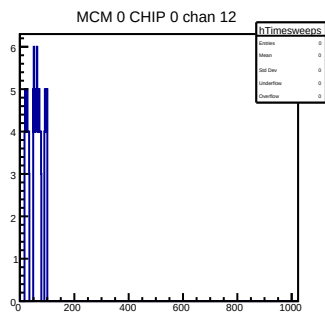
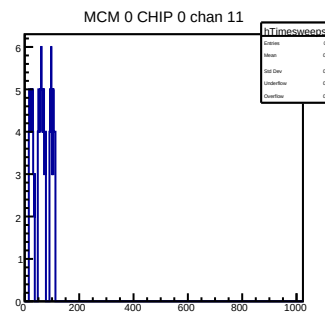
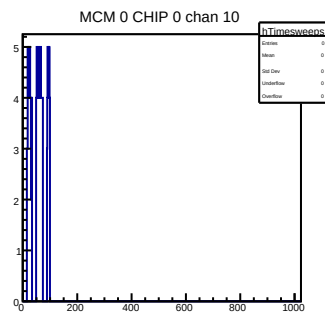
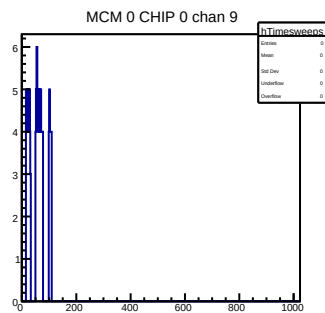
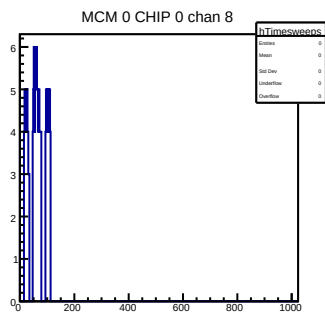
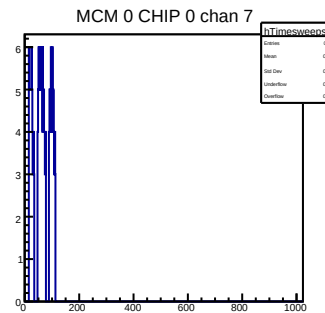
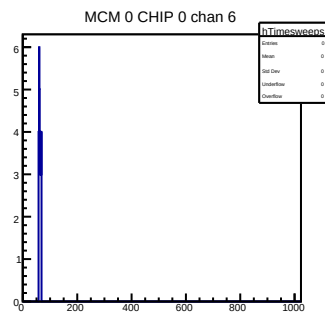
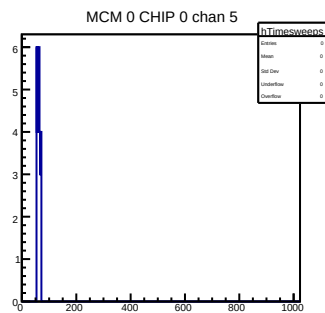
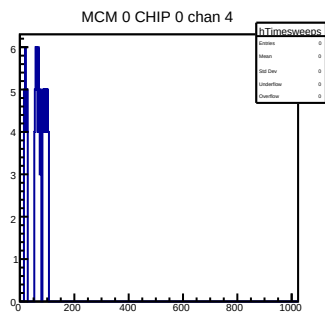
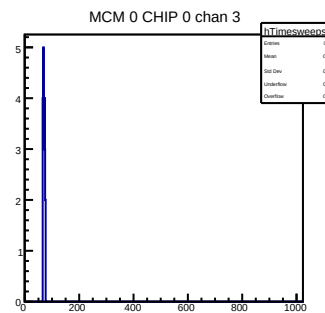
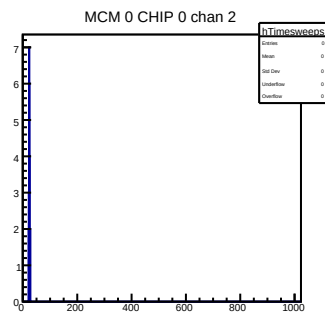
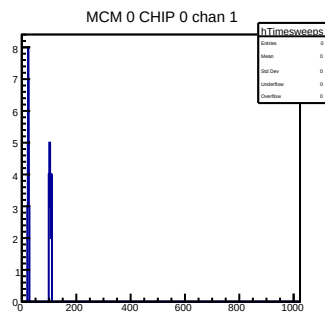
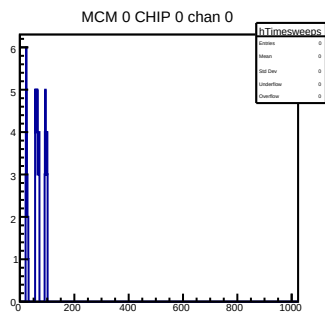
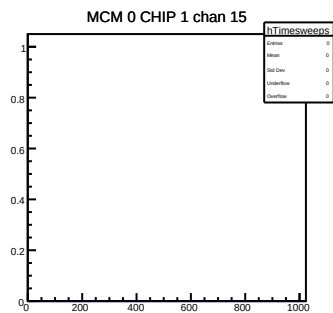
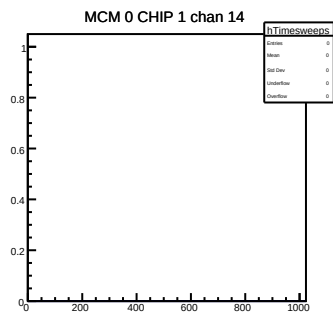
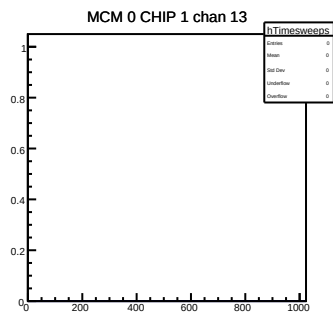
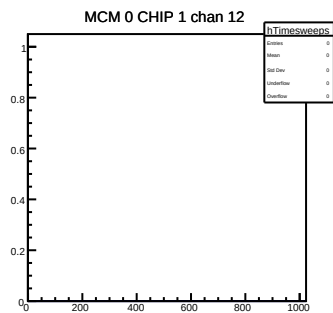
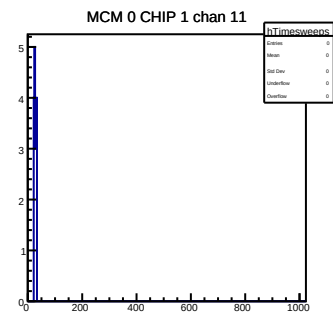
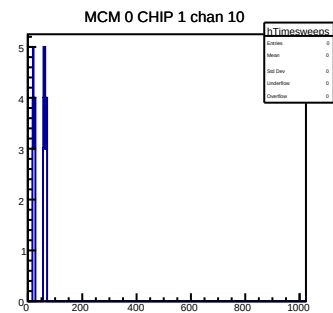
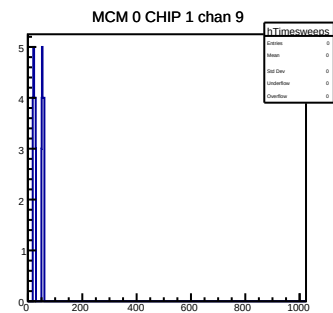
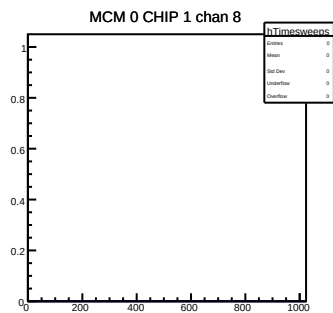
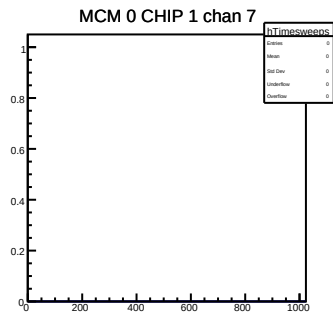
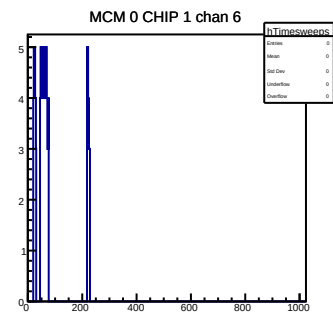
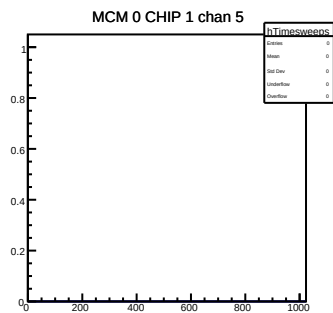
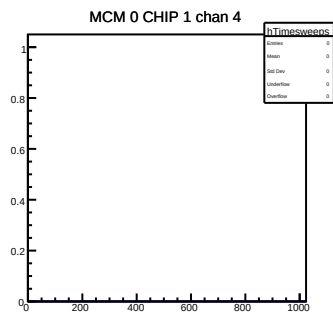
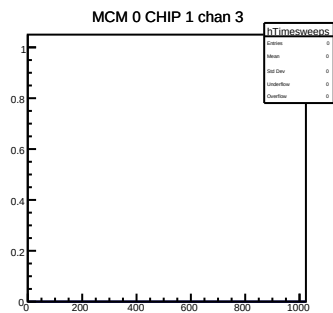
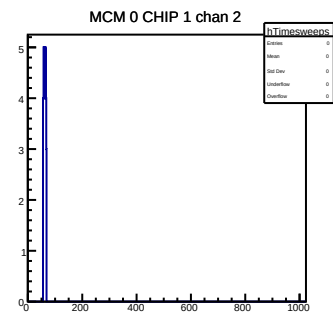
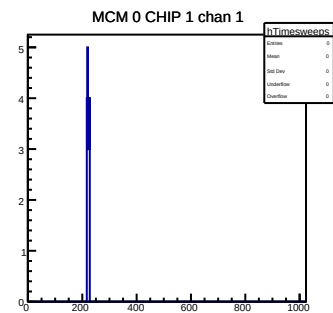
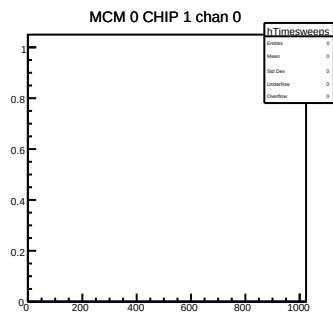
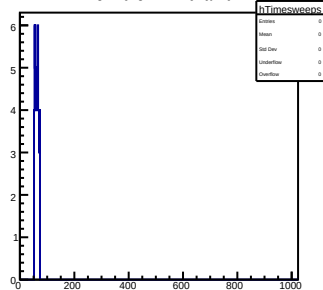


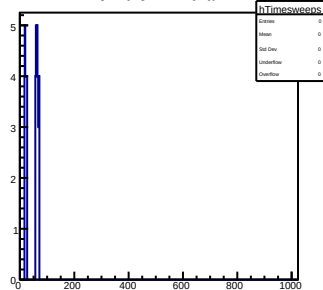




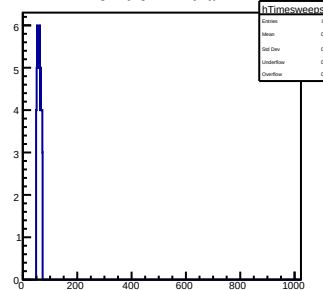
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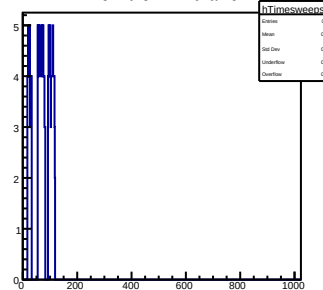
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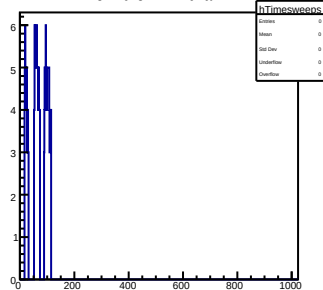
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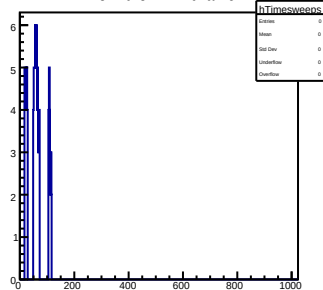
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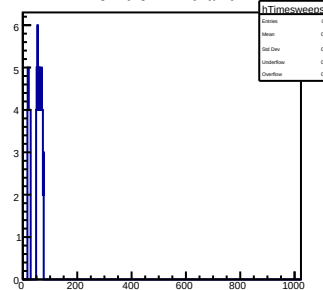
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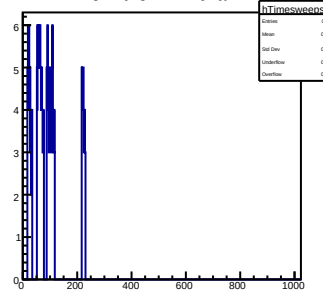
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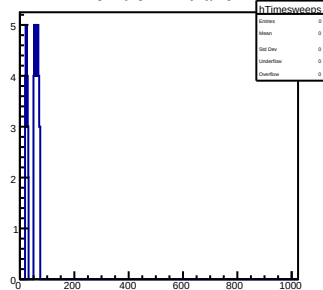
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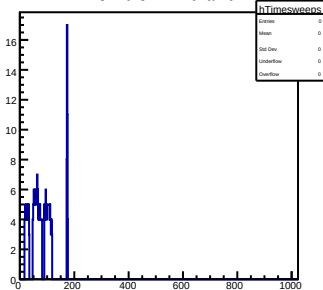
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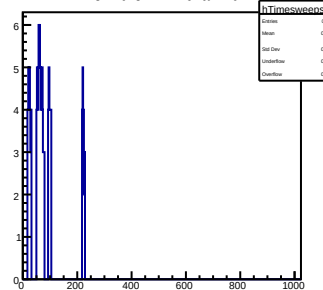
MCM 0 CHIP 2 chan 8



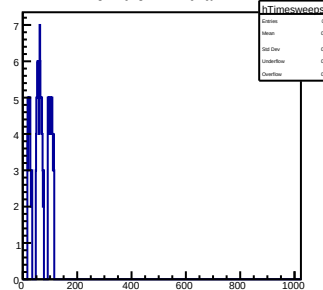
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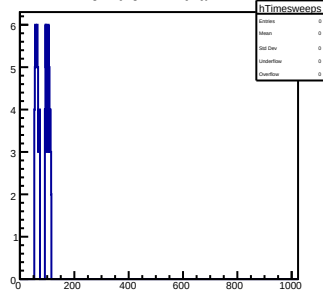
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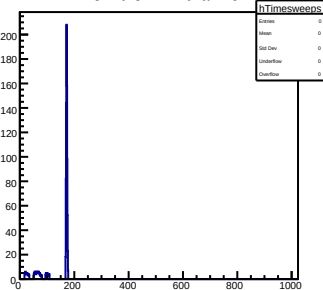
MCM 0 CHIP 2 chan 11



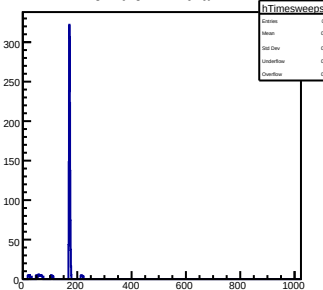
MCM 0 CHIP 2 chan 12



MCM 0 CHIP 2 chan 13



MCM 0 CHIP 2 chan 14



MCM 0 CHIP 2 chan 15

