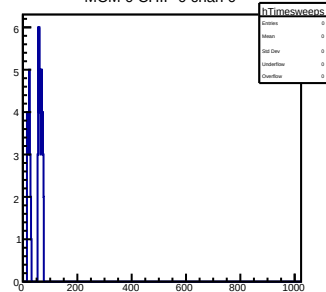
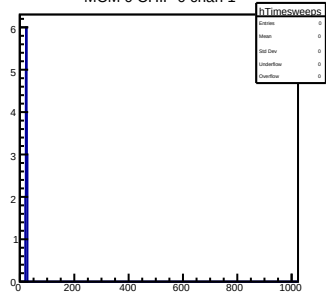


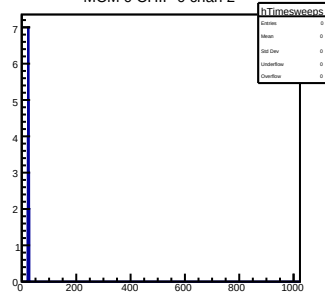
MCM 0 CHIP 0 chan 0



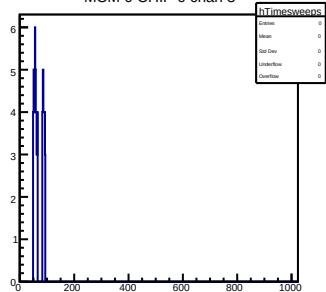
MCM 0 CHIP 0 chan 1



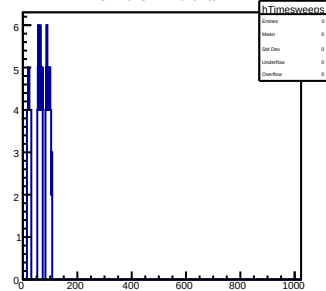
MCM 0 CHIP 0 chan 2



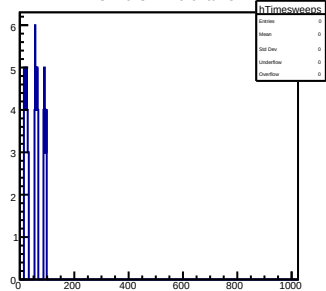
MCM 0 CHIP 0 chan 3



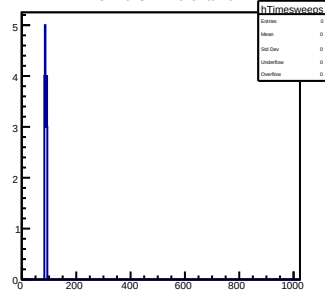
MCM 0 CHIP 0 chan 4



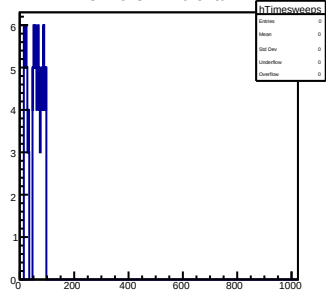
MCM 0 CHIP 0 chan 5



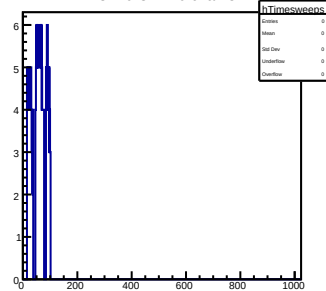
MCM 0 CHIP 0 chan 6



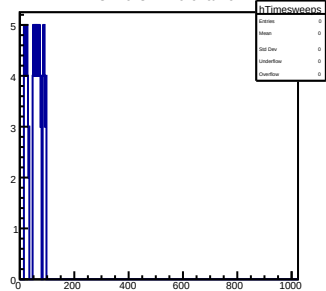
MCM 0 CHIP 0 chan 7



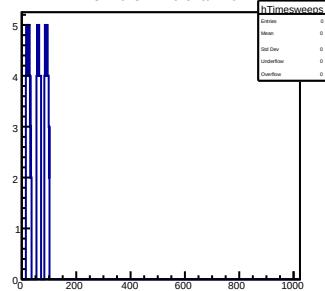
MCM 0 CHIP 0 chan 8



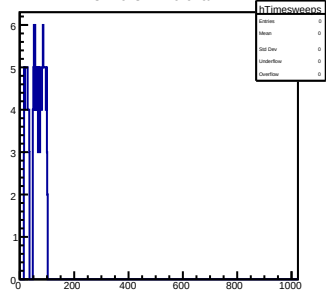
MCM 0 CHIP 0 chan 9



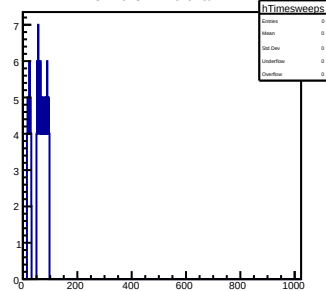
MCM 0 CHIP 0 chan 10



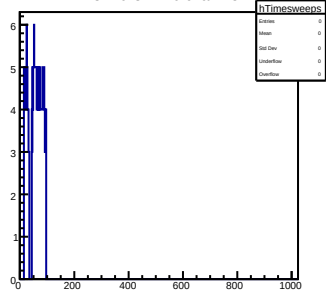
MCM 0 CHIP 0 chan 11



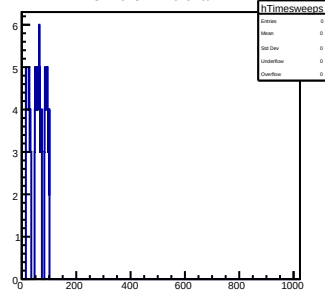
MCM 0 CHIP 0 chan 12



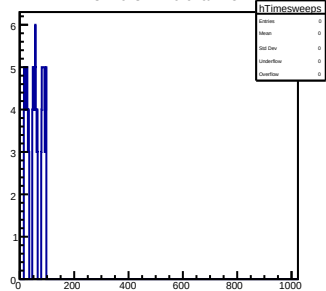
MCM 0 CHIP 0 chan 13

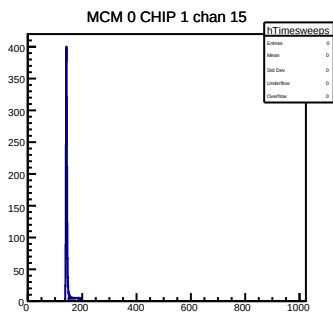
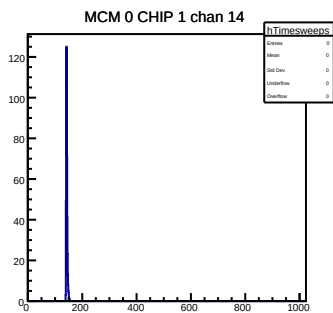
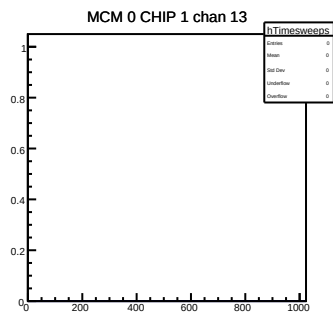
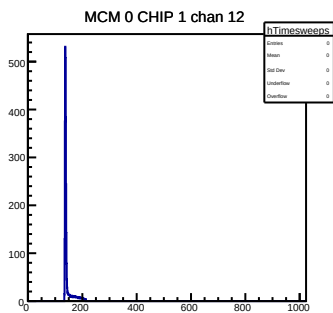
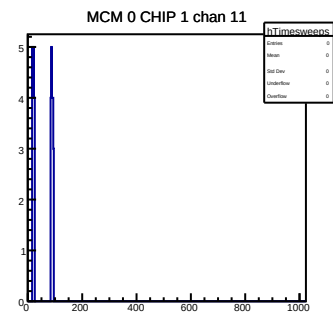
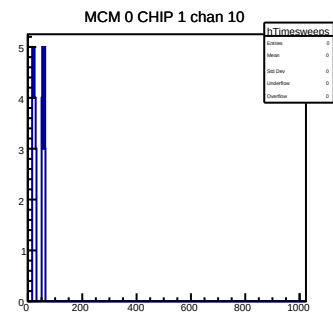
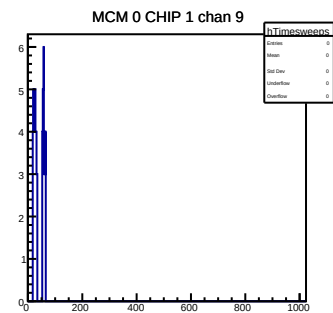
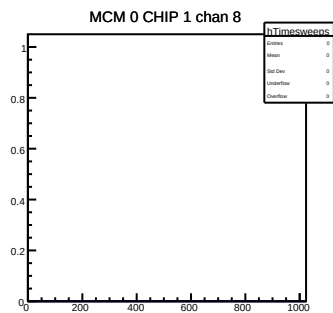
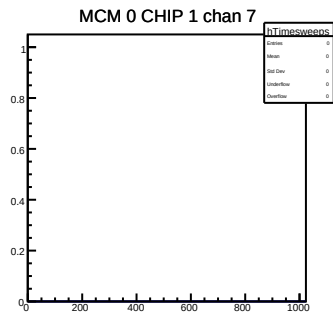
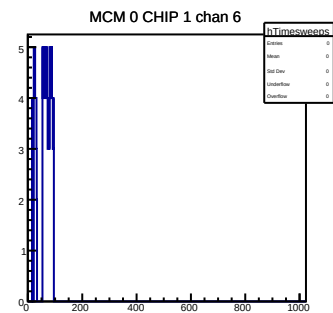
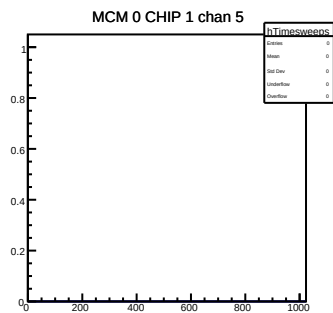
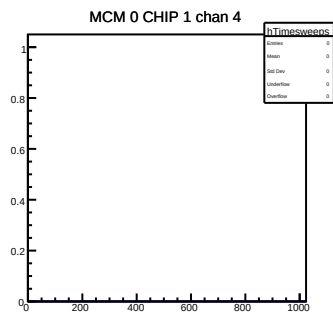
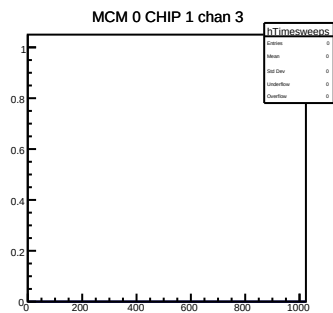
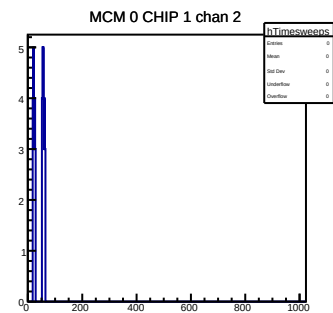
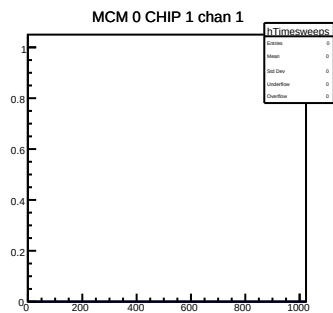
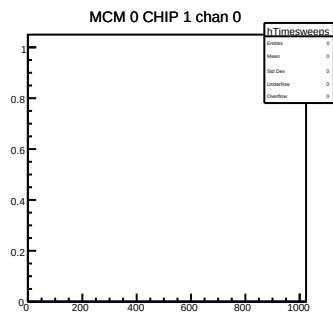


MCM 0 CHIP 0 chan 14

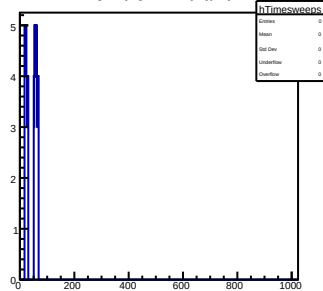


MCM 0 CHIP 0 chan 15

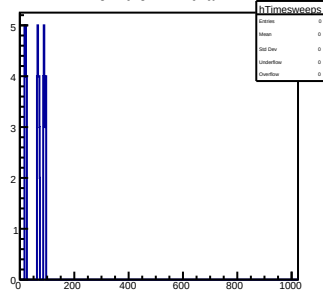




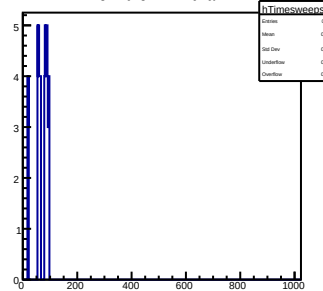
MCM 0 CHIP 2 chan 0



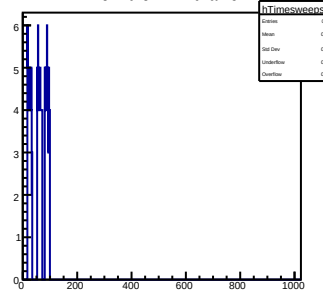
MCM 0 CHIP 2 chan 1



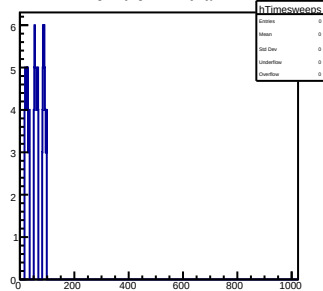
MCM 0 CHIP 2 chan 2



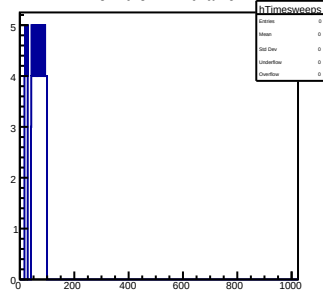
MCM 0 CHIP 2 chan 3



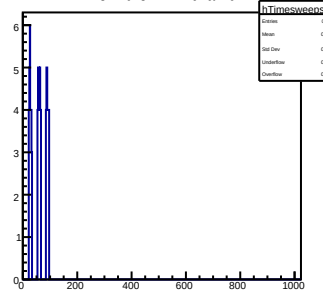
MCM 0 CHIP 2 chan 4



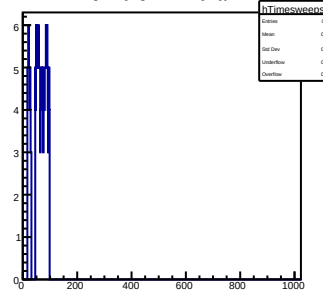
MCM 0 CHIP 2 chan 5



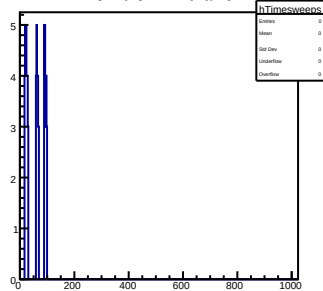
MCM 0 CHIP 2 chan 6



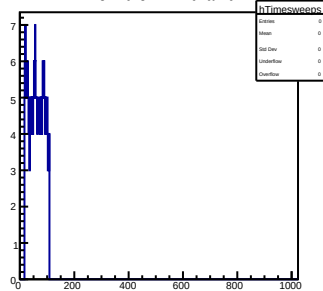
MCM 0 CHIP 2 chan 7



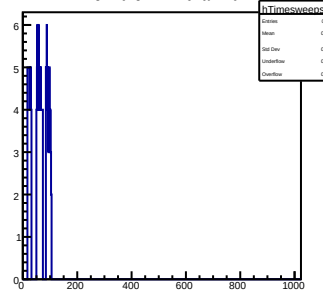
MCM 0 CHIP 2 chan 8



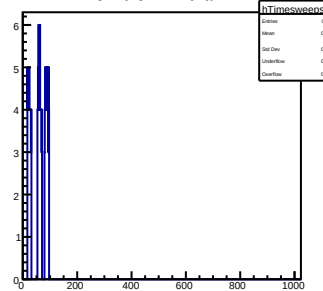
MCM 0 CHIP 2 chan 9



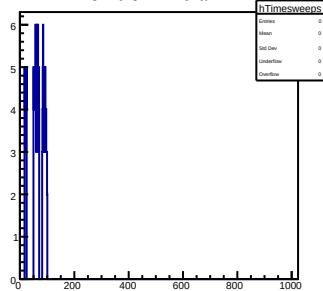
MCM 0 CHIP 2 chan 10



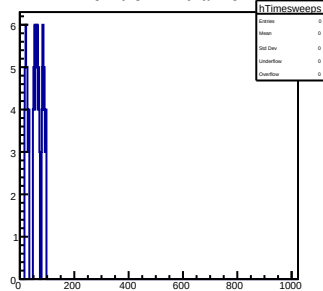
MCM 0 CHIP 2 chan 11



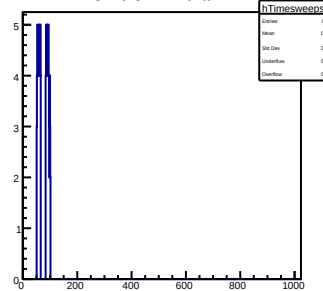
MCM 0 CHIP 2 chan 12



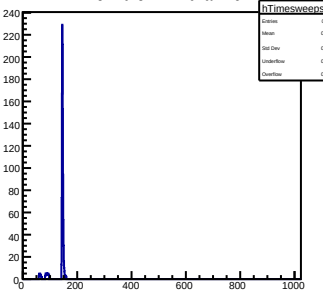
MCM 0 CHIP 2 chan 13

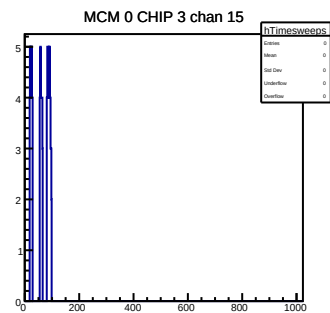
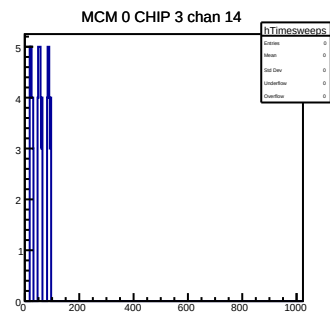
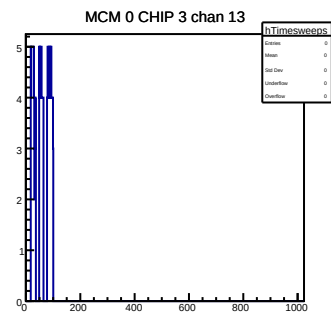
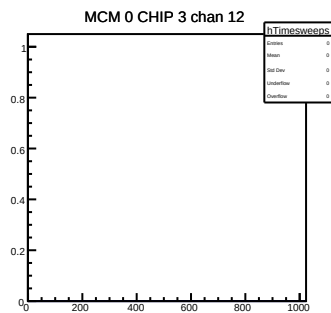
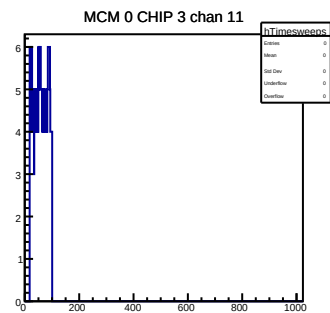
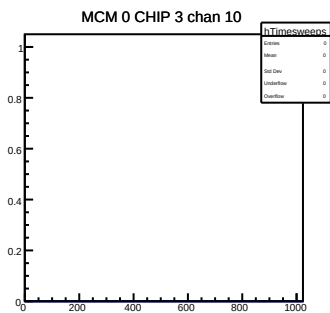
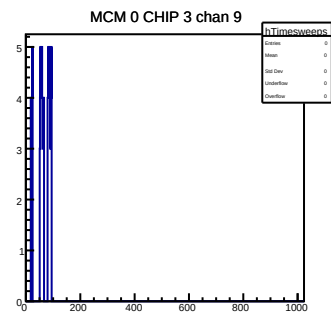
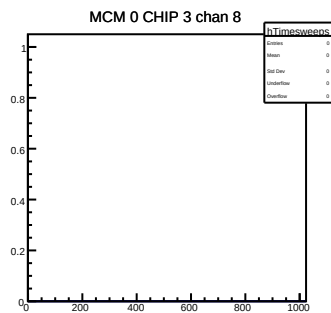
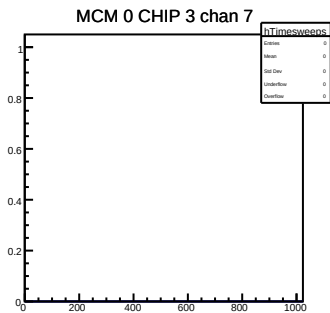
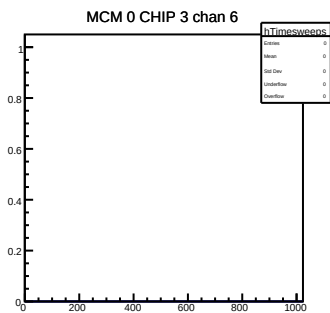
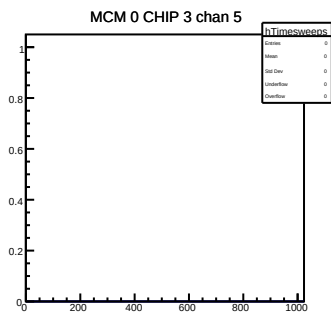
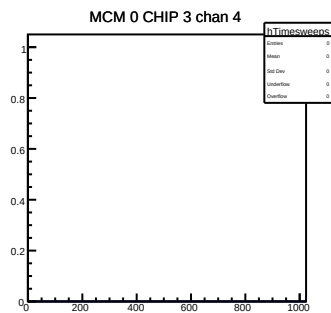
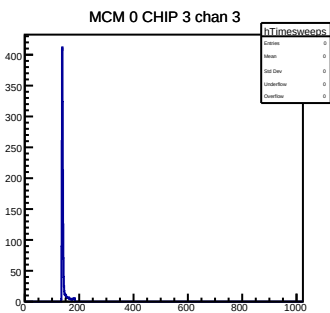
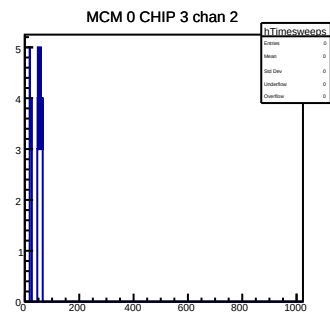
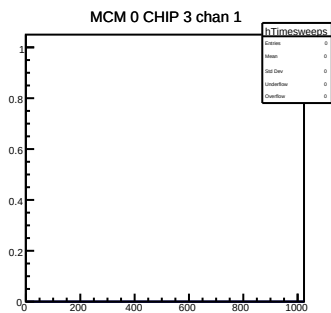
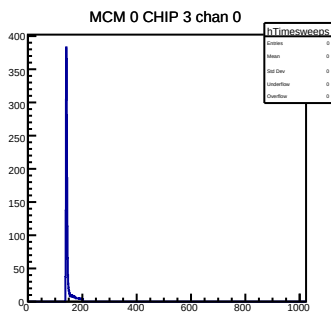


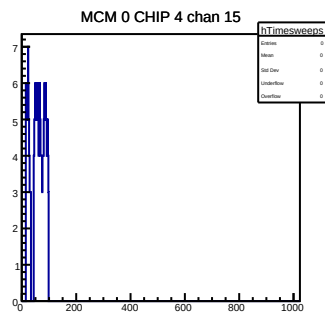
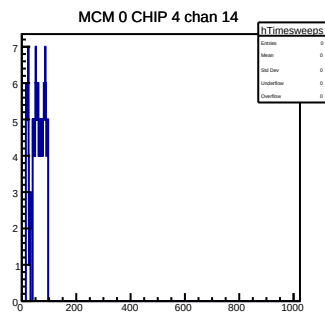
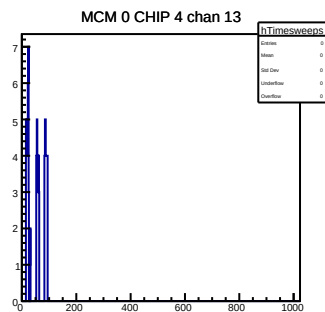
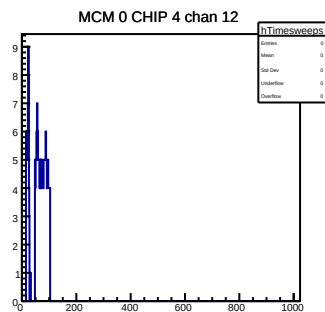
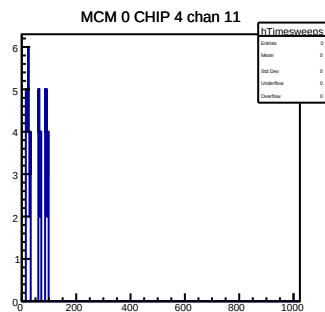
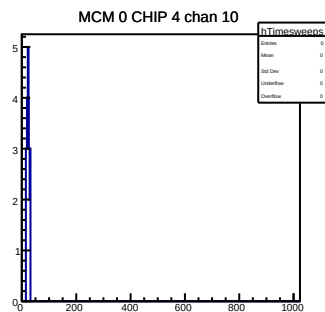
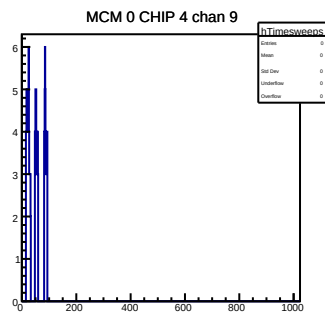
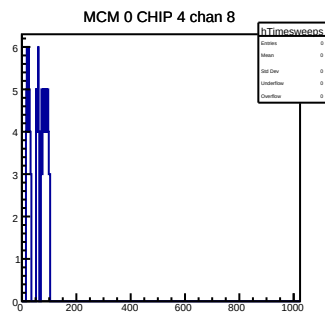
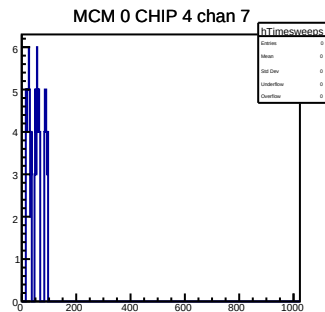
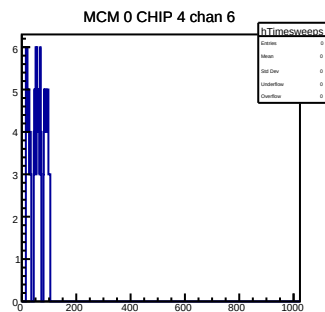
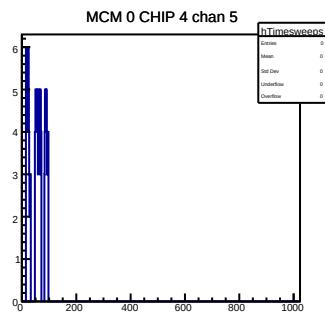
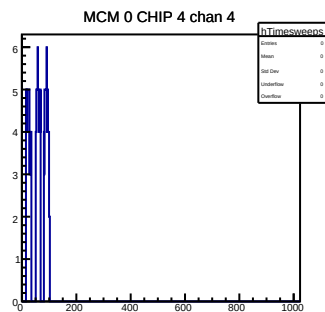
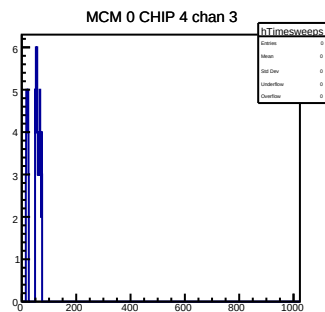
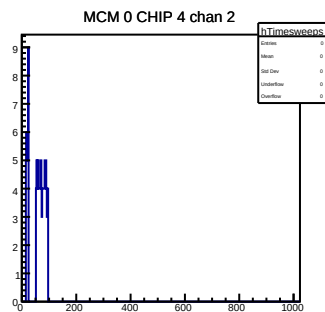
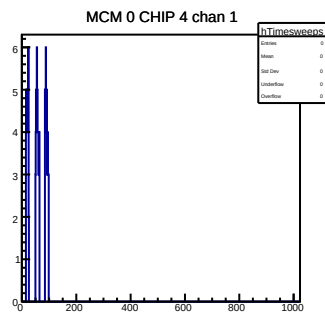
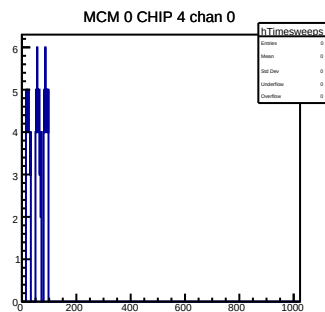
MCM 0 CHIP 2 chan 14

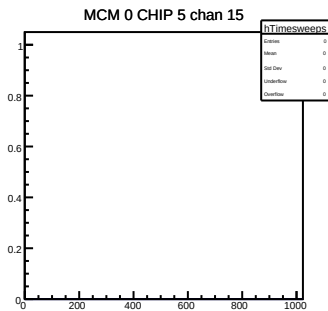
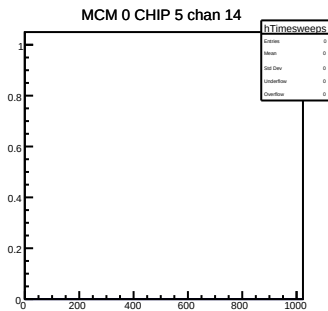
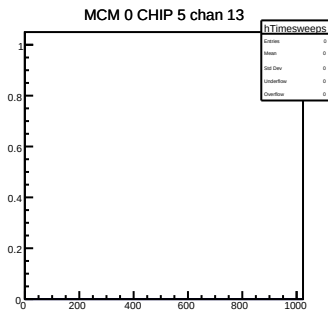
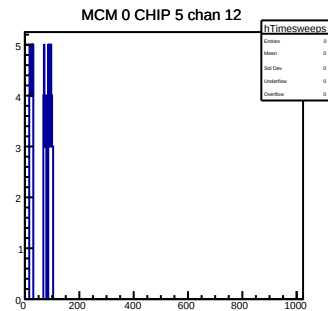
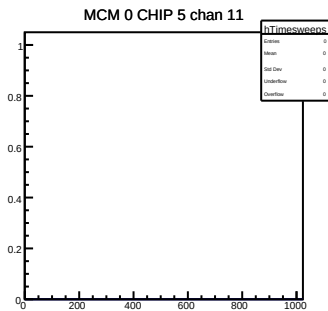
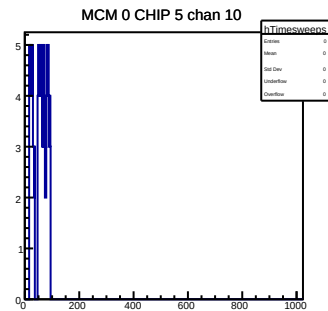
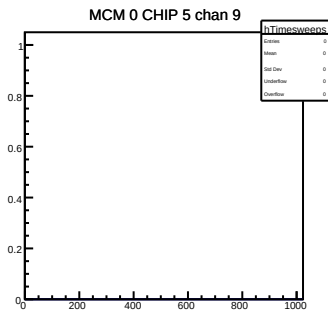
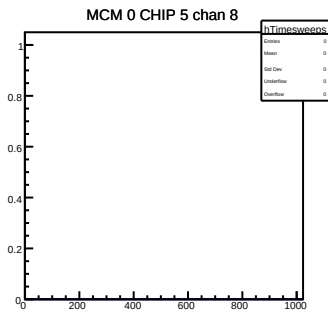
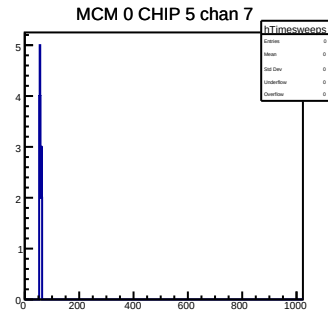
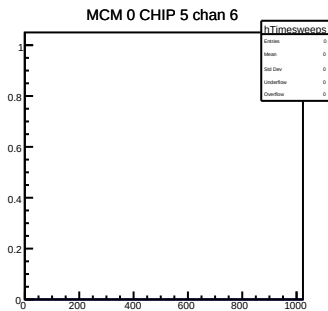
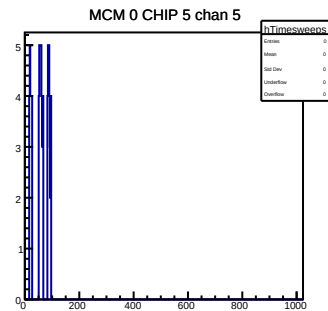
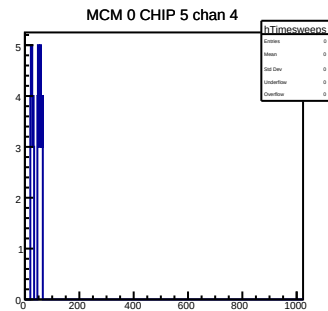
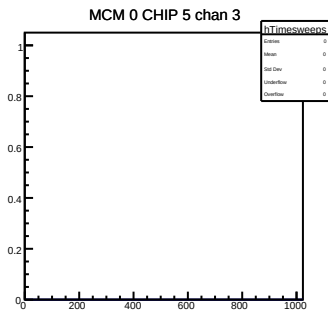
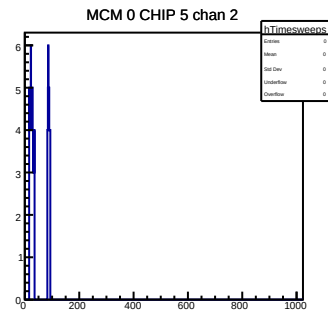
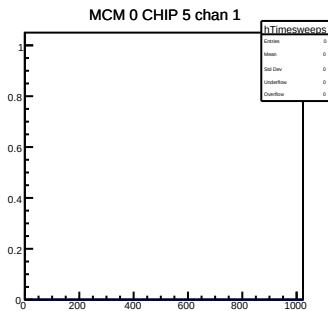
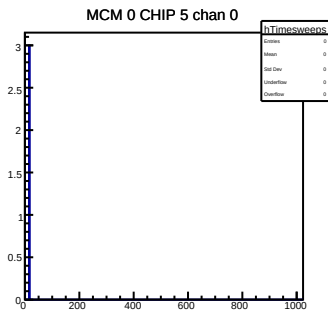


MCM 0 CHIP 2 chan 15

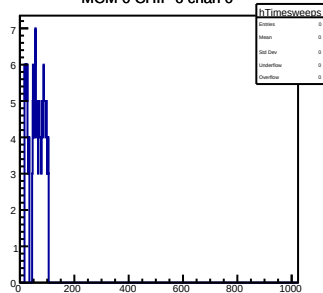




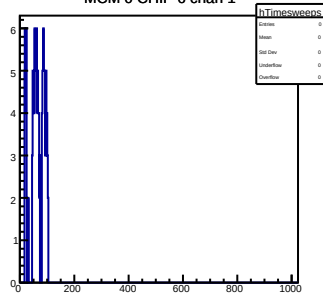




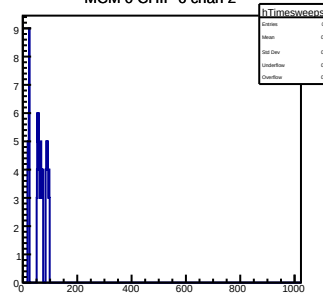
MCM 0 CHIP 6 chan 0



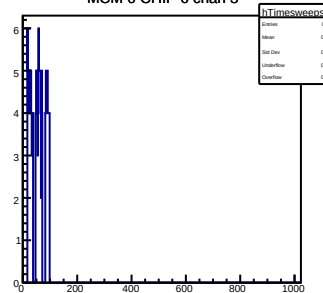
MCM 0 CHIP 6 chan 1



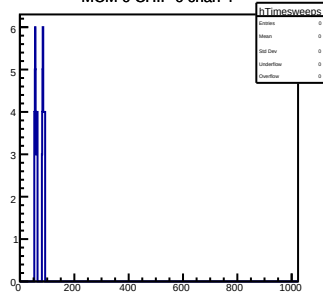
MCM 0 CHIP 6 chan 2



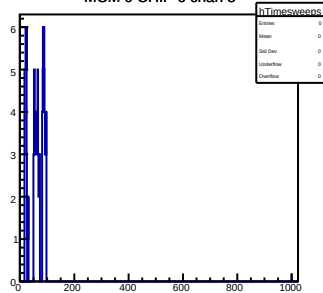
MCM 0 CHIP 6 chan 3



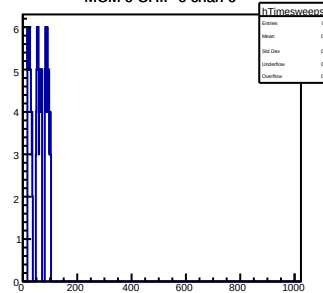
MCM 0 CHIP 6 chan 4



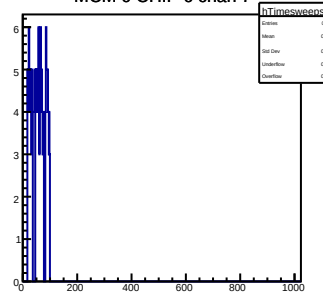
MCM 0 CHIP 6 chan 5



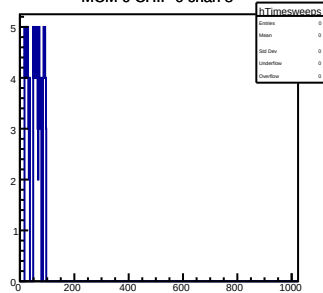
MCM 0 CHIP 6 chan 6



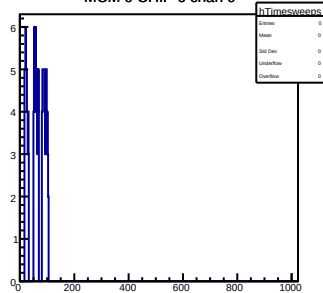
MCM 0 CHIP 6 chan 7



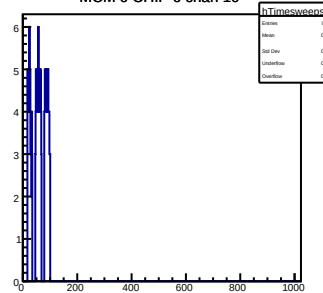
MCM 0 CHIP 6 chan 8



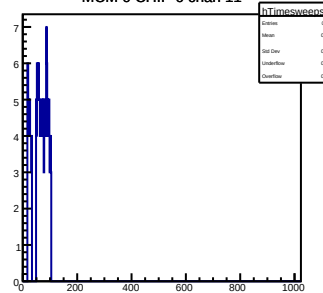
MCM 0 CHIP 6 chan 9



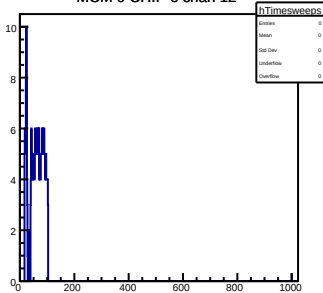
MCM 0 CHIP 6 chan 10



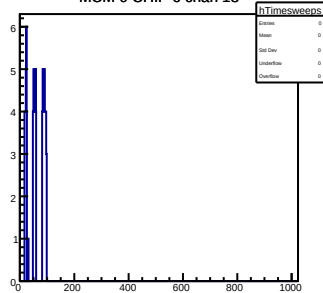
MCM 0 CHIP 6 chan 11



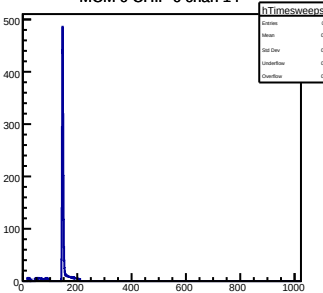
MCM 0 CHIP 6 chan 12



MCM 0 CHIP 6 chan 13



MCM 0 CHIP 6 chan 14



MCM 0 CHIP 6 chan 15

