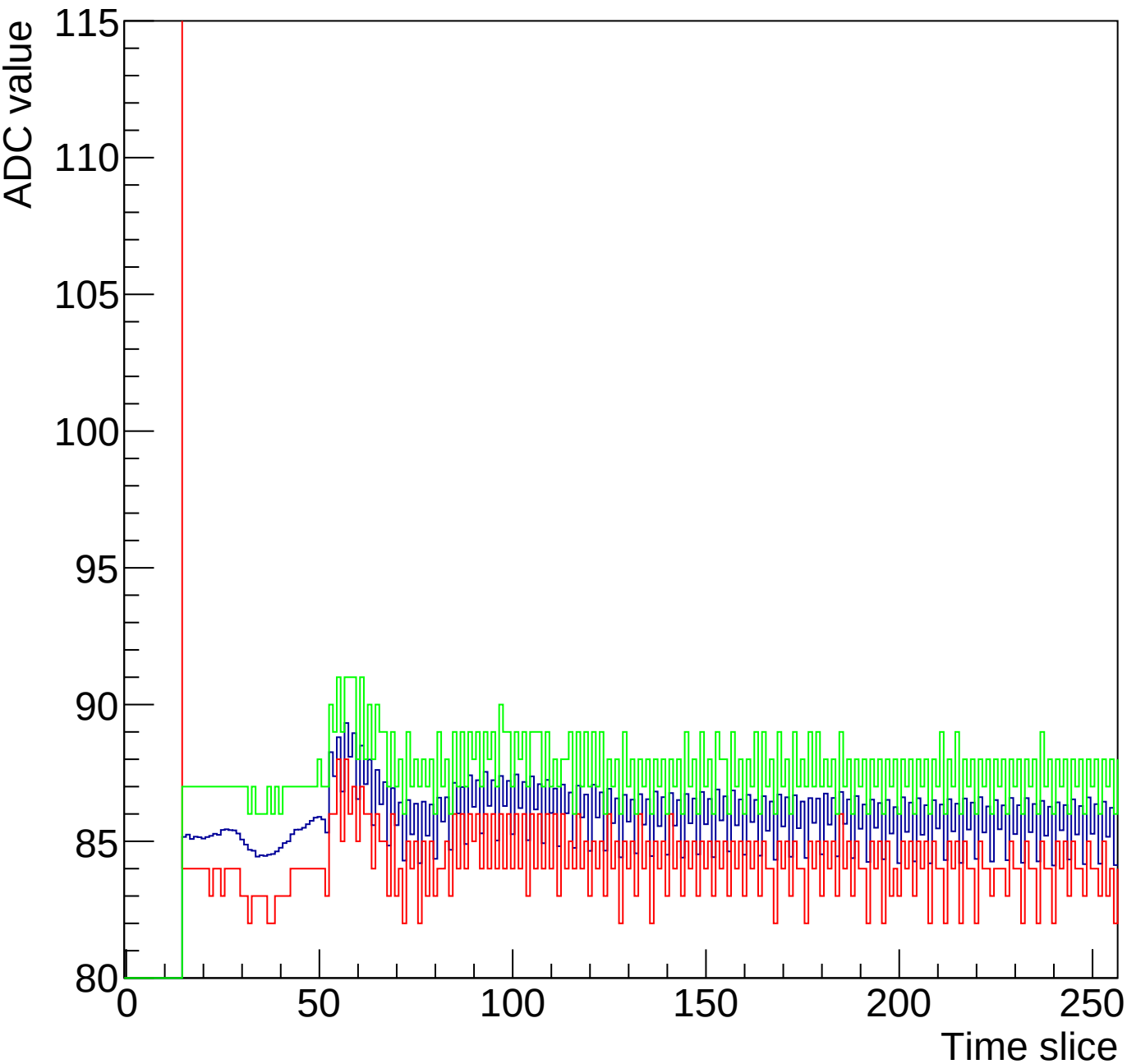
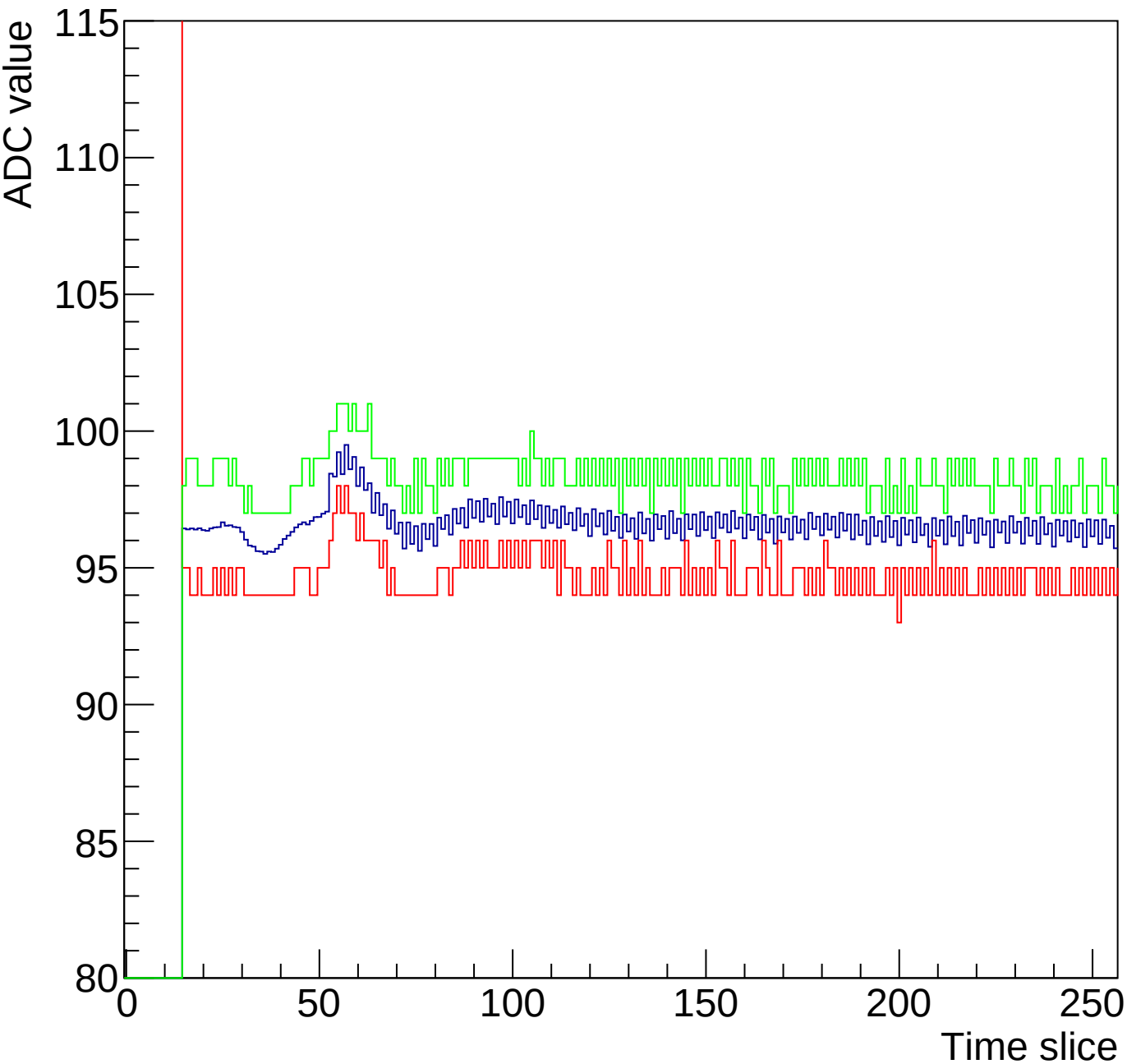


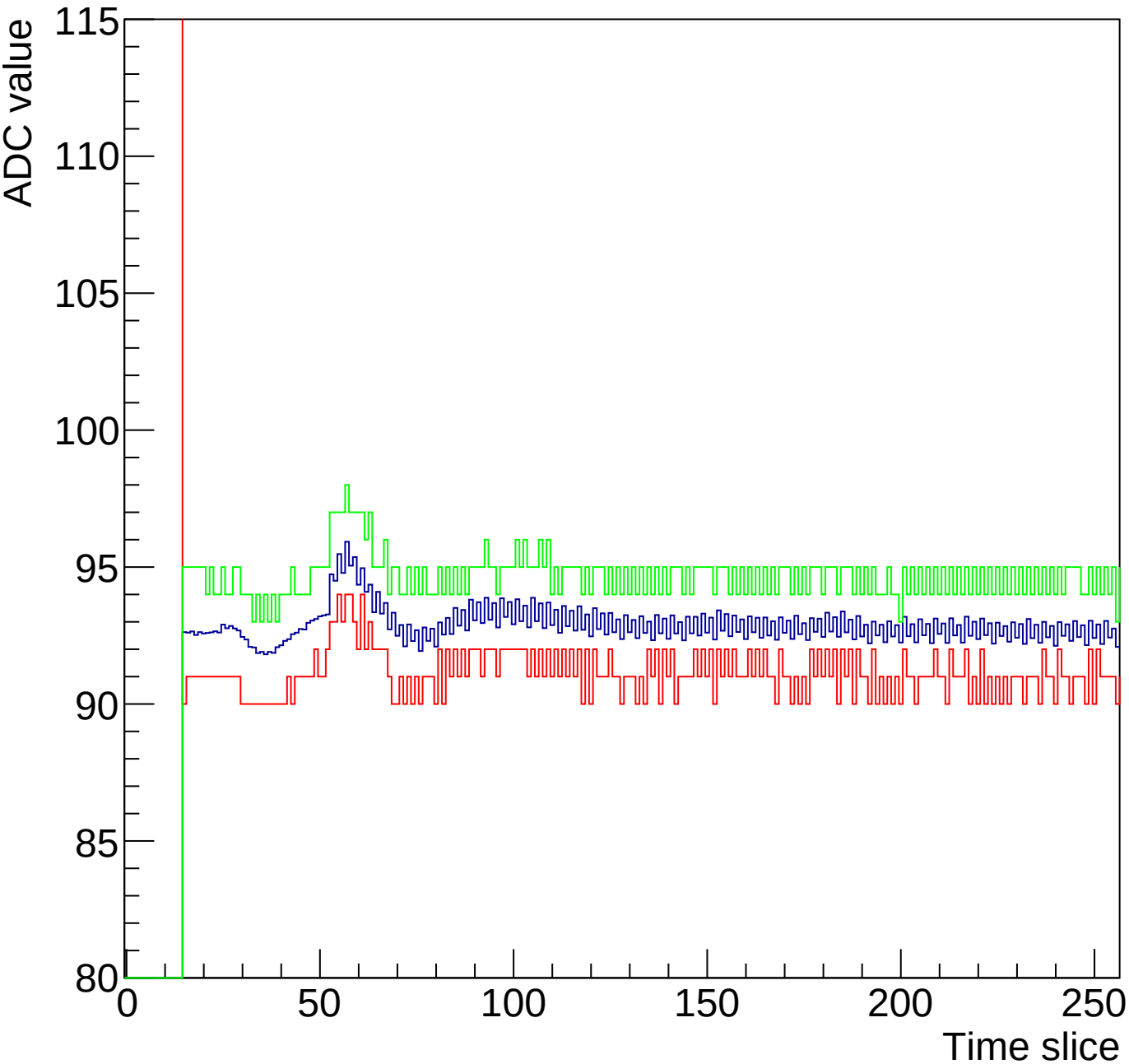
Run 1 Time mcm 0 chip 1 chan 0 256



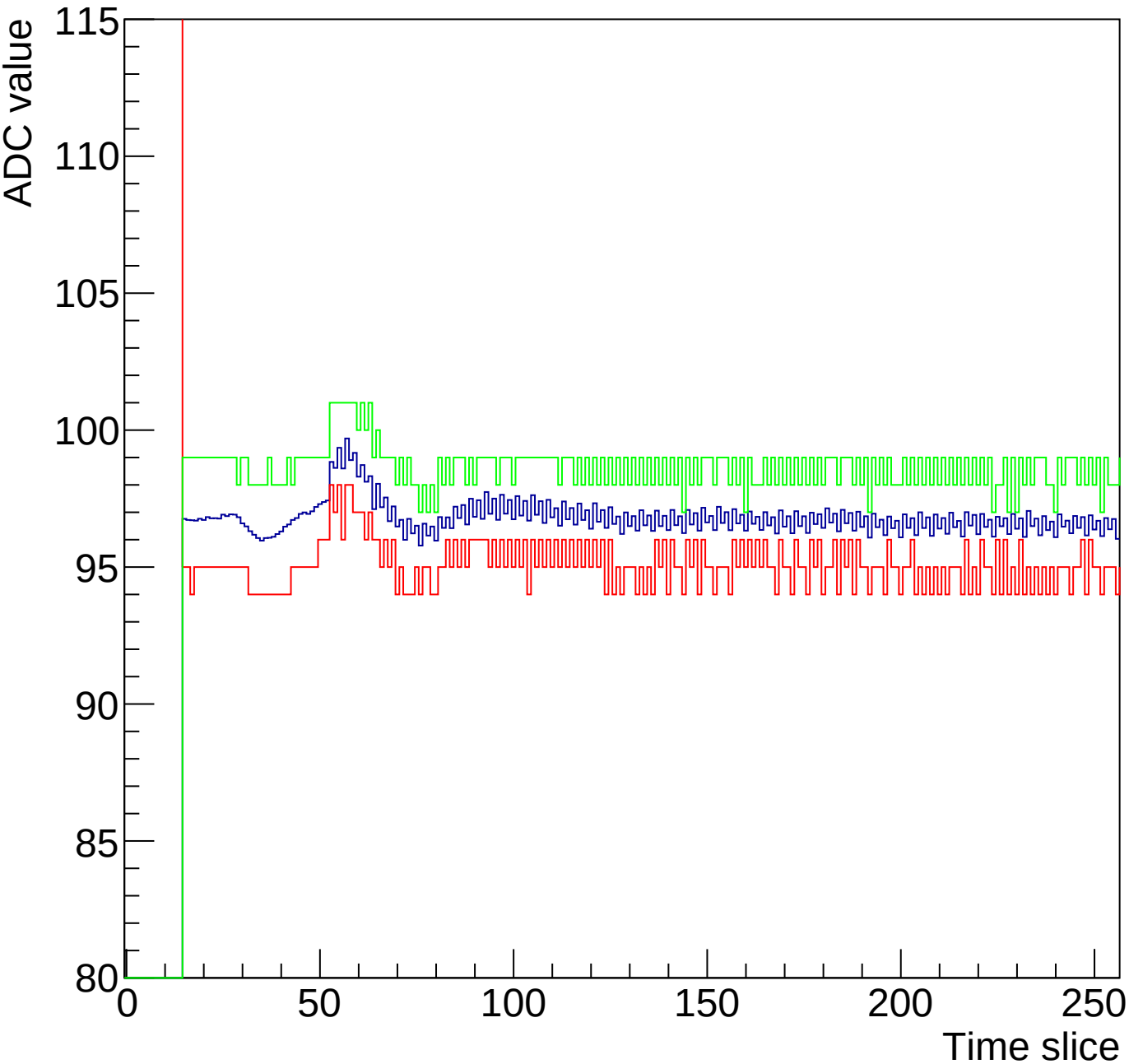
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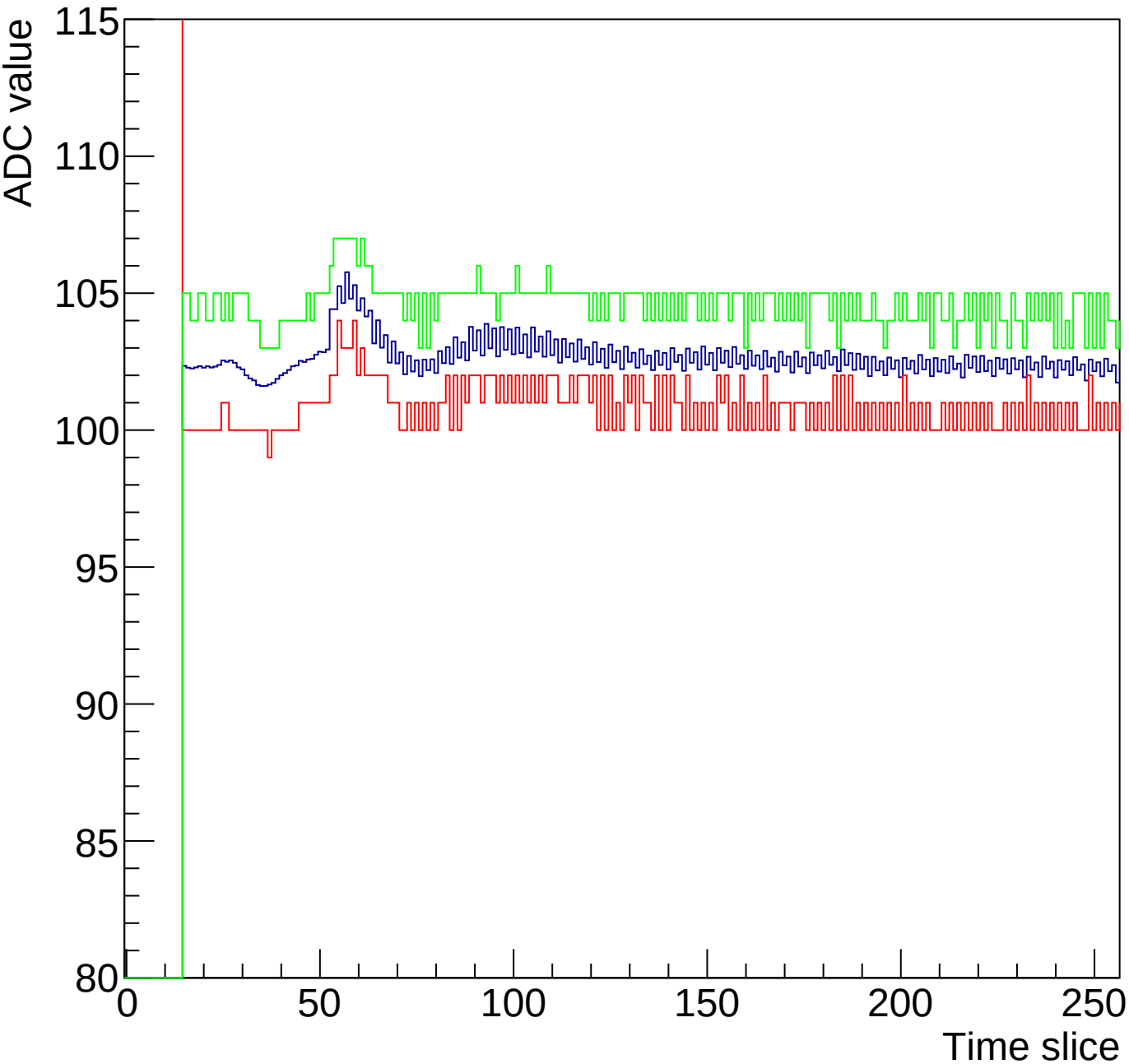
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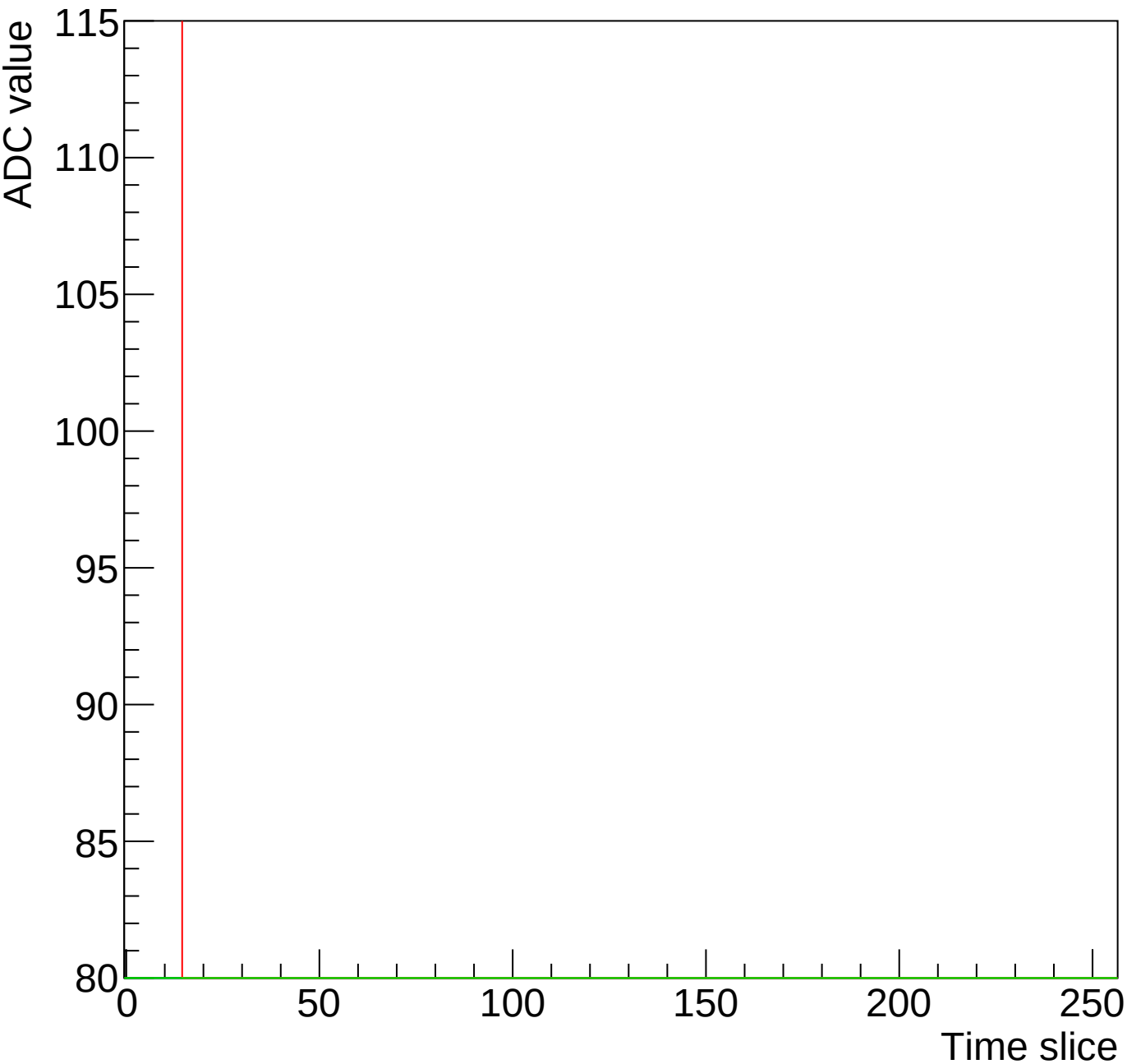
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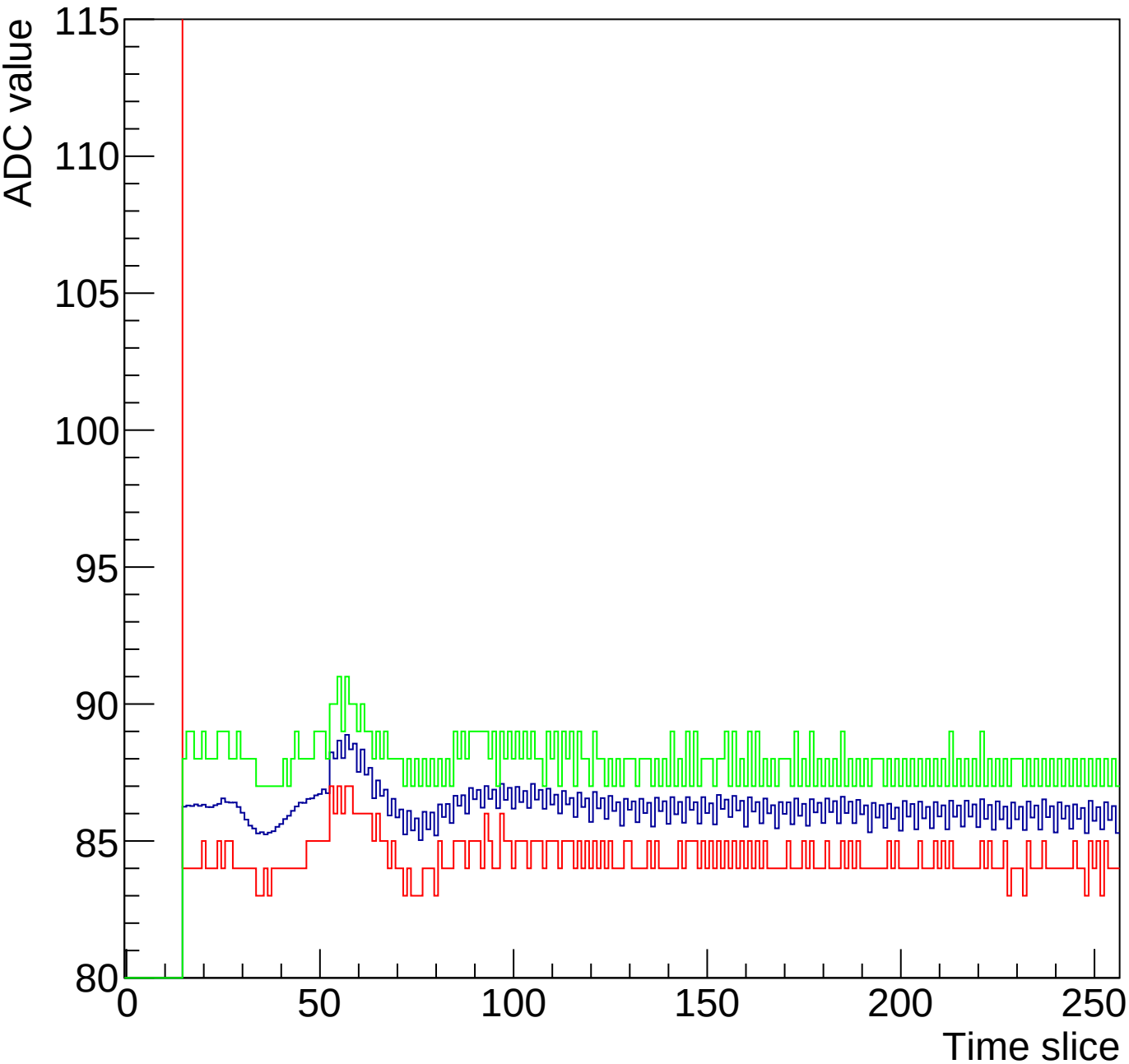
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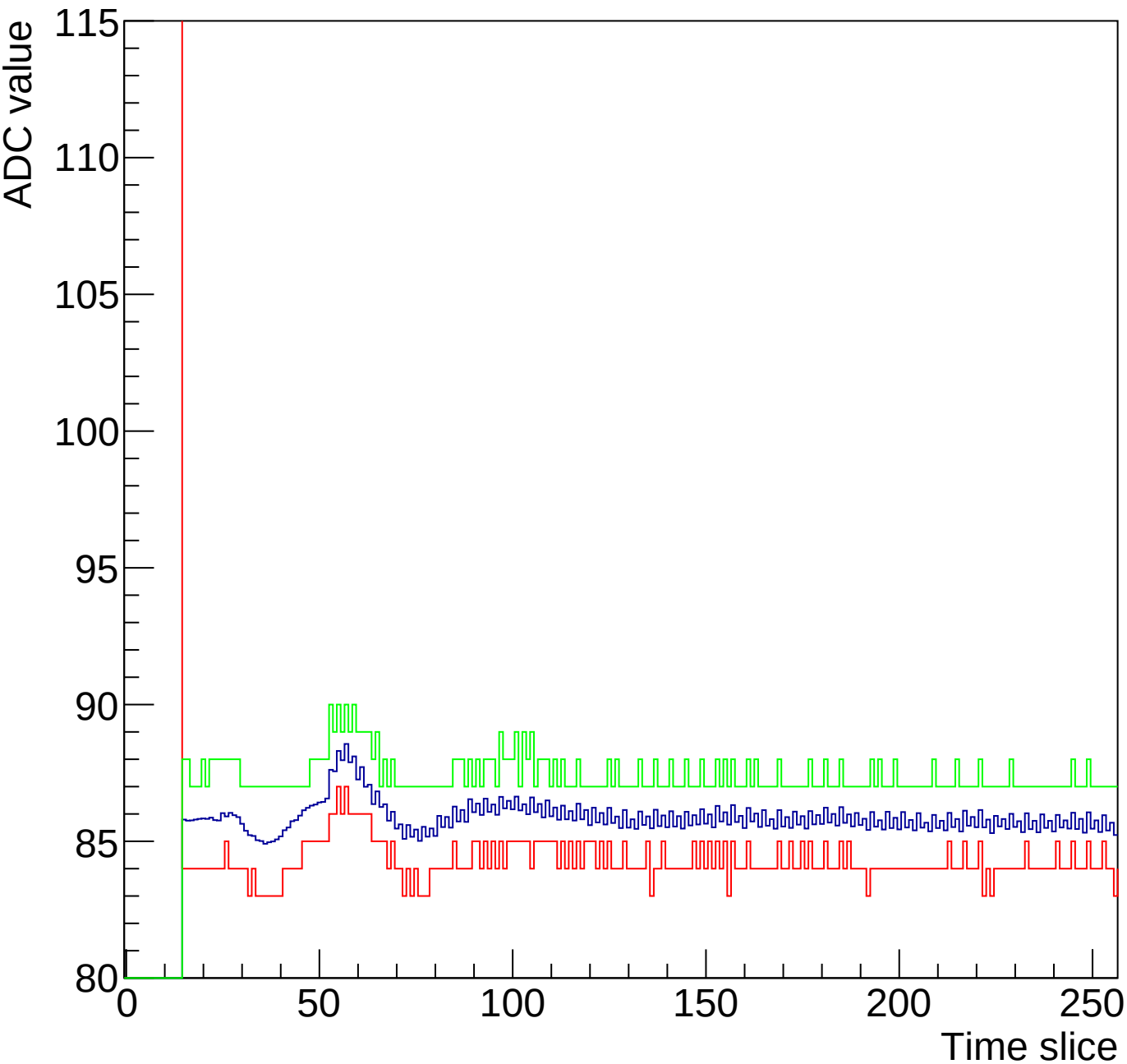
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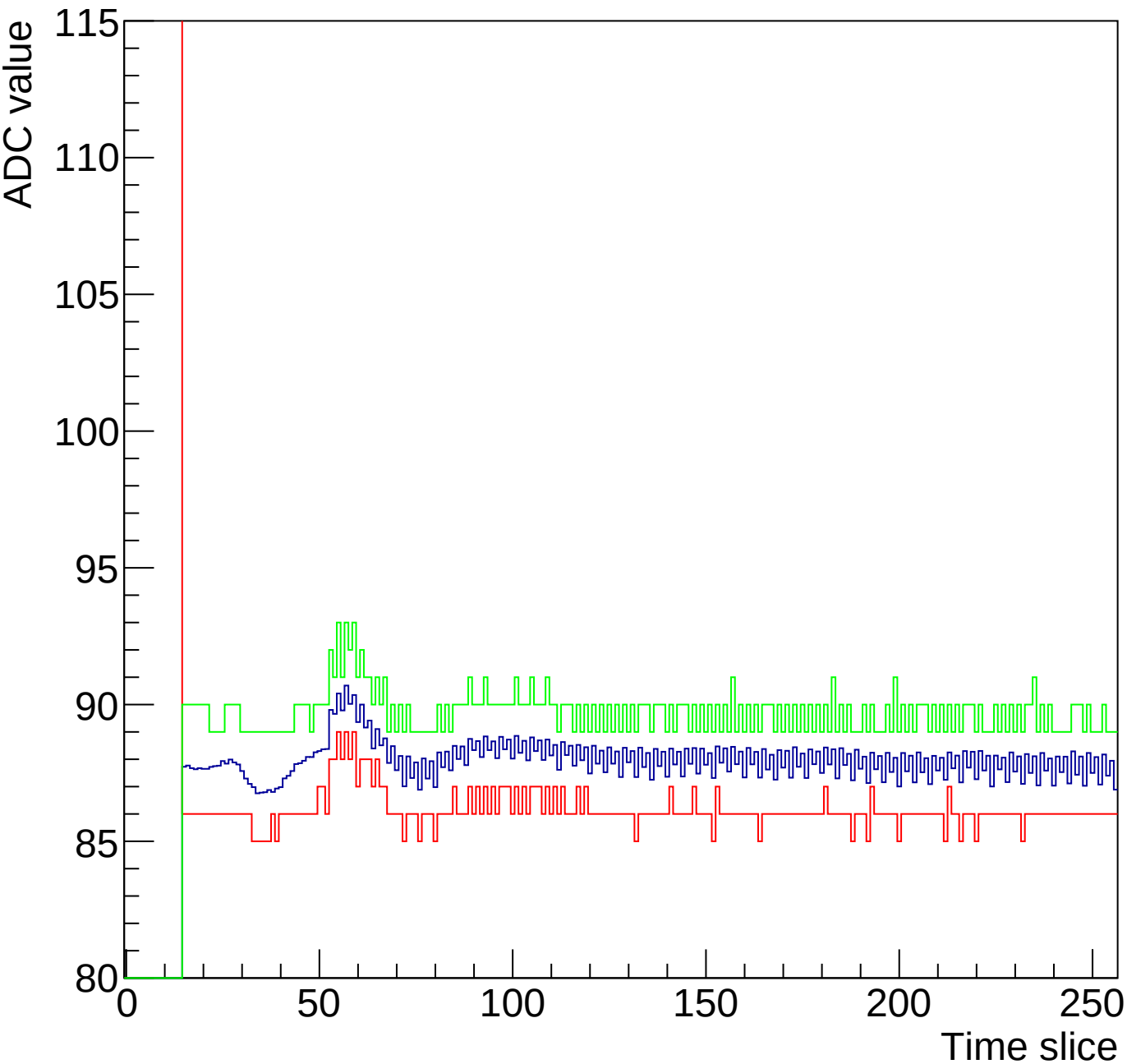
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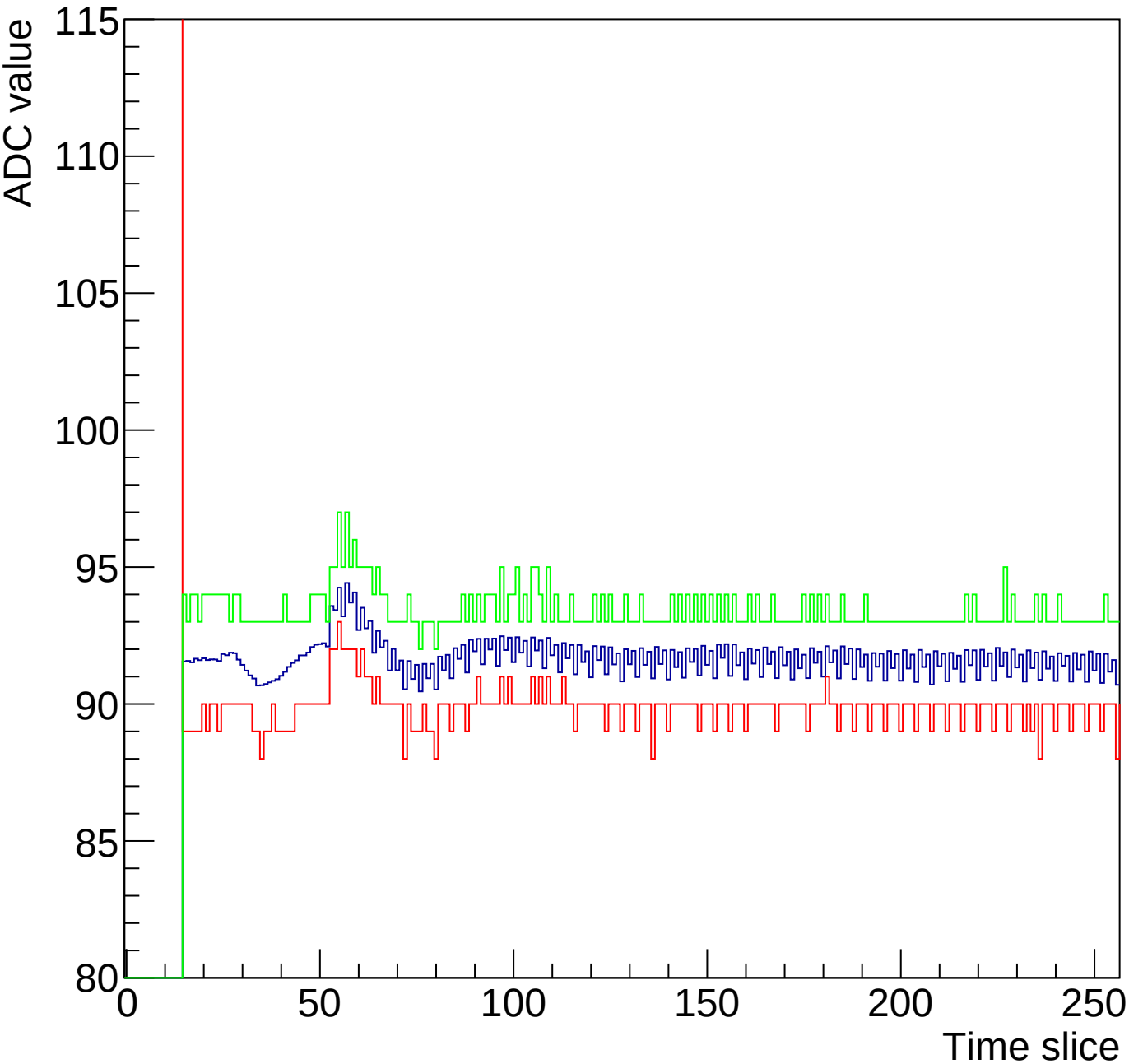
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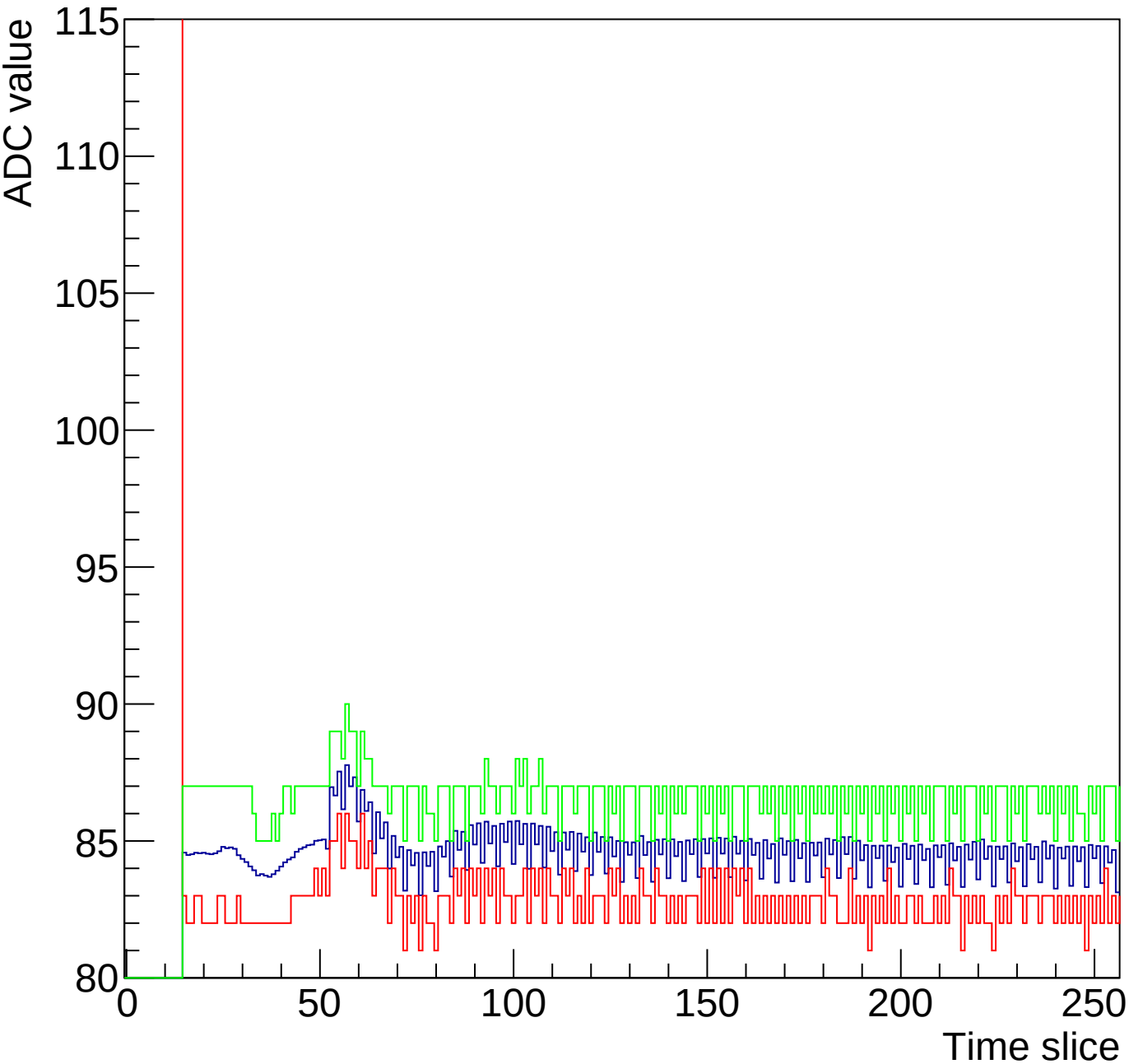
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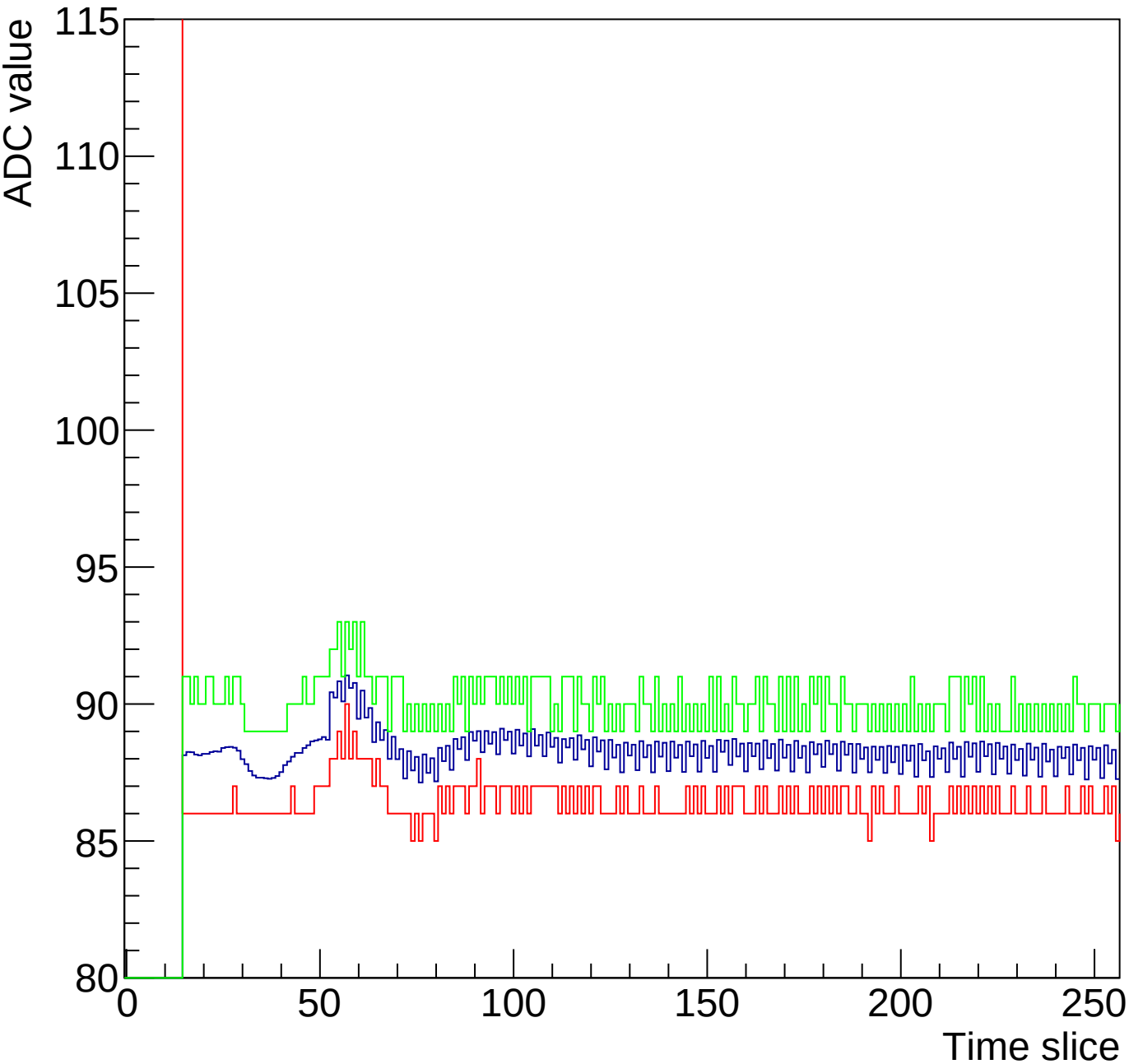
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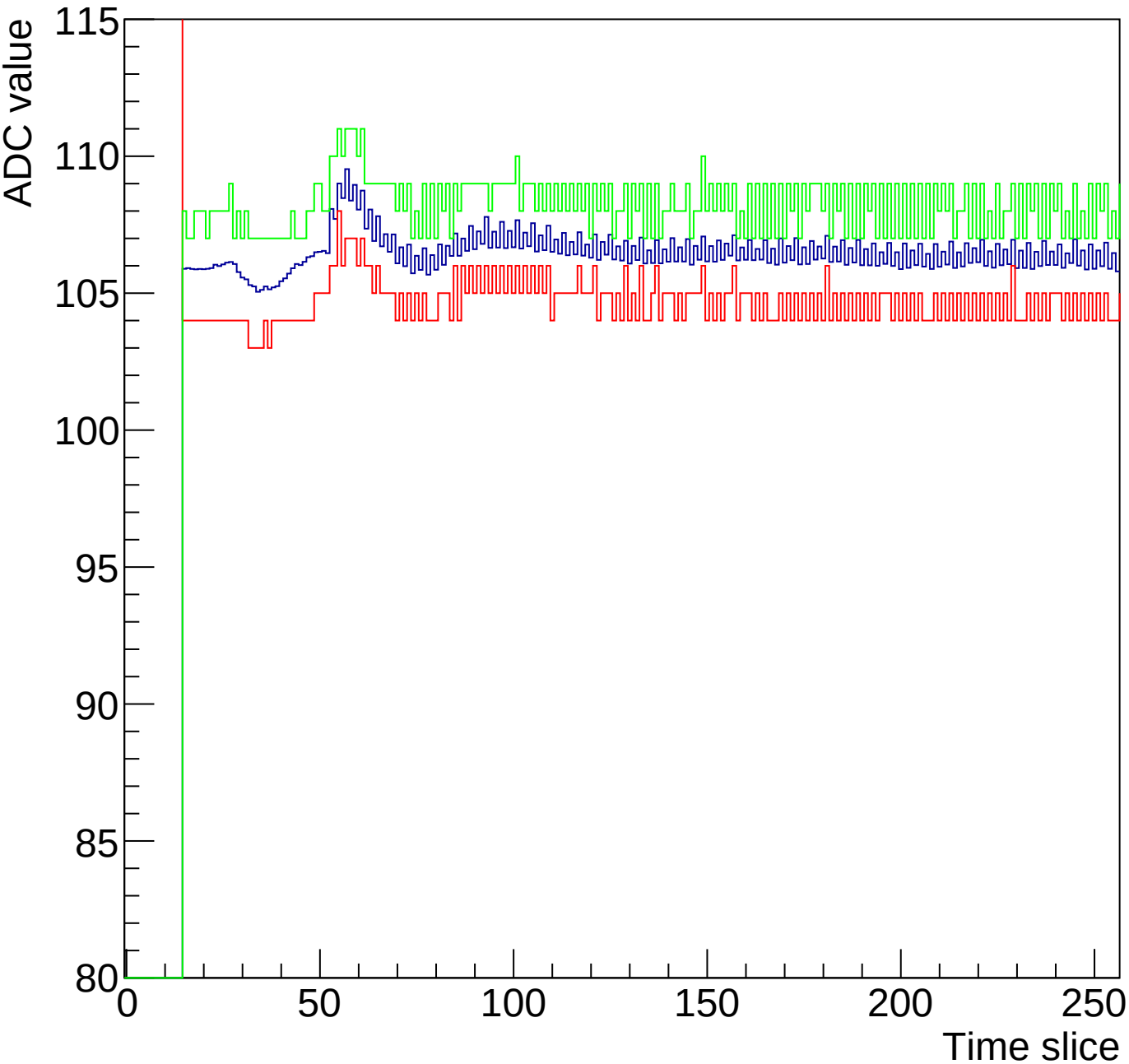
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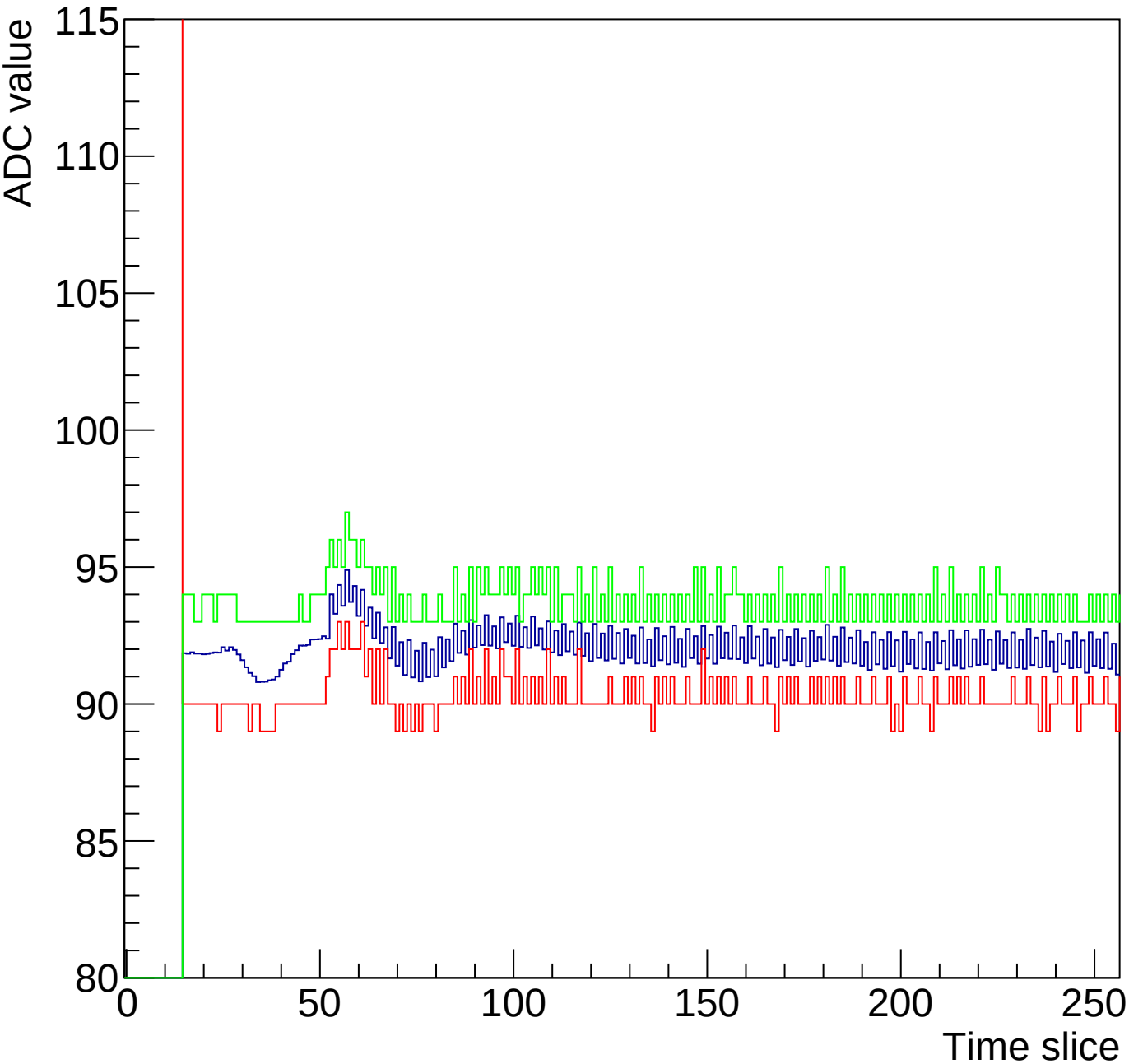
Run 1 Time mcm 0 chip 1 chan 11 256



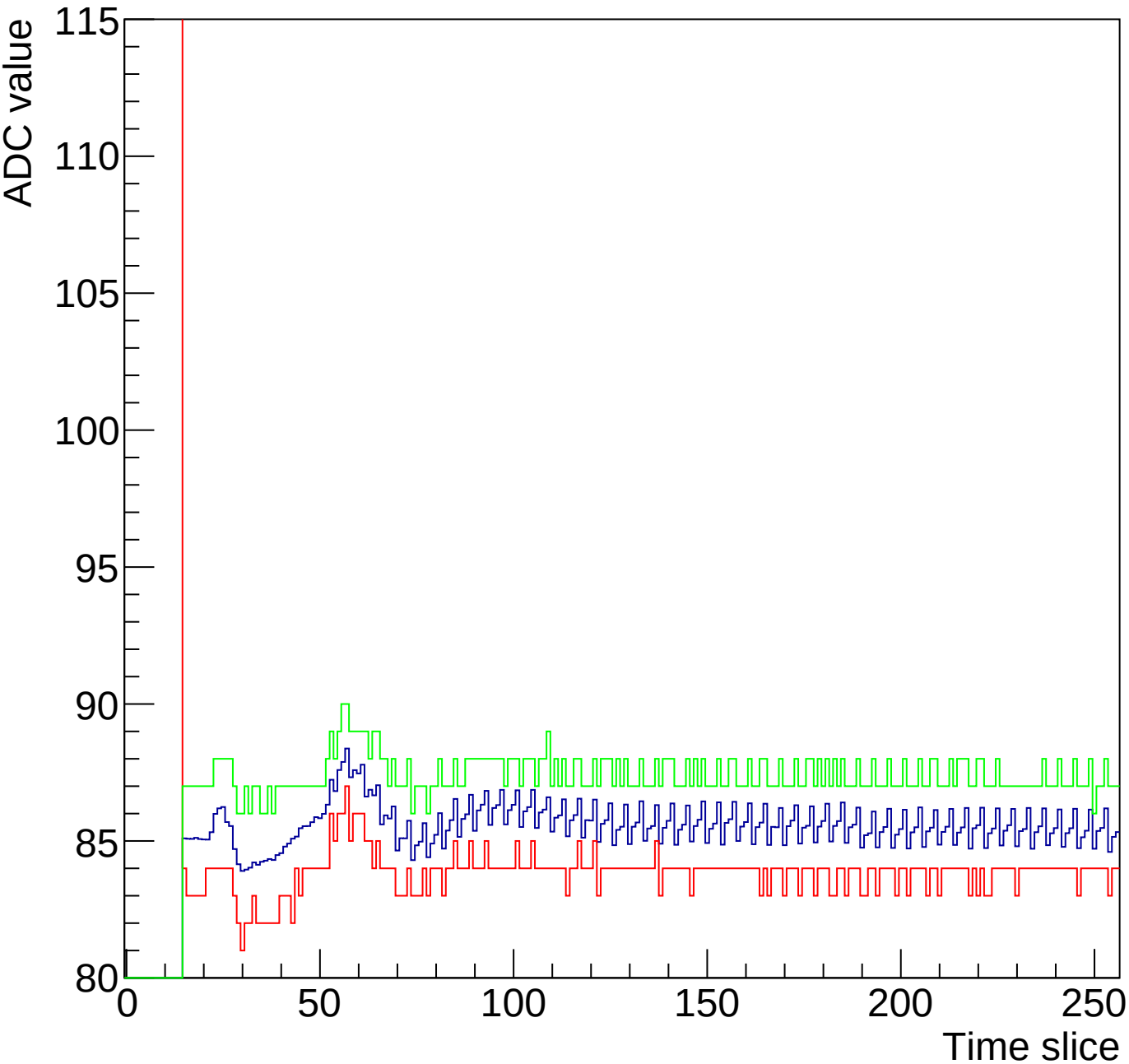
Run 1 Time mcm 0 chip 1 chan 12 256



Run 1 Time mcm 0 chip 1 chan 13 256



Run 1 Time mcm 0 chip 1 chan 14 256



Run 1 Time mcm 0 chip 1 chan 15 256

