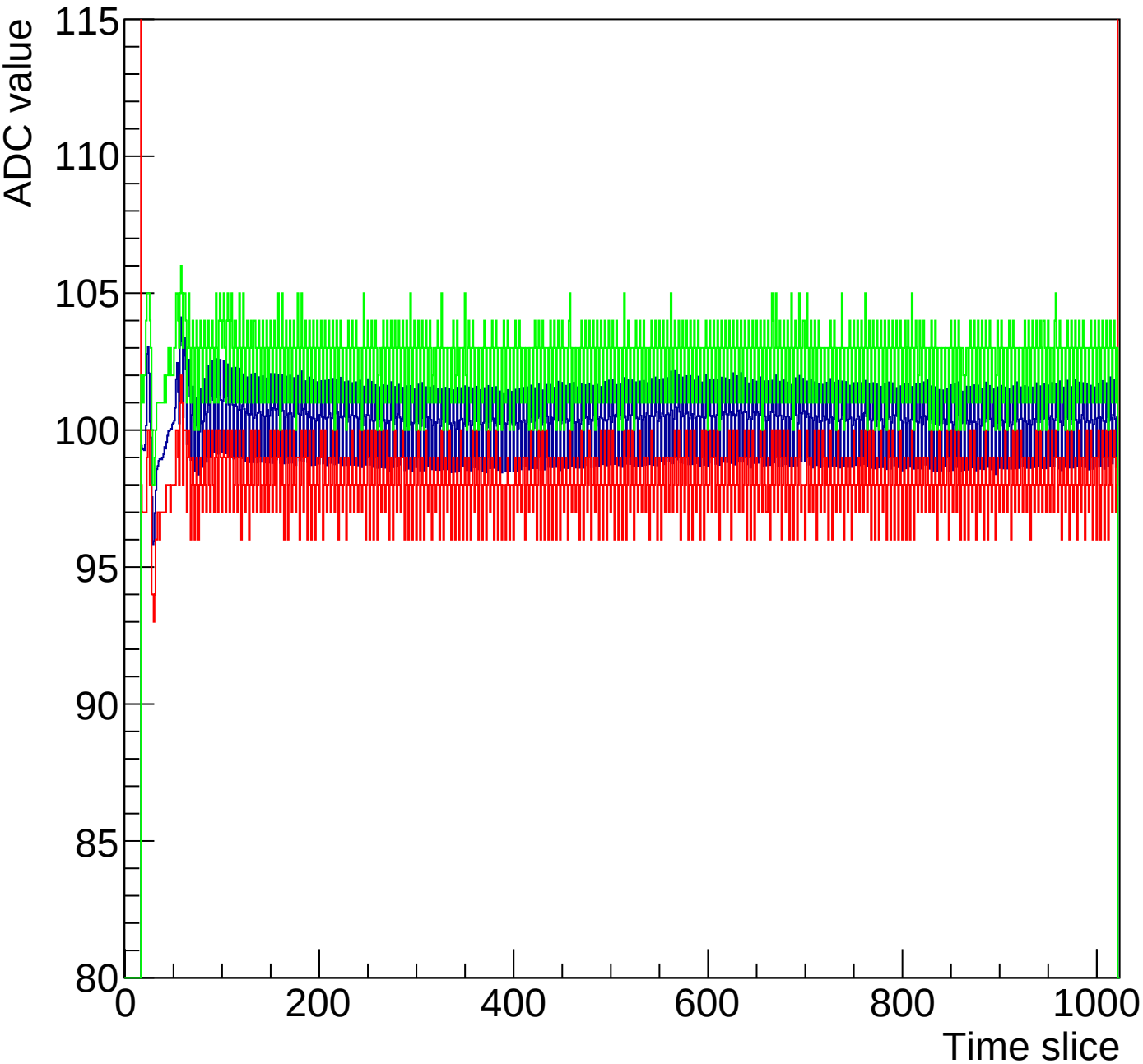
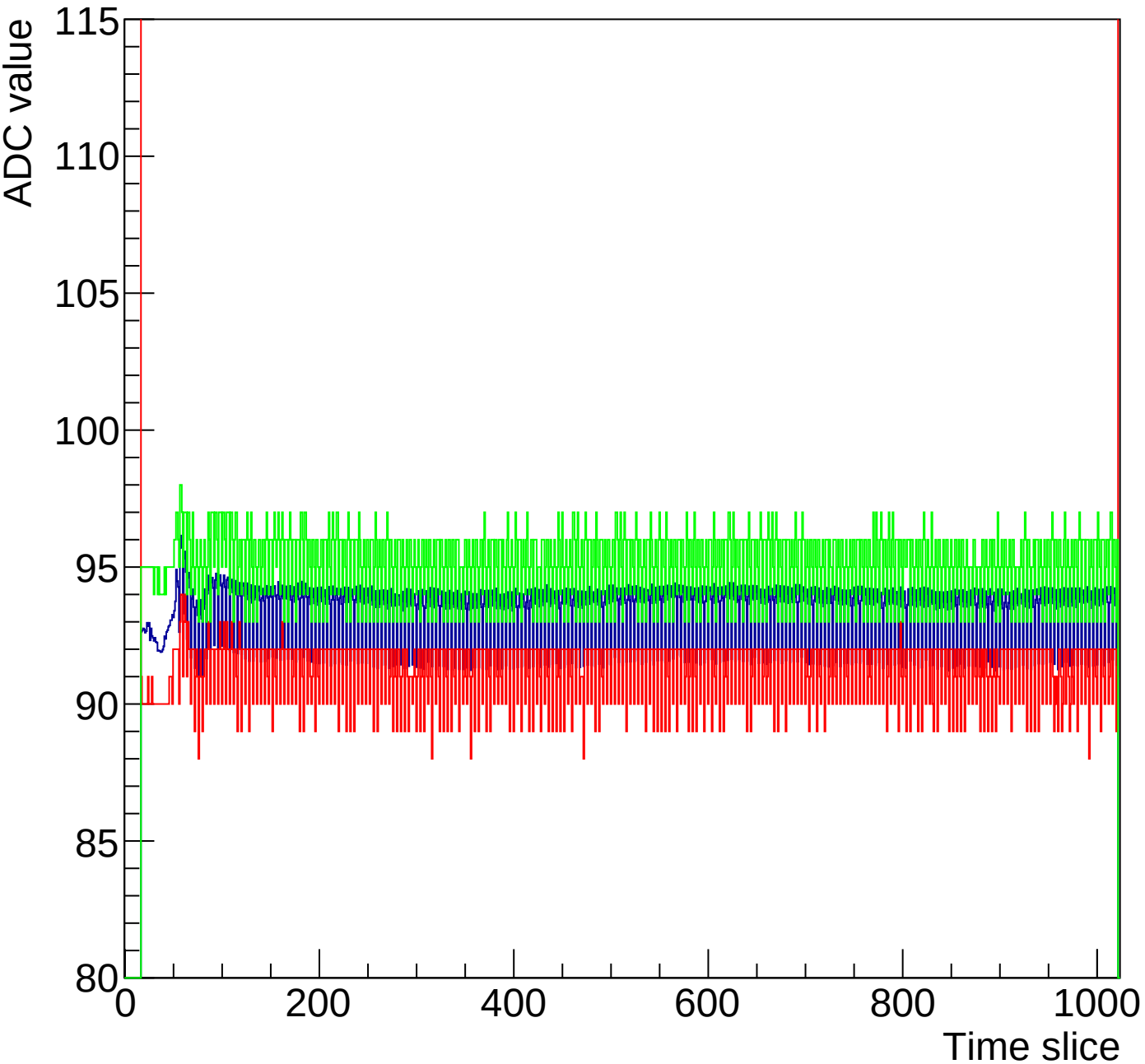


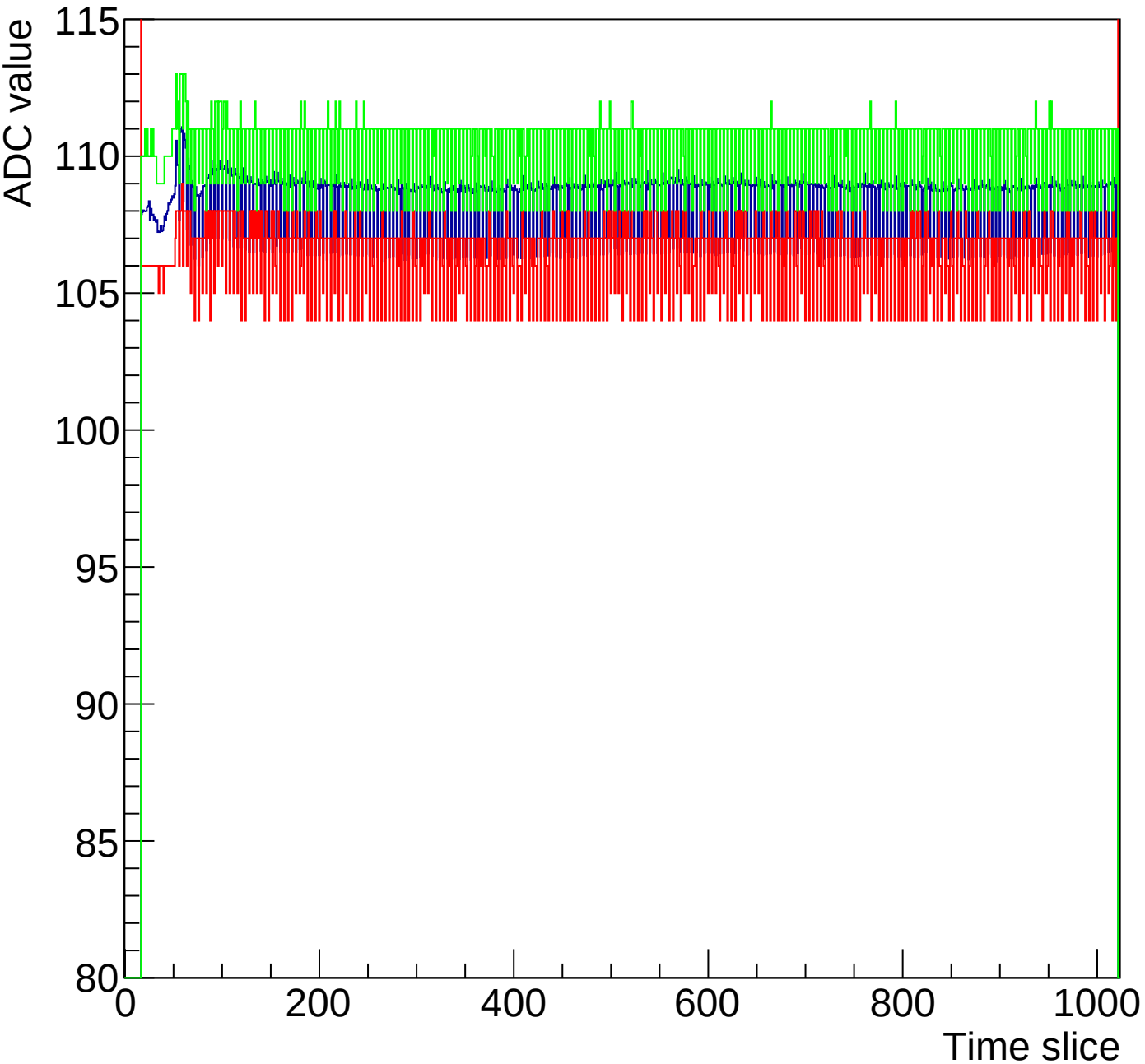
Run 1 Time mcm 0 chip 3 chan 0 full



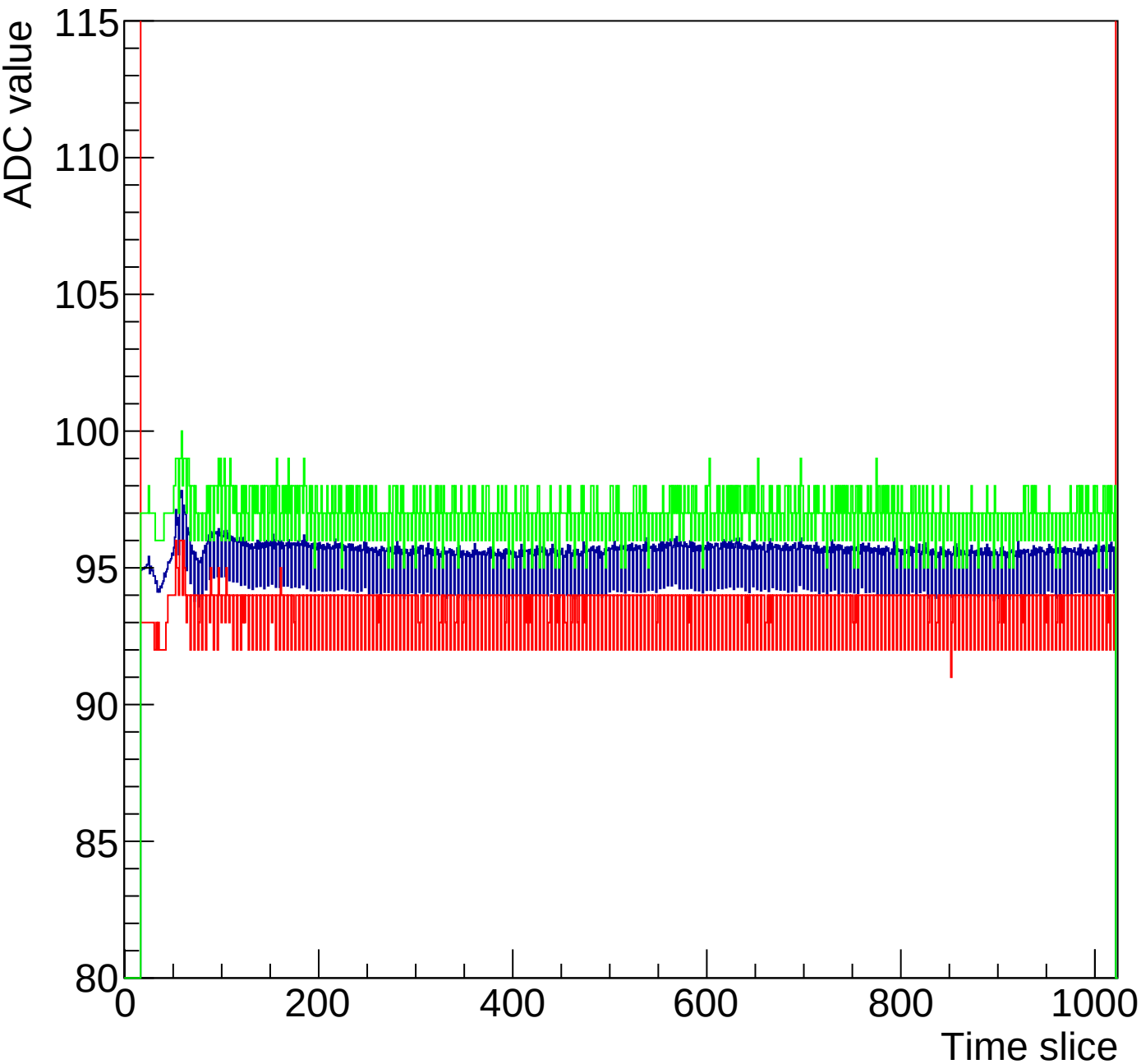
Run 1 Time mcm 0 chip 3 chan 1 full



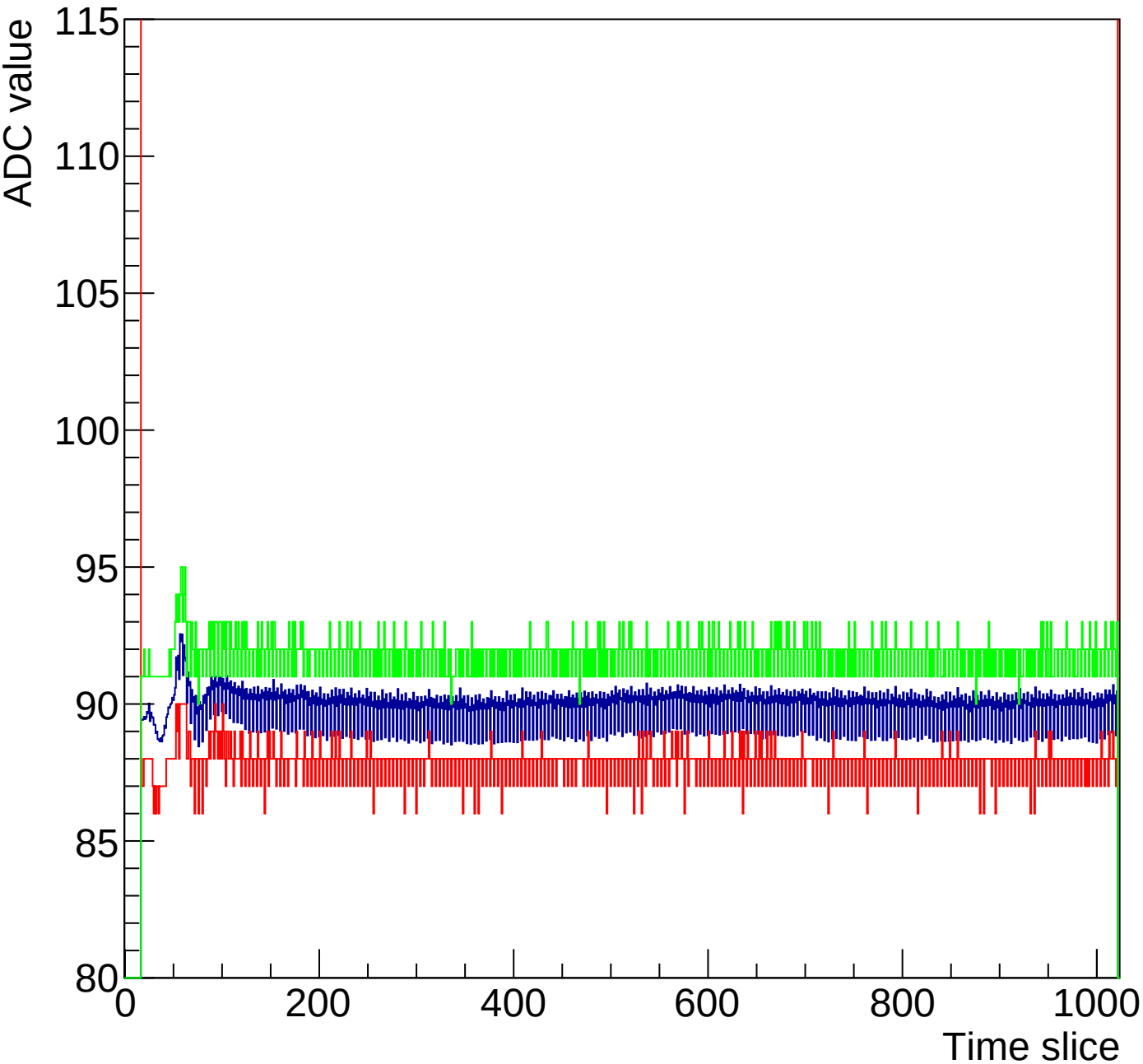
Run 1 Time mcm 0 chip 3 chan 2 full



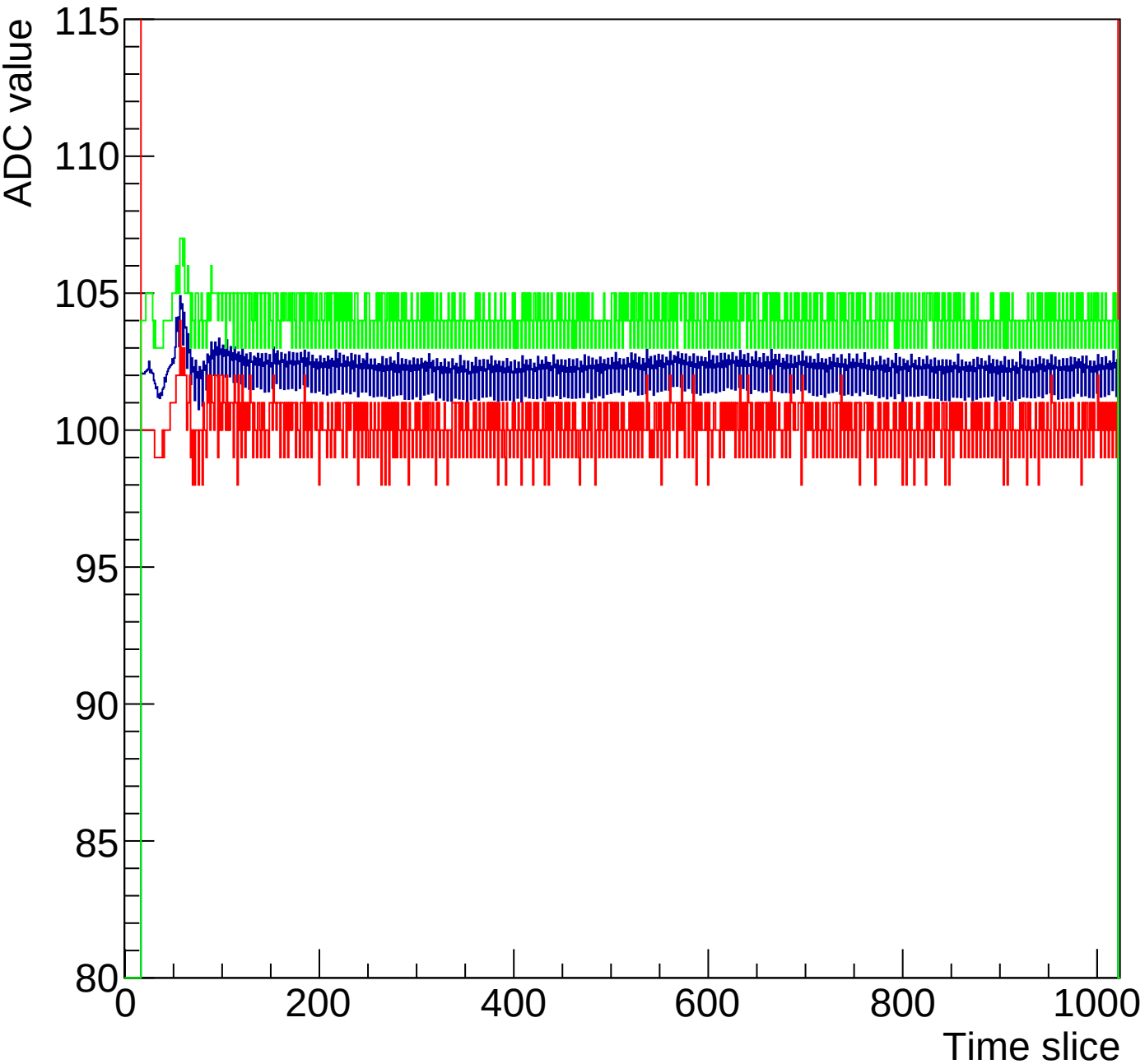
Run 1 Time mcm 0 chip 3 chan 3 full



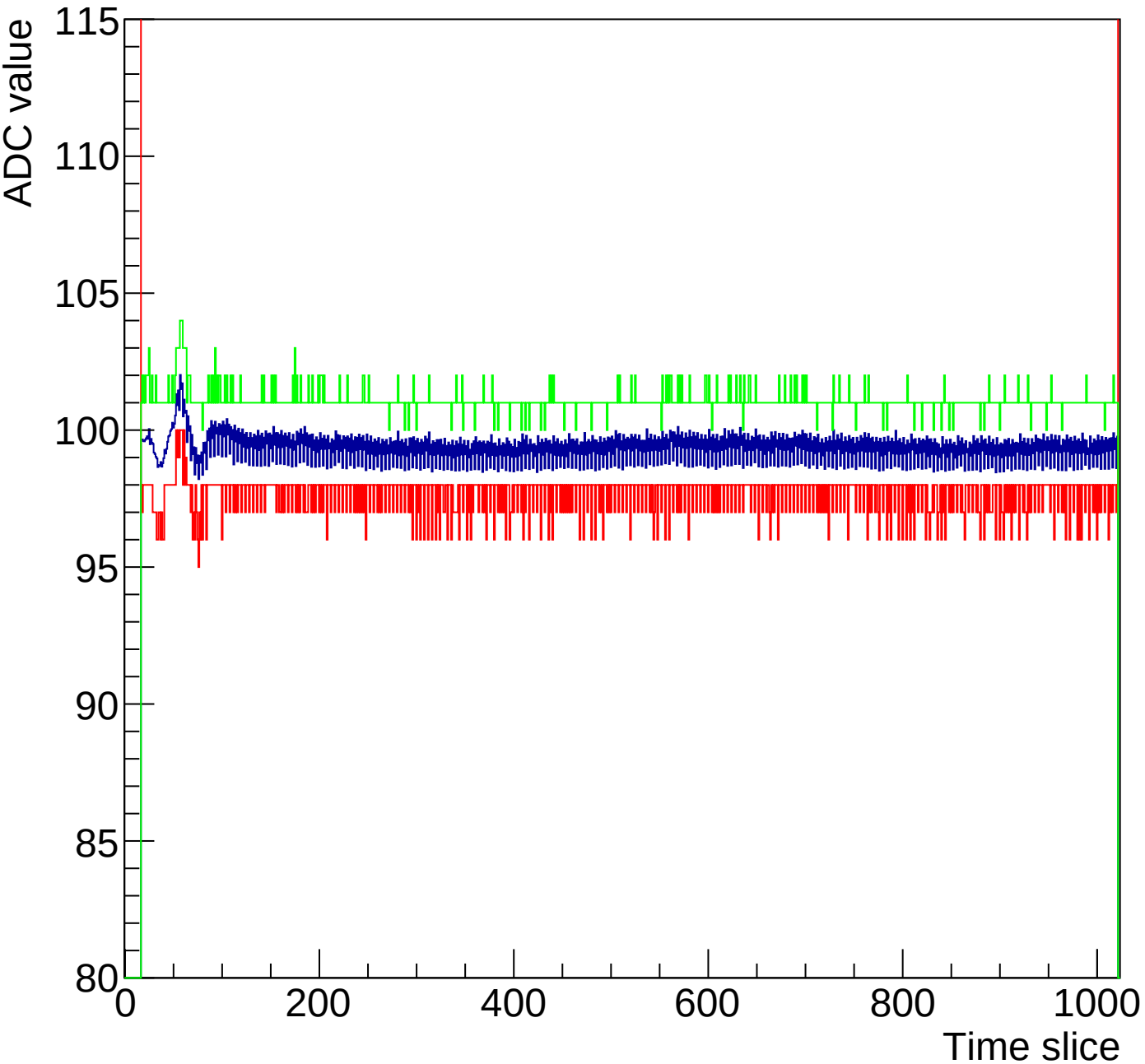
Run 1 Time mcm 0 chip 3 chan 4 full



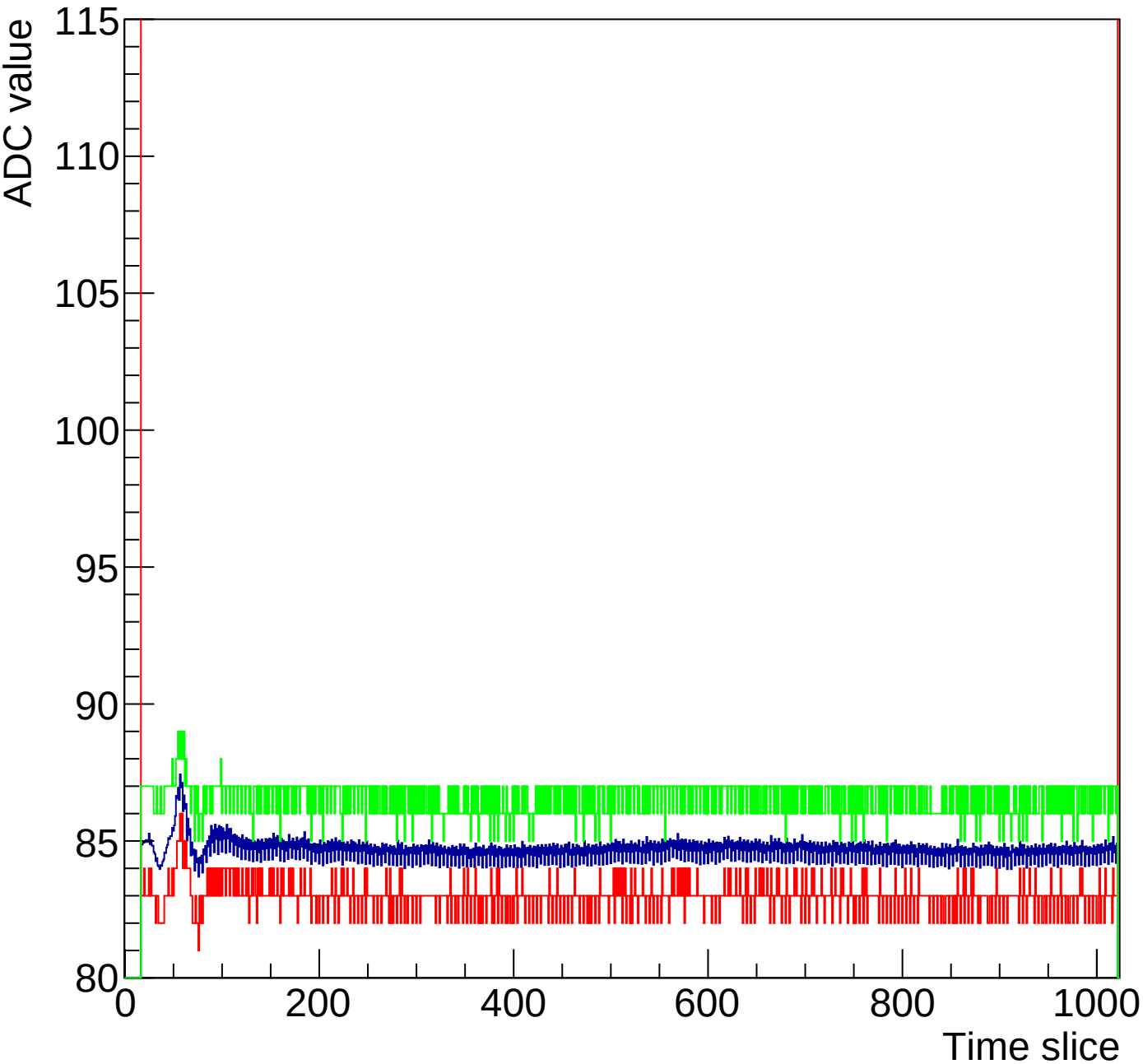
Run 1 Time mcm 0 chip 3 chan 5 full



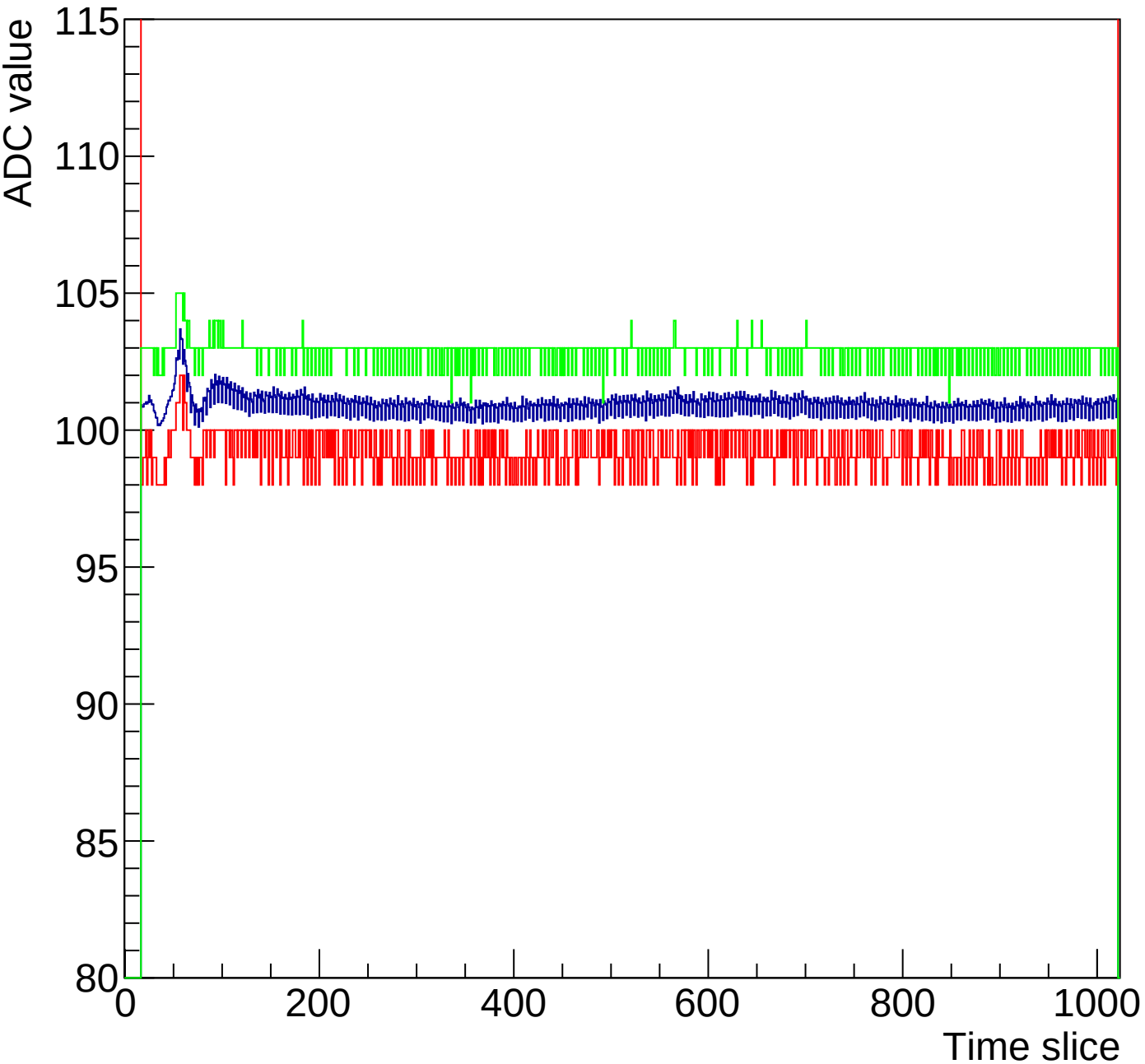
Run 1 Time mcm 0 chip 3 chan 6 full



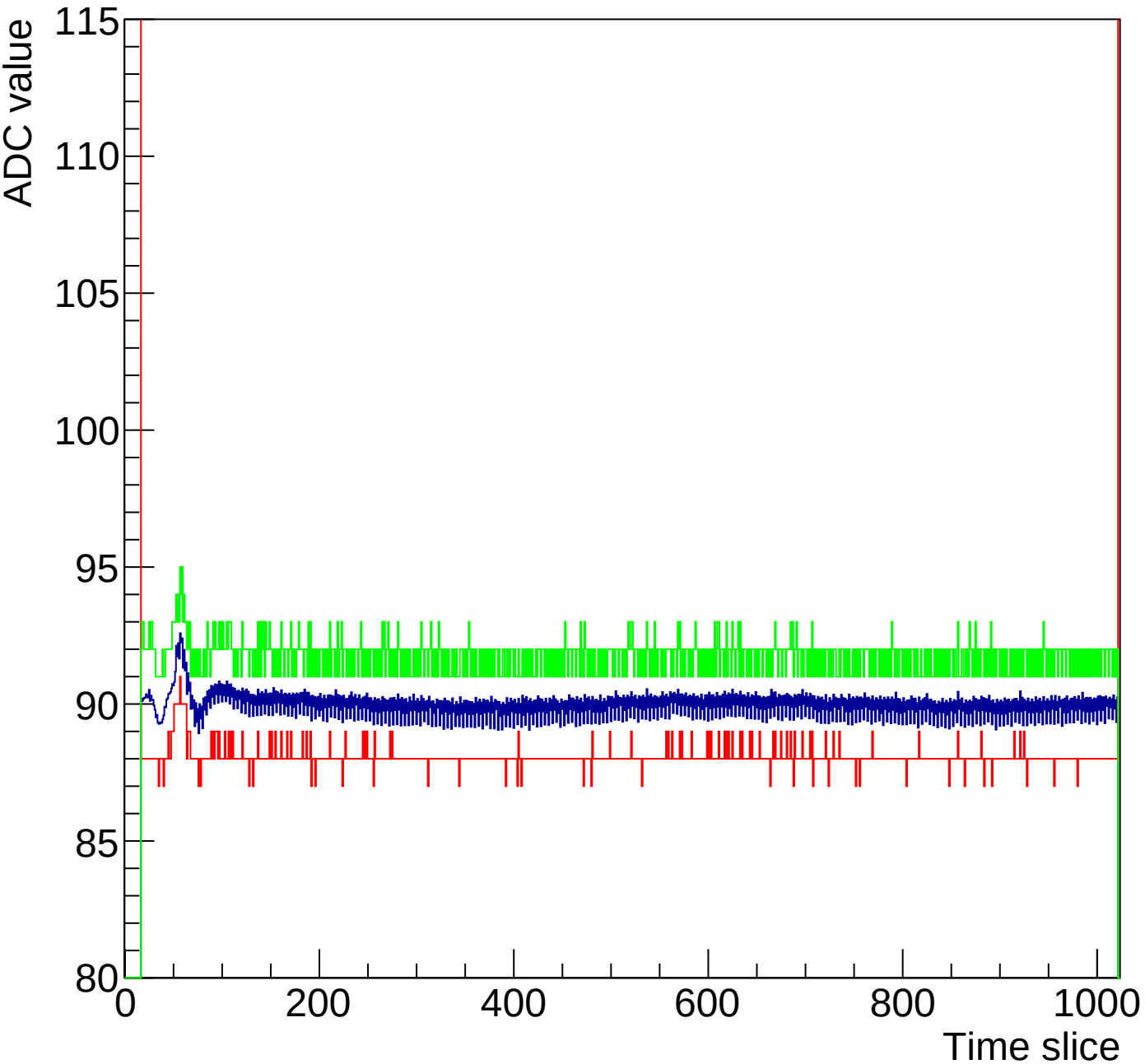
Run 1 Time mcm 0 chip 3 chan 7 full



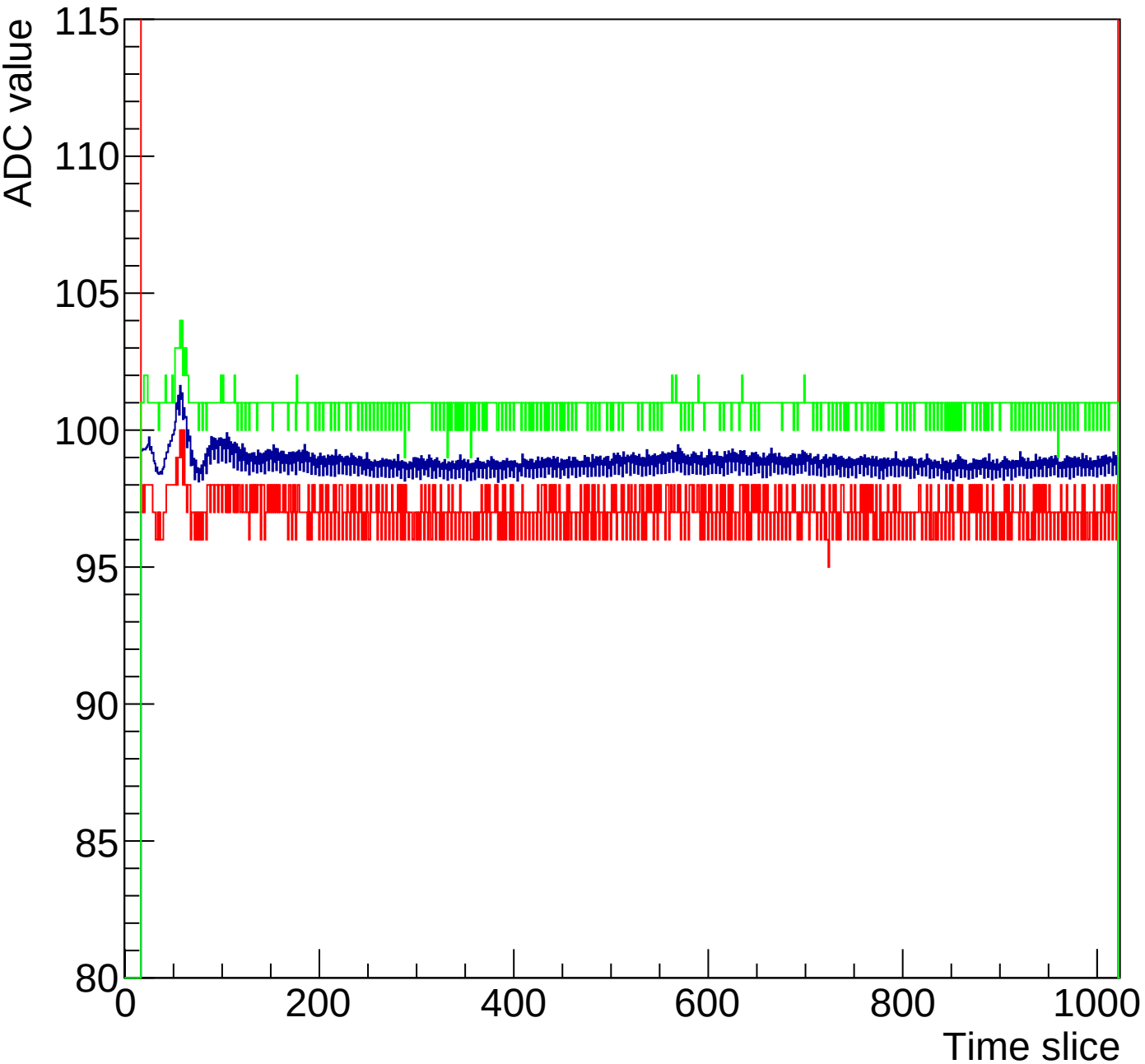
Run 1 Time mcm 0 chip 3 chan 8 full



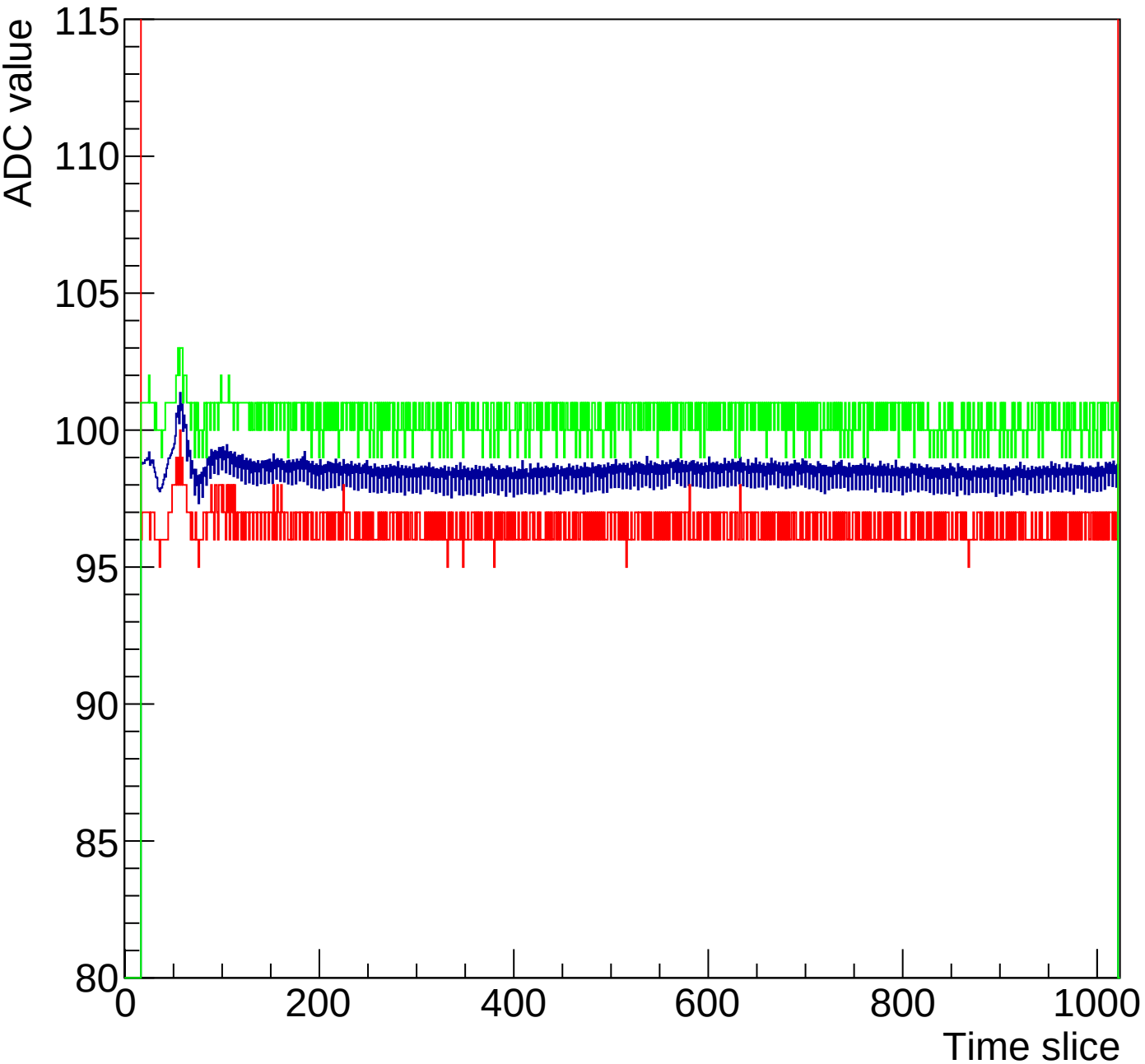
Run 1 Time mcm 0 chip 3 chan 9 full



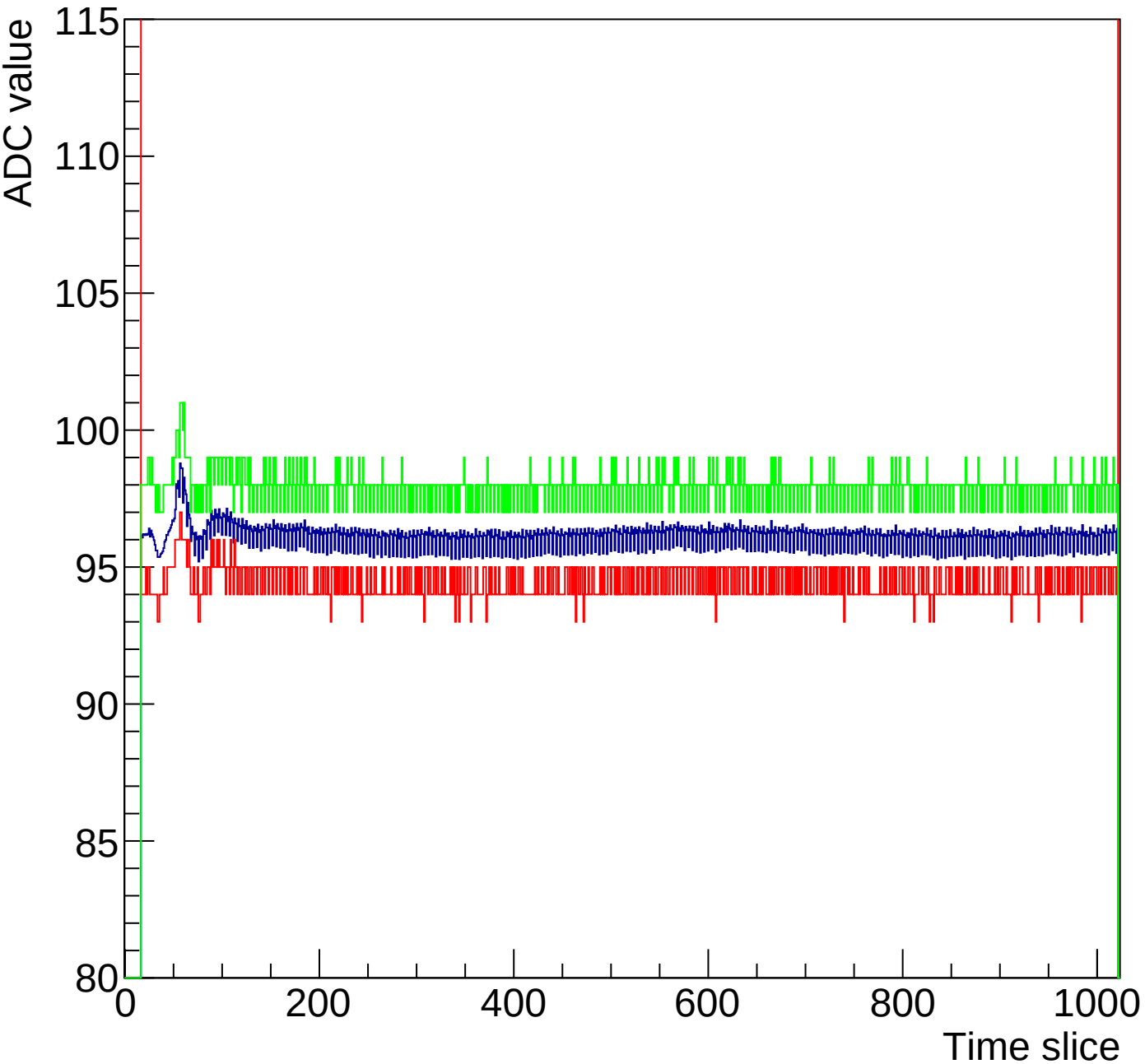
Run 1 Time mcm 0 chip 3 chan 10 full



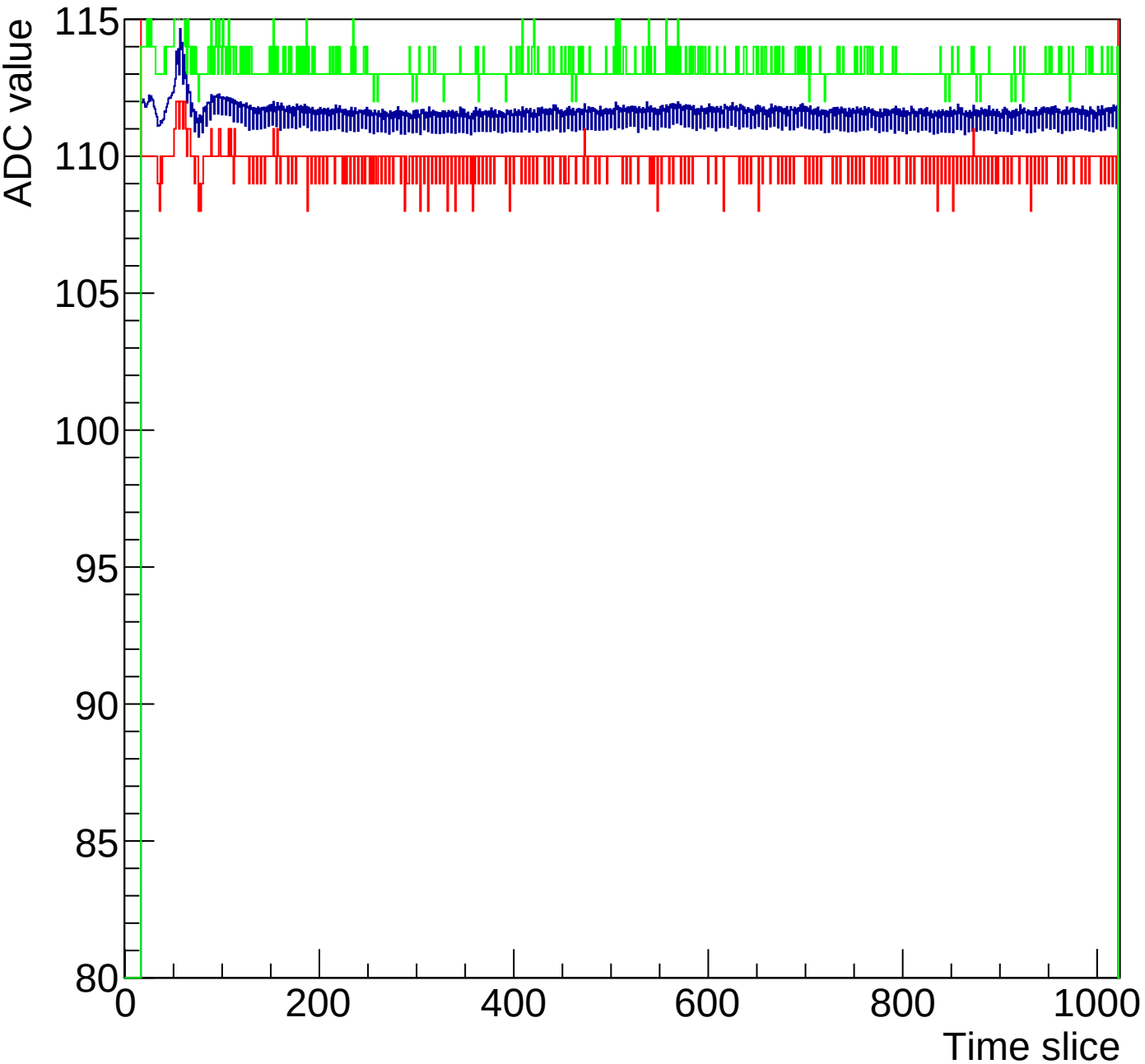
Run 1 Time mcm 0 chip 3 chan 11 full



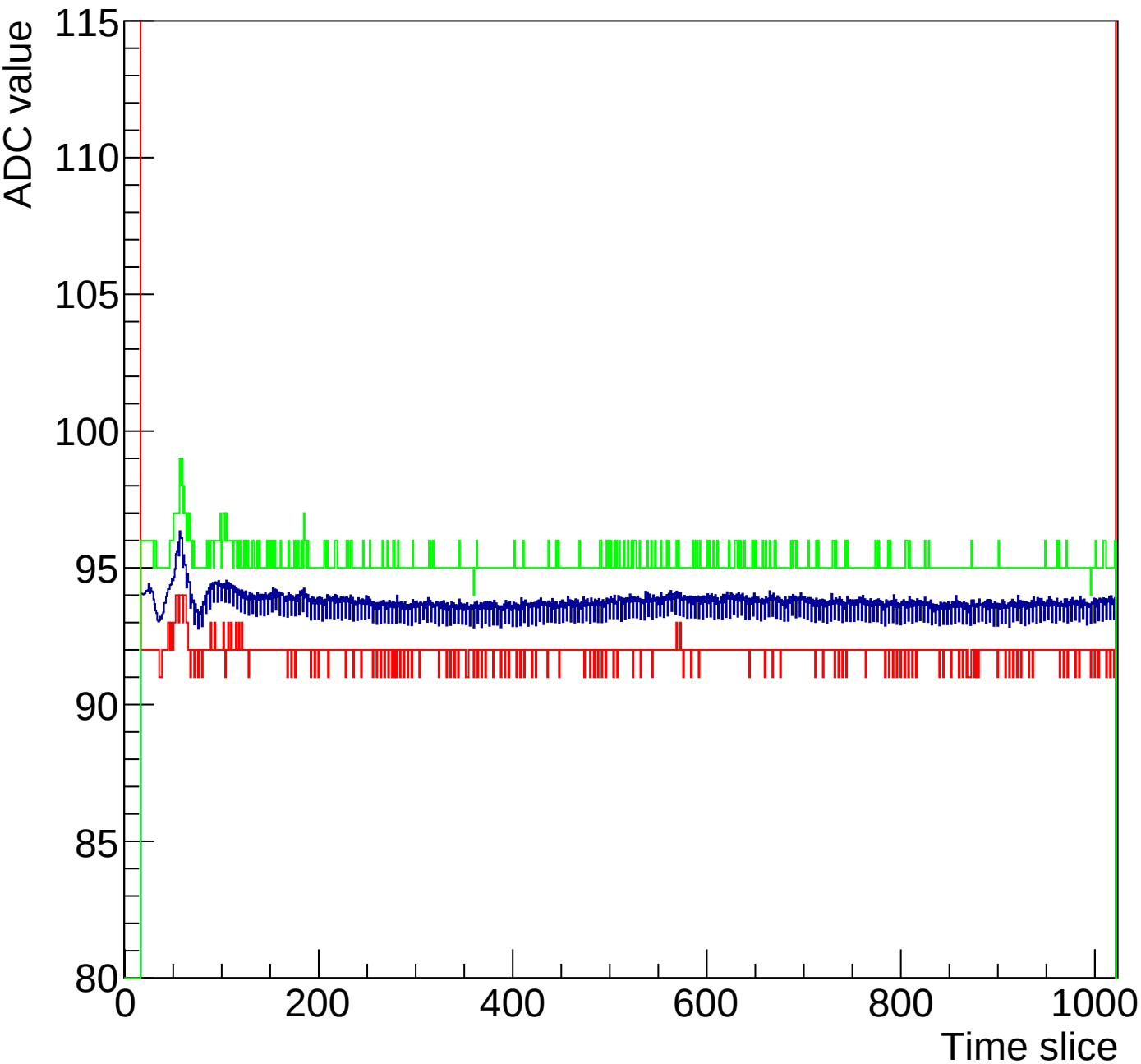
Run 1 Time mcm 0 chip 3 chan 12 full



Run 1 Time mcm 0 chip 3 chan 13 full



Run 1 Time mcm 0 chip 3 chan 14 full



Run 1 Time mcm 0 chip 3 chan 15 full

