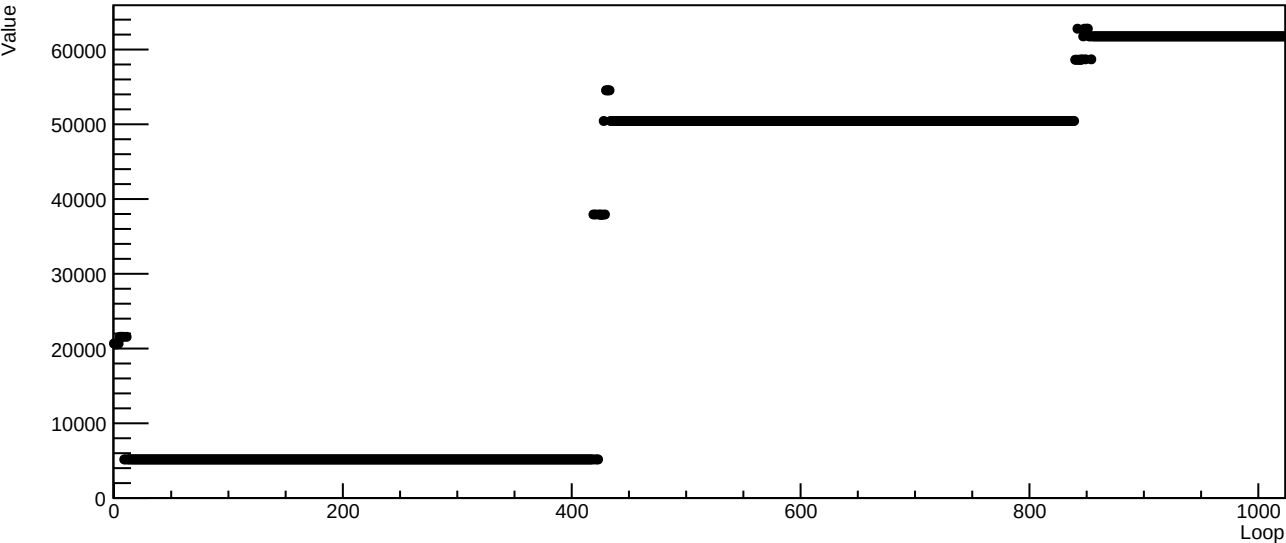
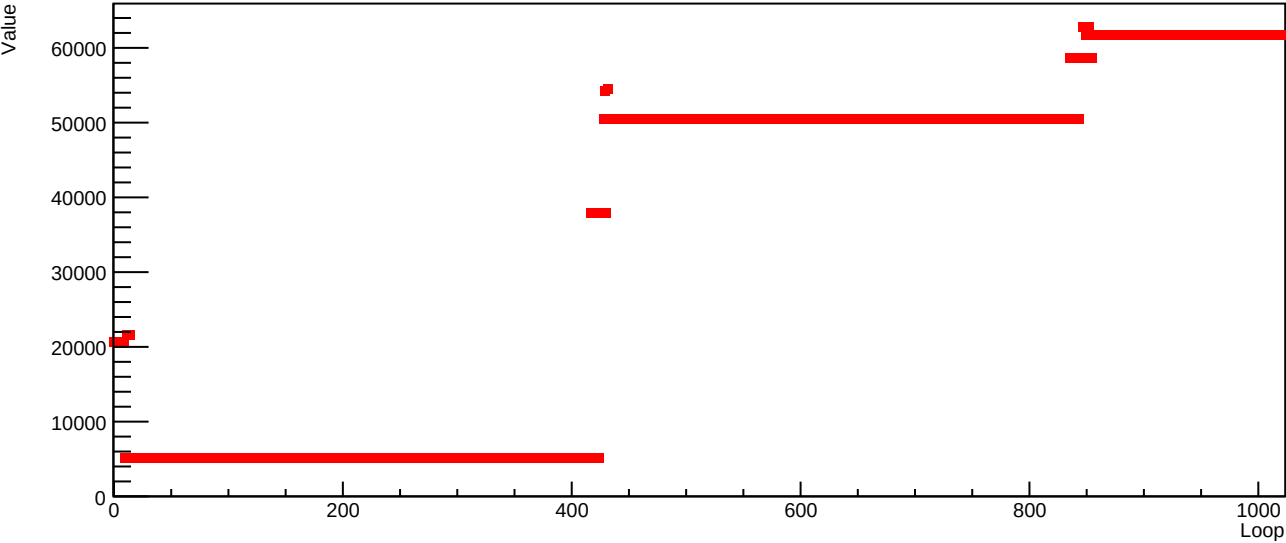


DCL timing4-reset2.csv MCM0



DCL timing4-reset2.csv MCM1



DCL timing4-reset2.csv MCM2

