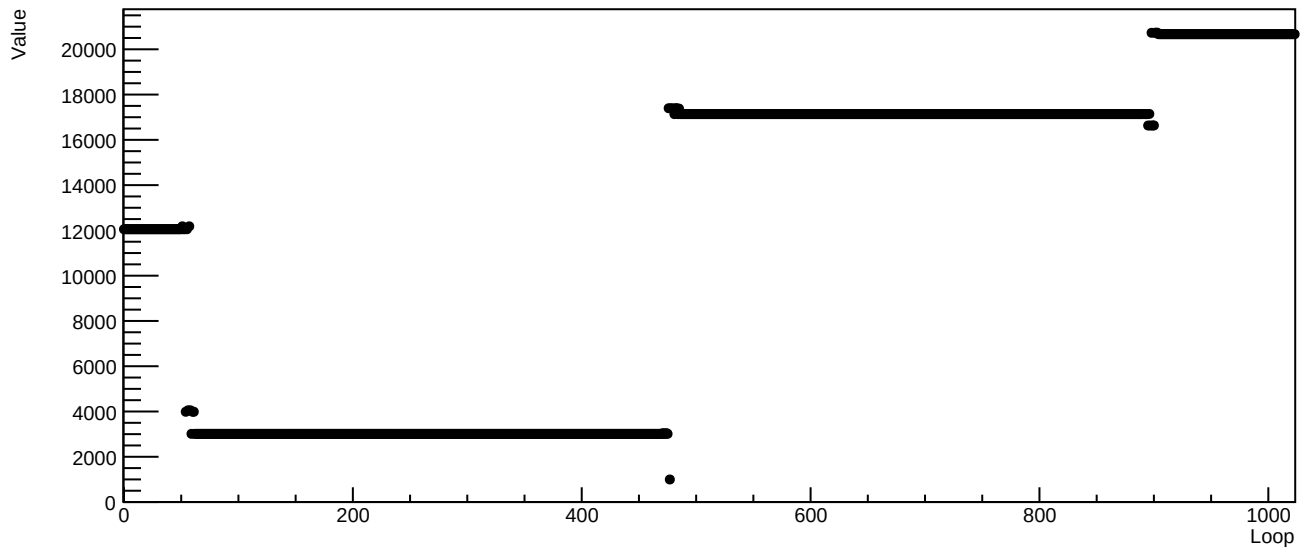
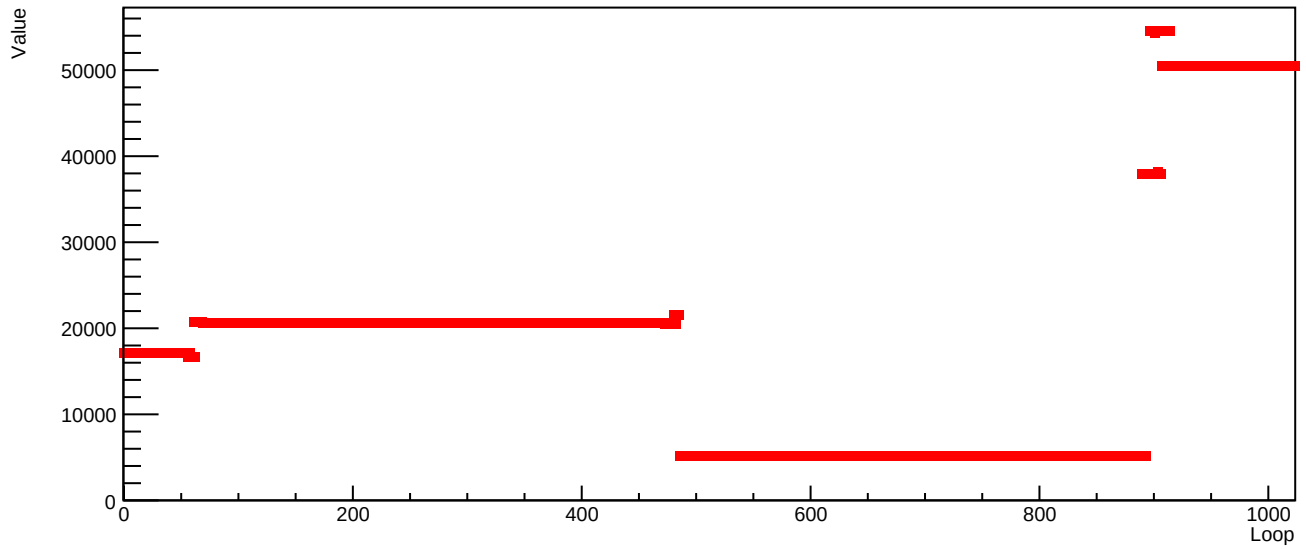


DCL timing5-reset2.csv MCM0



DCL timing5-reset2.csv MCM1



DCL timing5-reset2.csv MCM2

