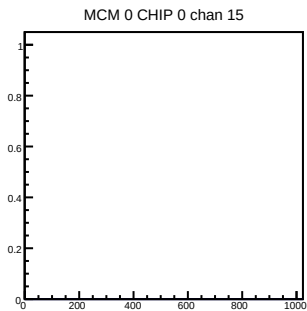
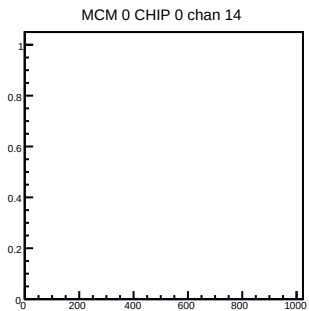
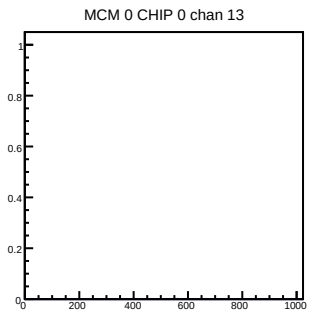
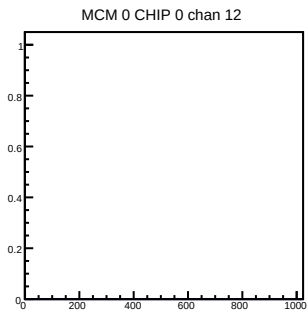
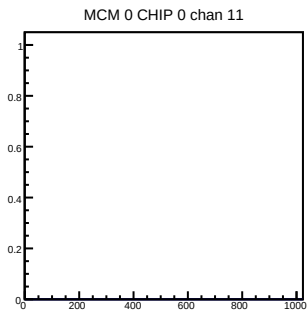
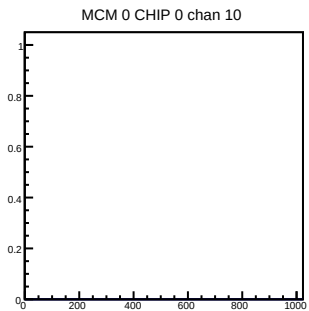
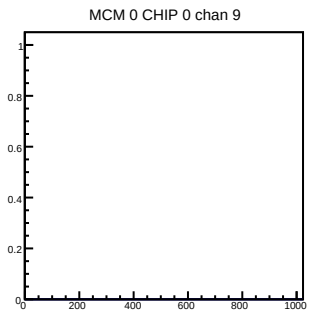
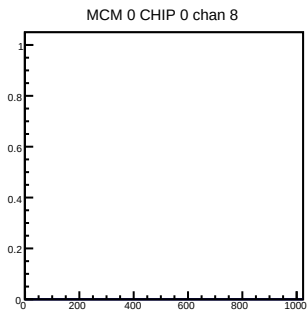
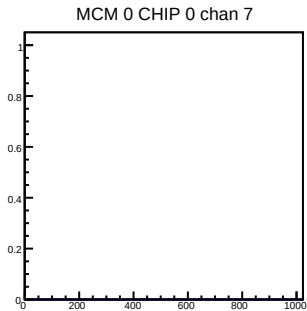
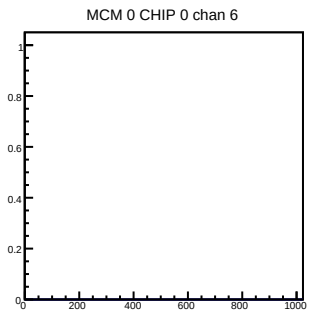
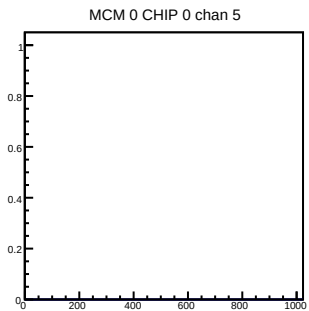
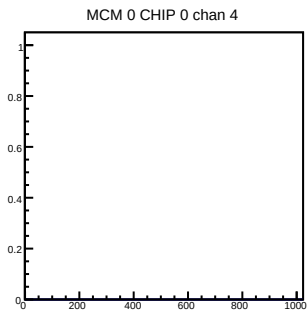
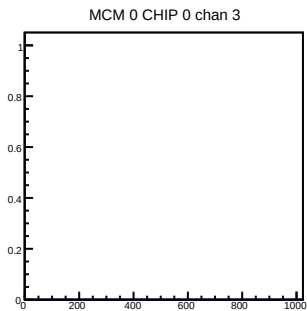
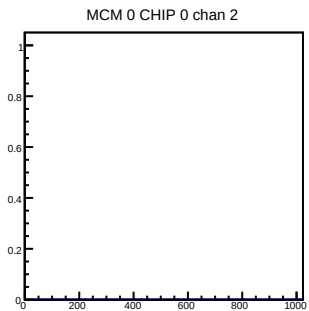
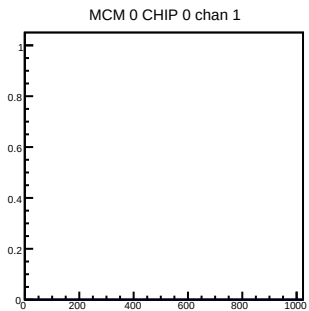
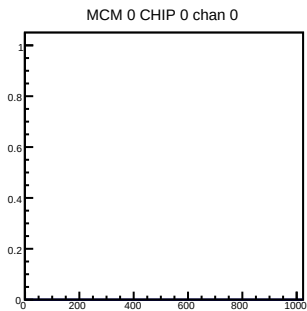
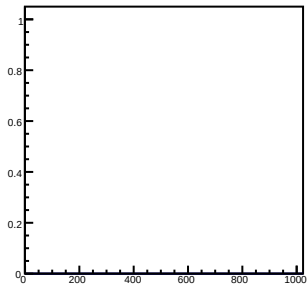
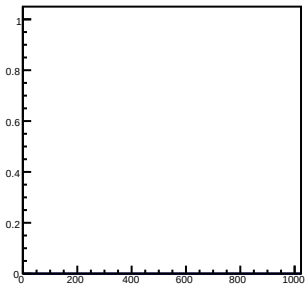




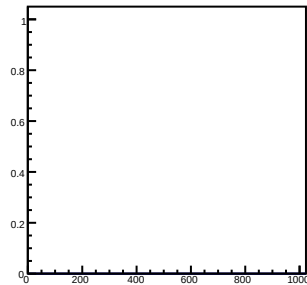
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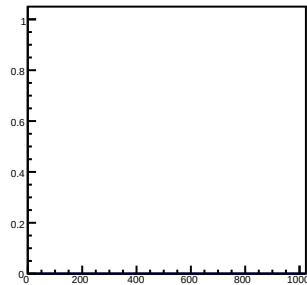
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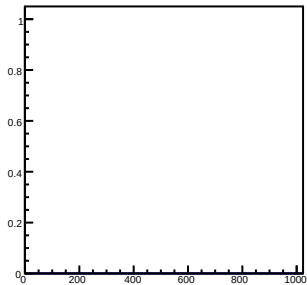
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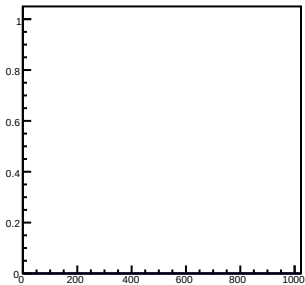
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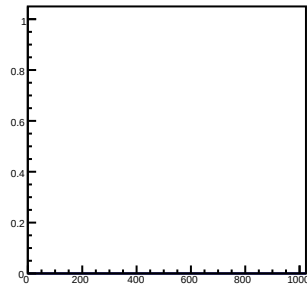
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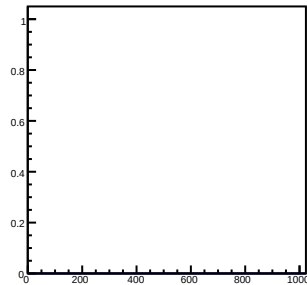
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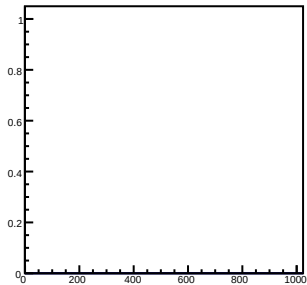
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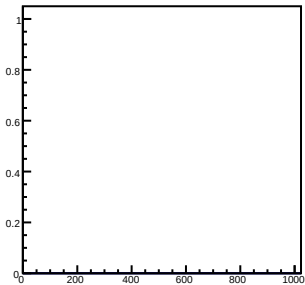
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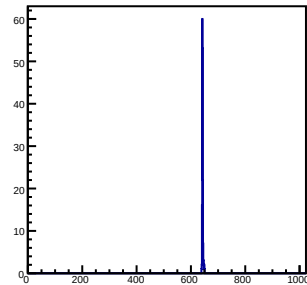
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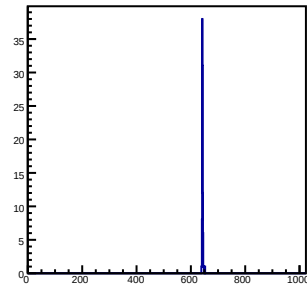
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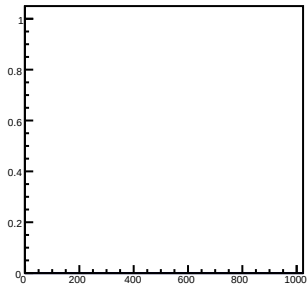
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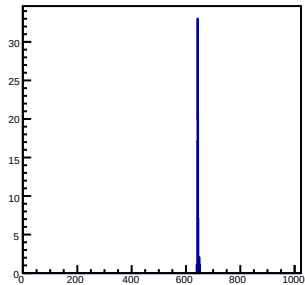
MCM 0 CHIP 1 chan 11



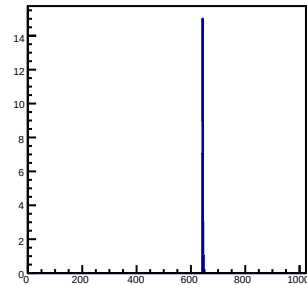
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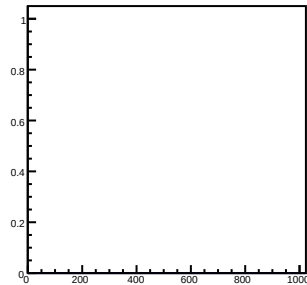
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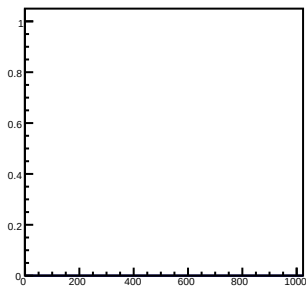
MCM 0 CHIP 1 chan 14



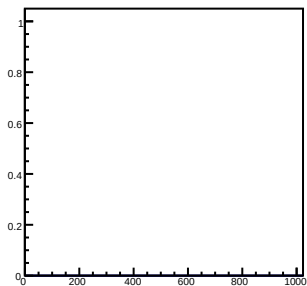
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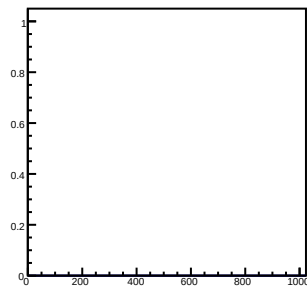
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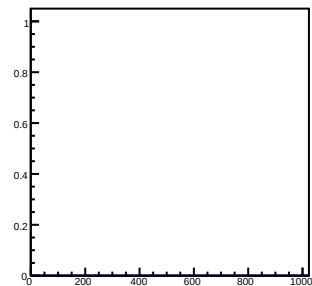
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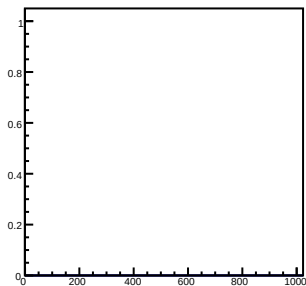
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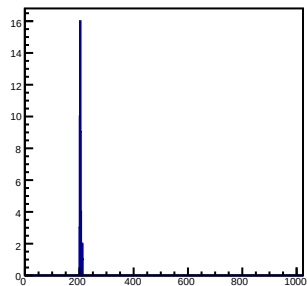
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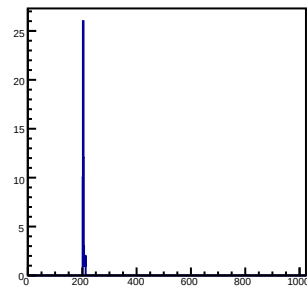
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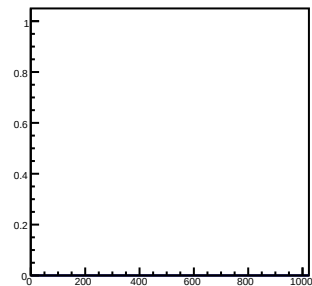
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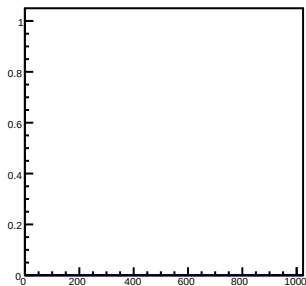
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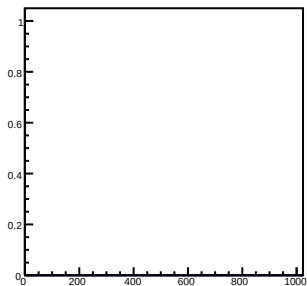
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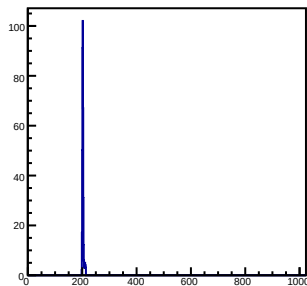
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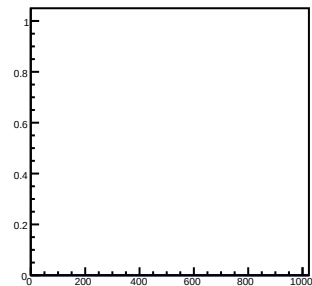
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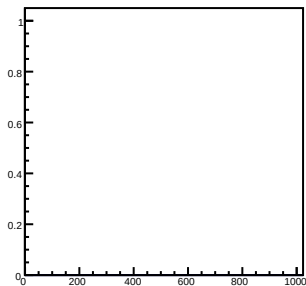
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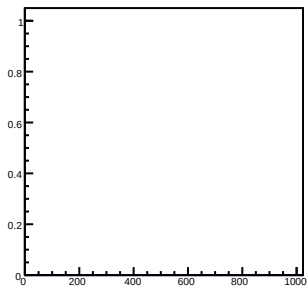
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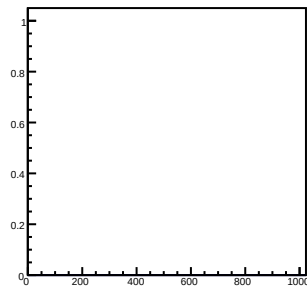
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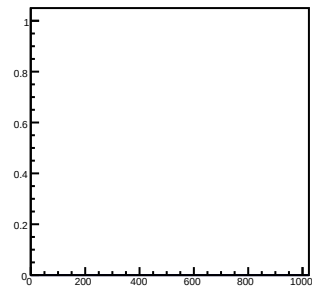
MCM 0 CHIP 2 chan 13

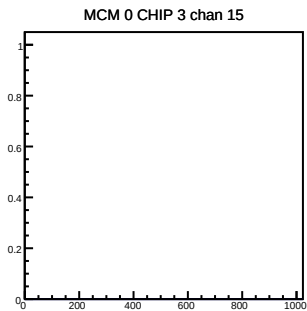
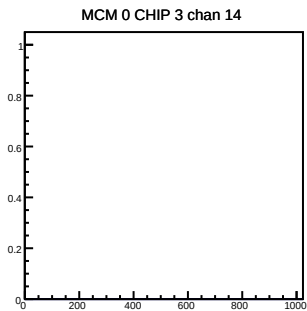
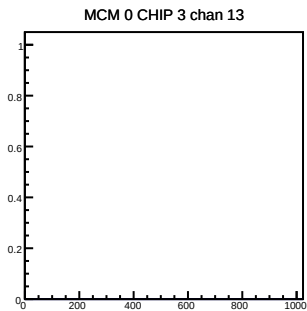
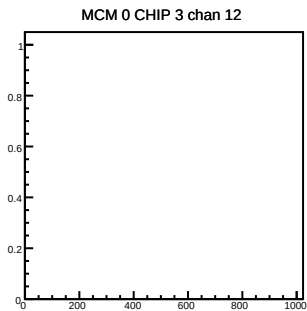
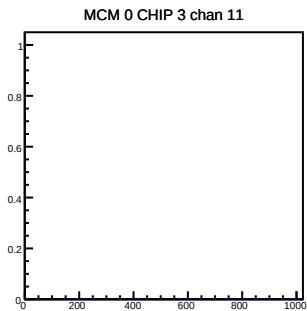
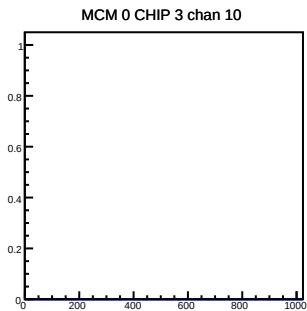
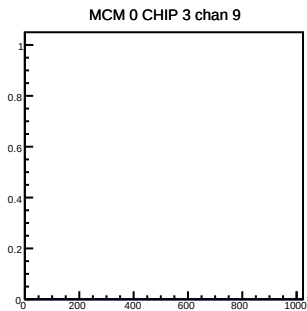
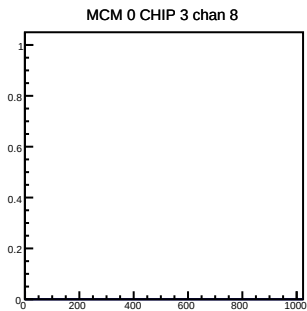
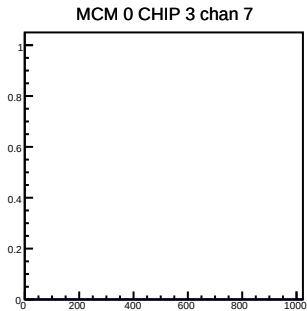
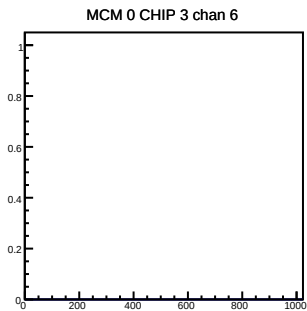
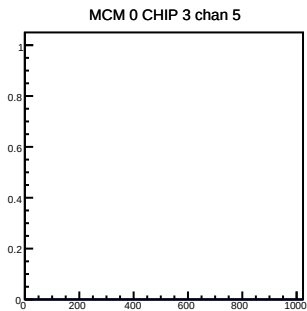
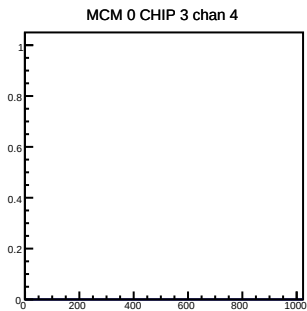
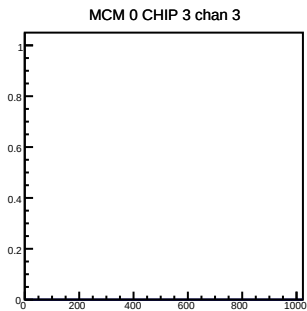
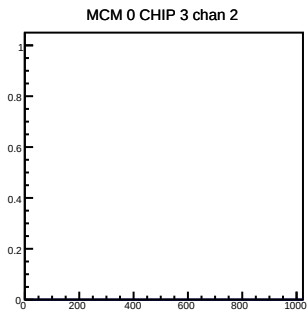
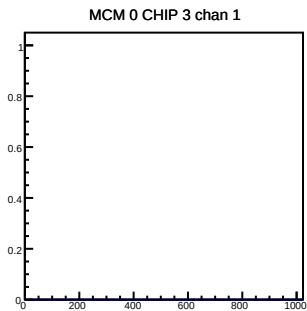
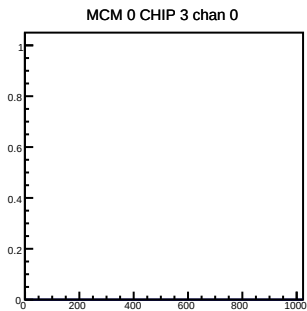


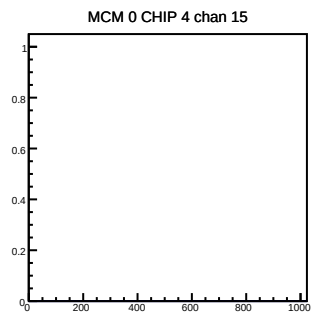
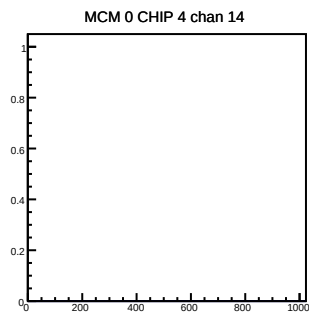
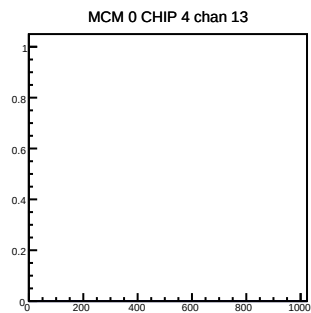
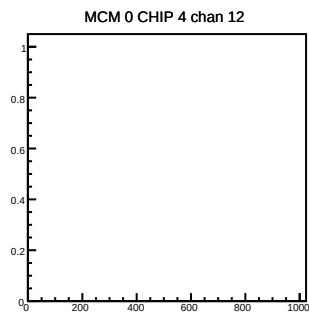
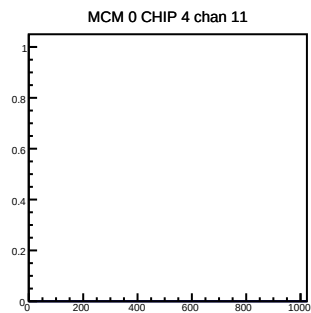
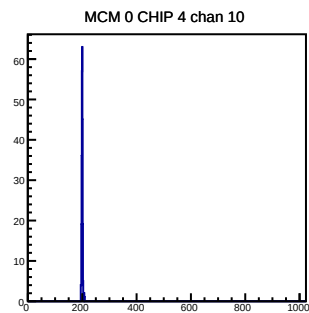
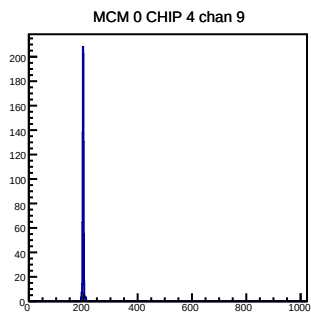
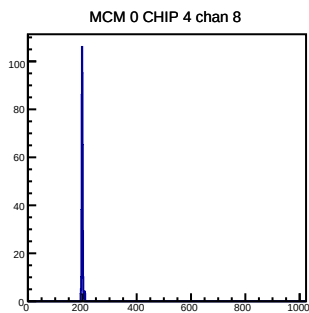
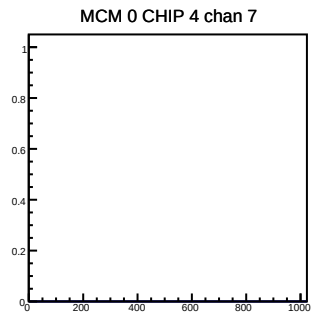
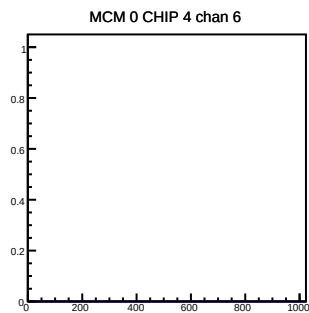
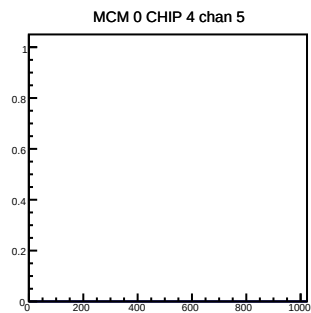
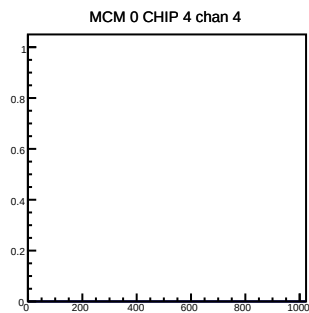
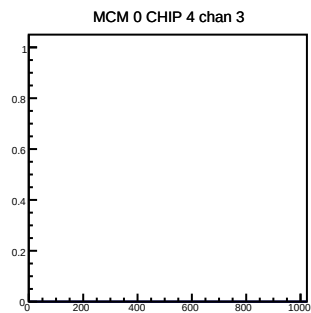
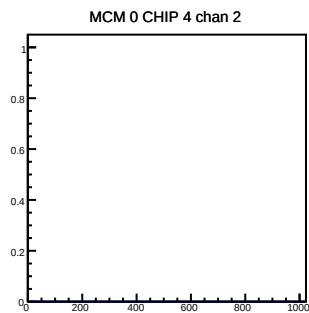
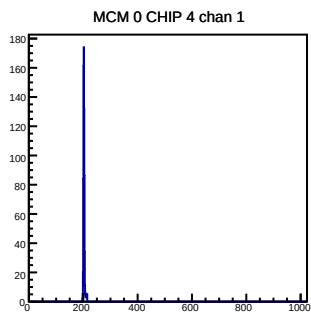
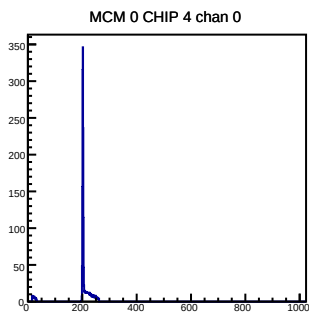
MCM 0 CHIP 2 chan 14



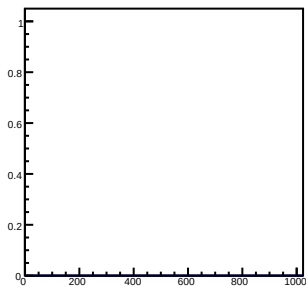
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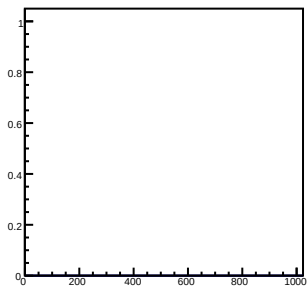




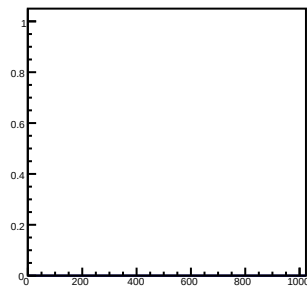
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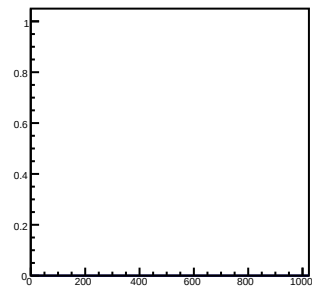
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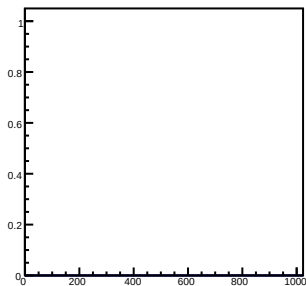
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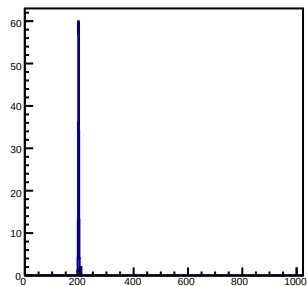
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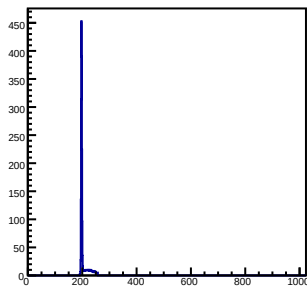
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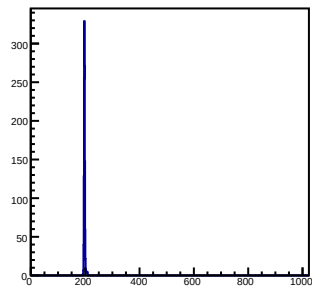
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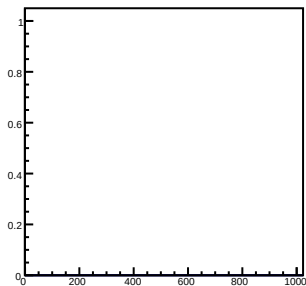
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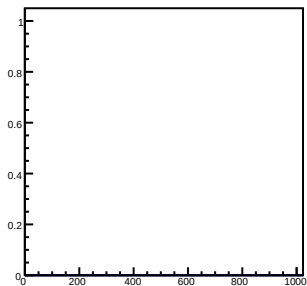
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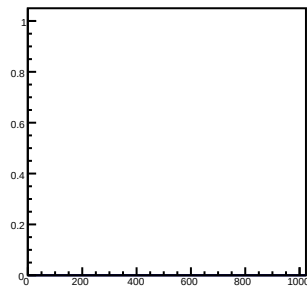
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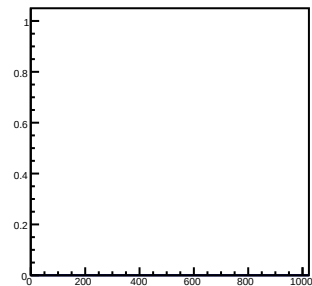
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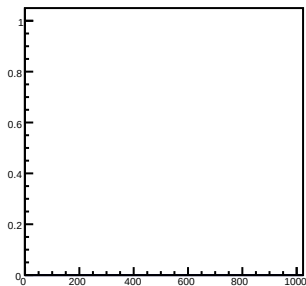
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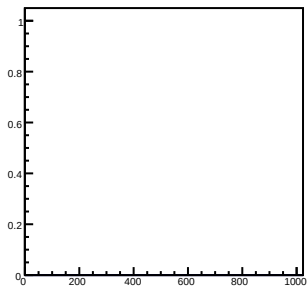
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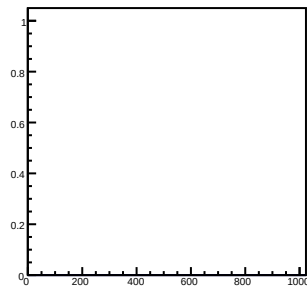
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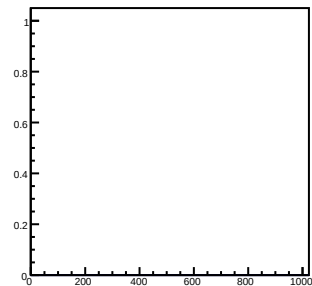
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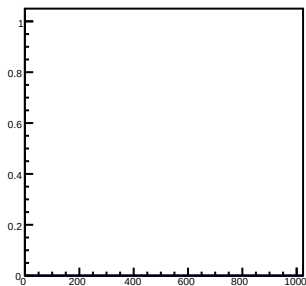
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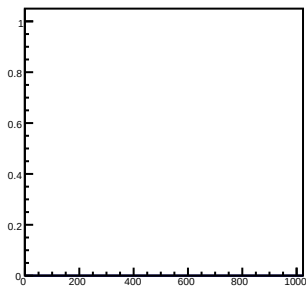
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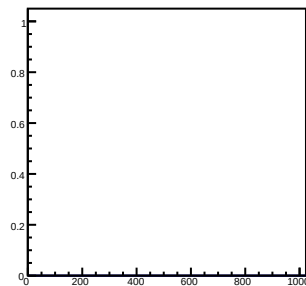
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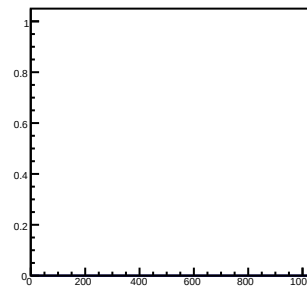
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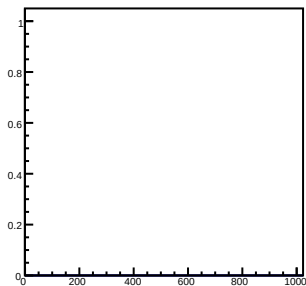
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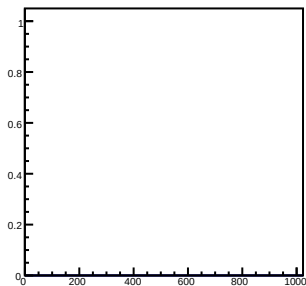
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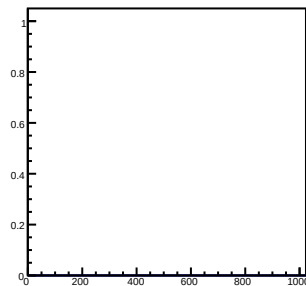
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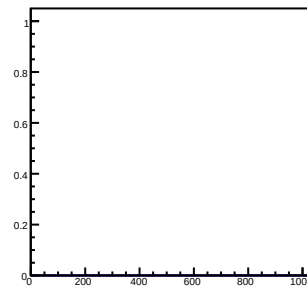
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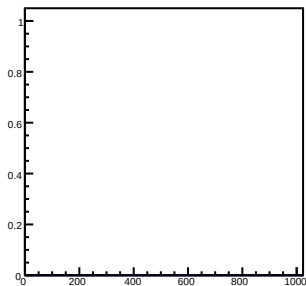
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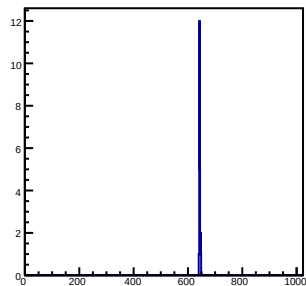
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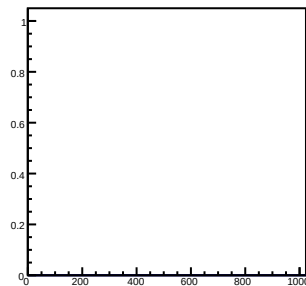
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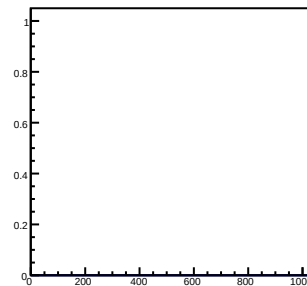
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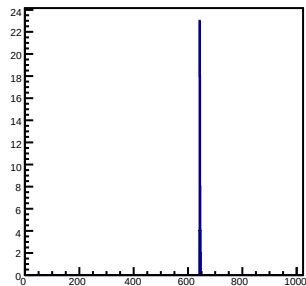
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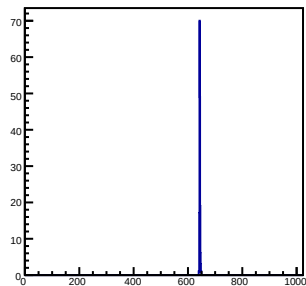
MCM 0 CHIP 6 chan 11



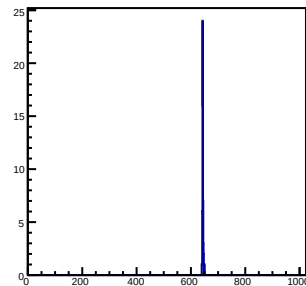
MCM 0 CHIP 6 chan 12



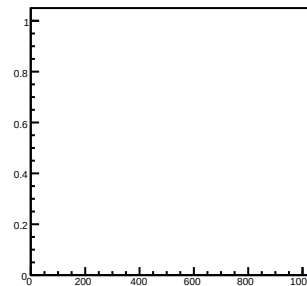
MCM 0 CHIP 6 chan 13

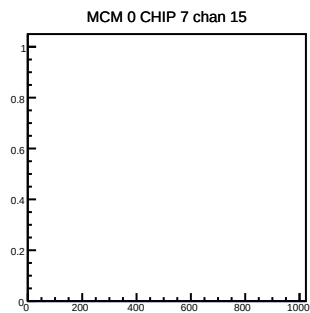
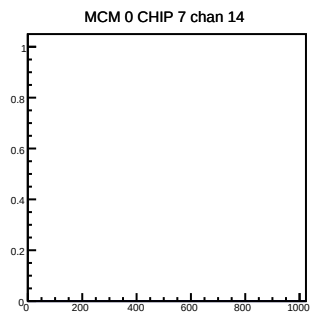
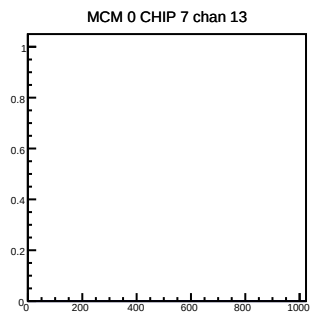
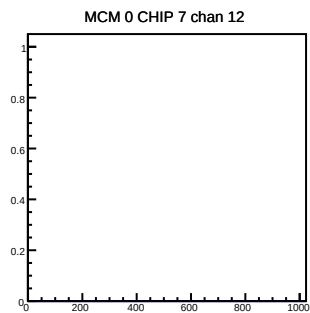
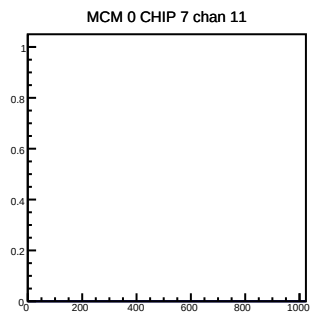
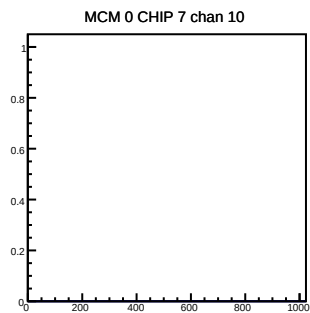
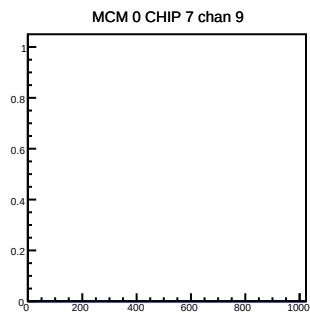
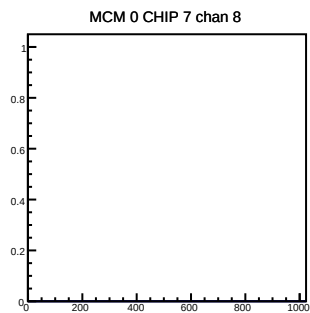
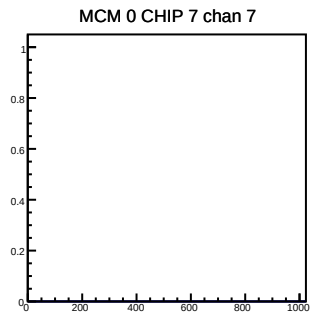
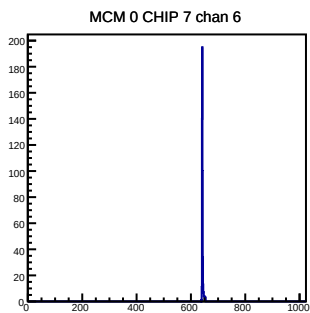
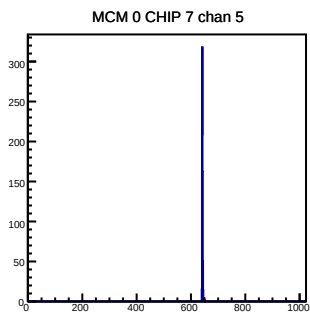
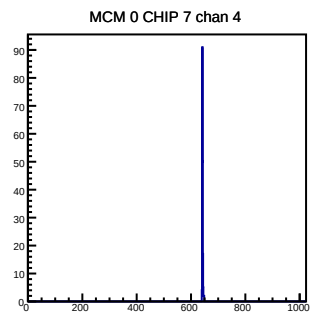
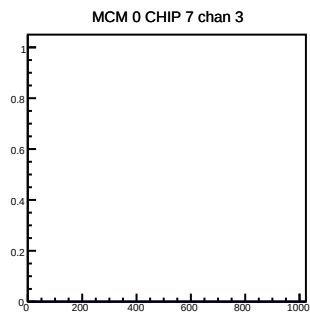
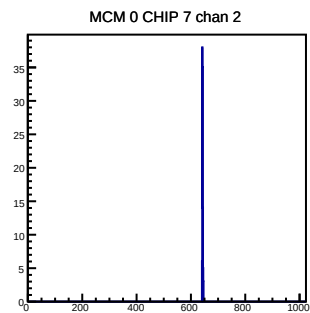
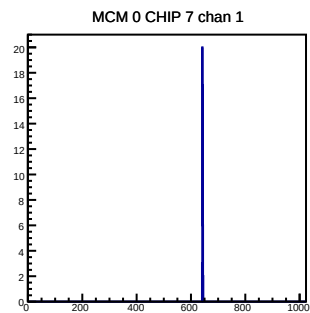
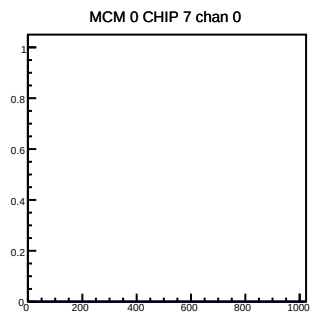


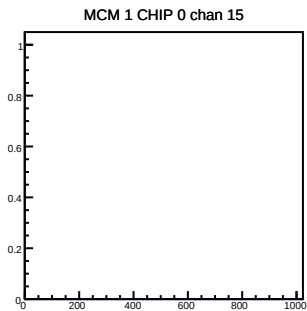
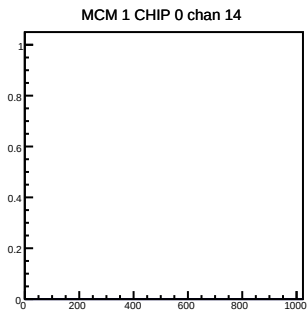
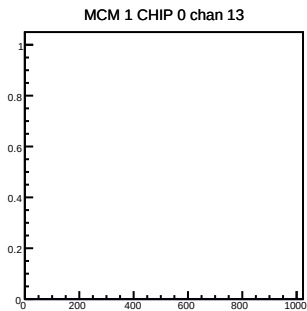
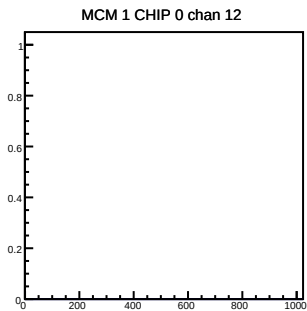
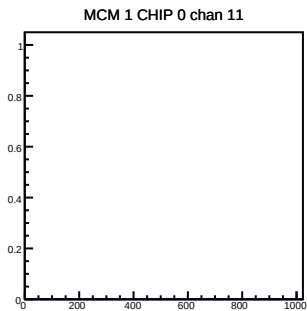
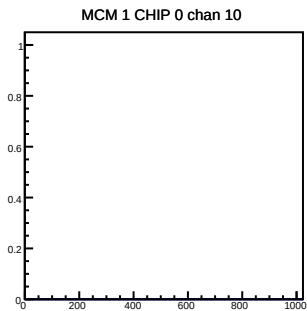
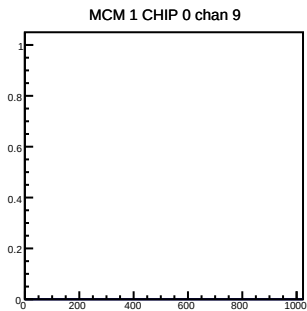
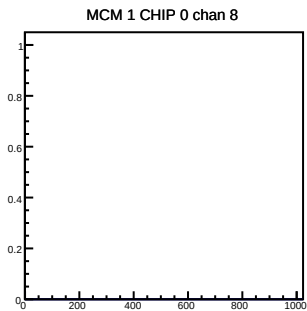
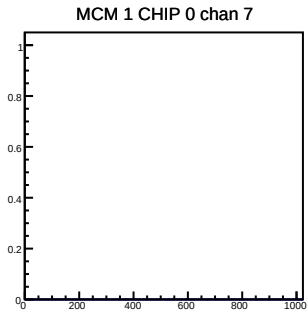
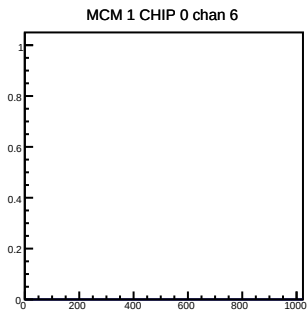
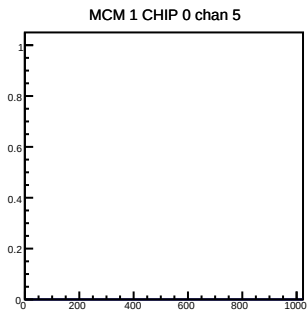
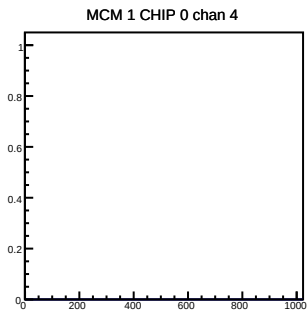
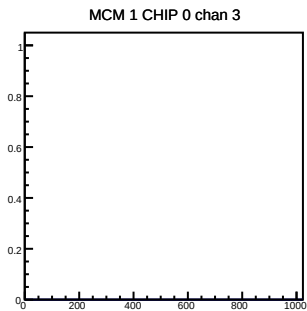
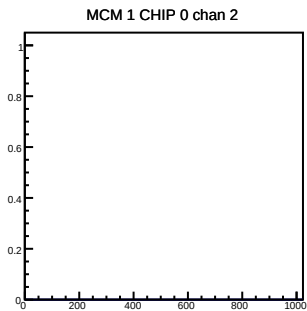
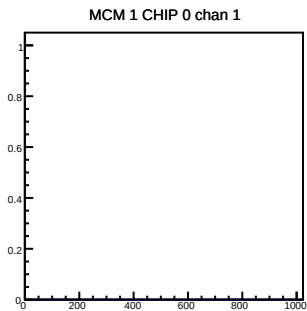
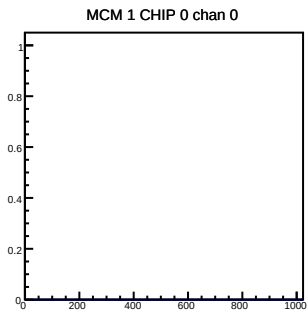
MCM 0 CHIP 6 chan 14



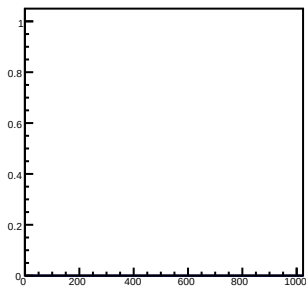
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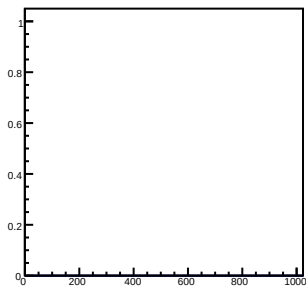




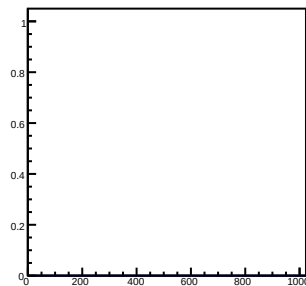
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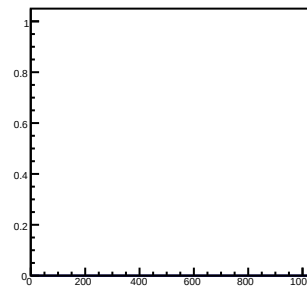
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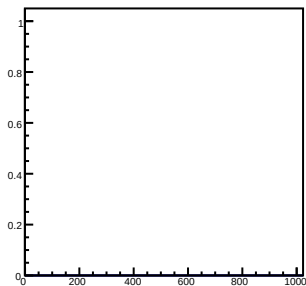
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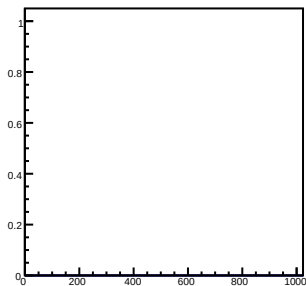
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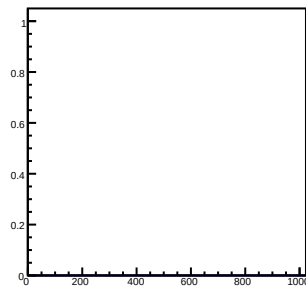
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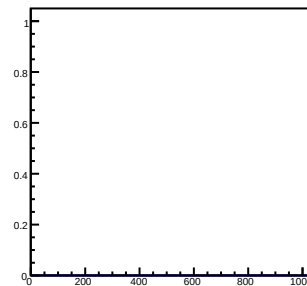
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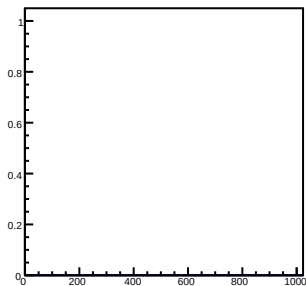
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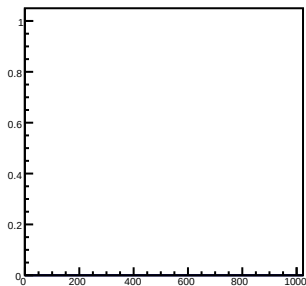
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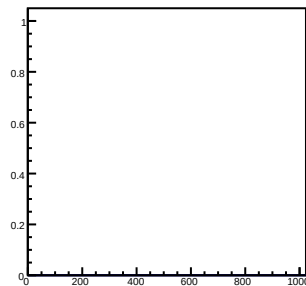
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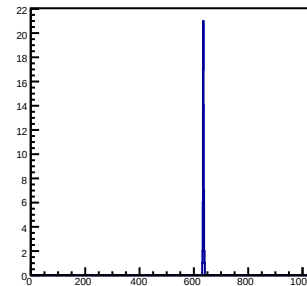
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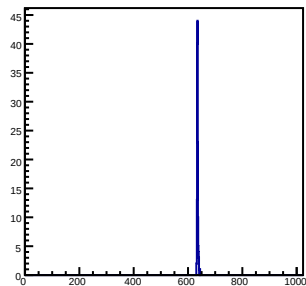
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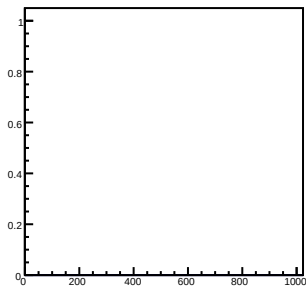
MCM 1 CHIP 1 chan 11



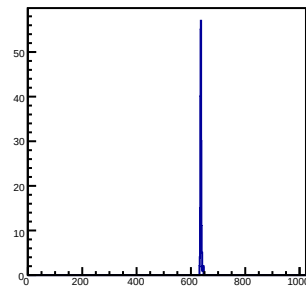
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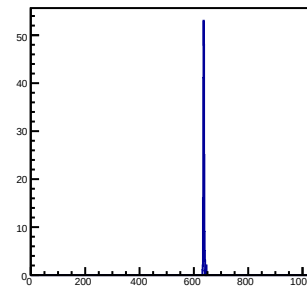
MCM 1 CHIP 1 chan 13

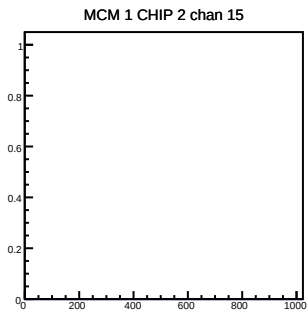
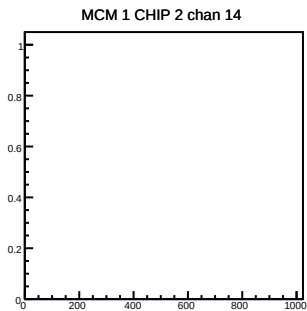
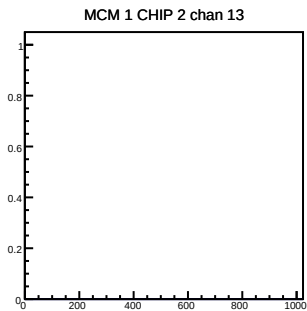
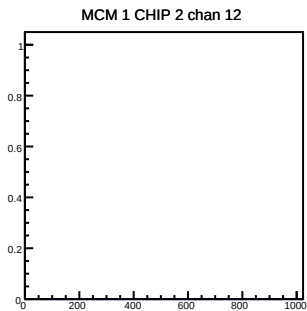
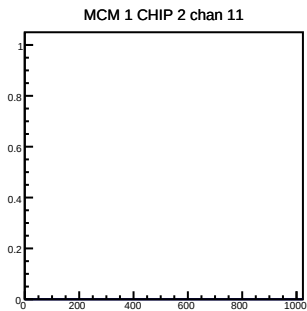
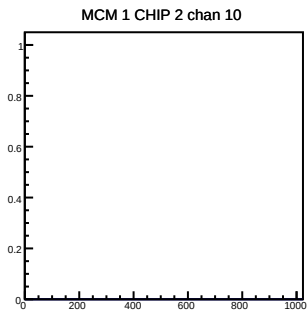
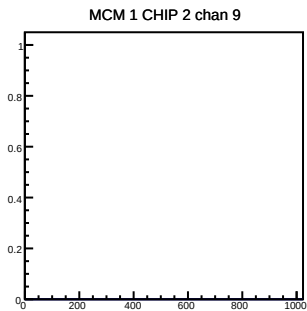
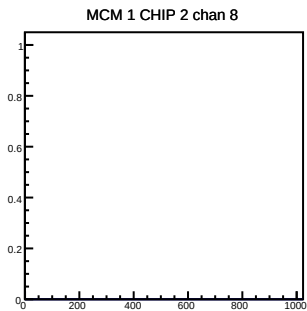
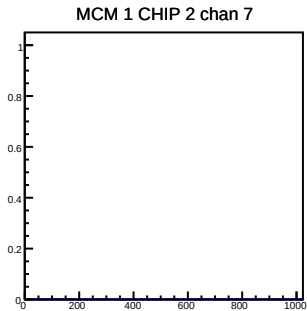
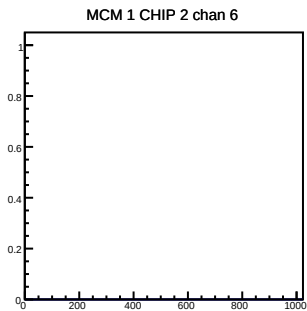
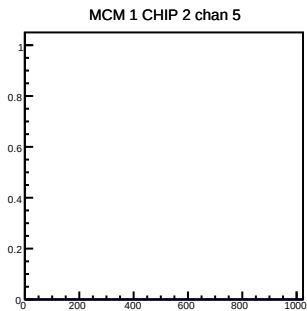
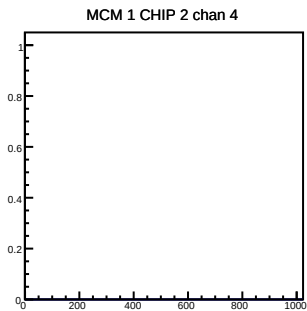
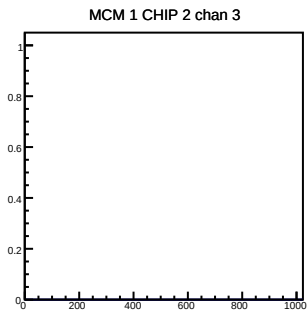
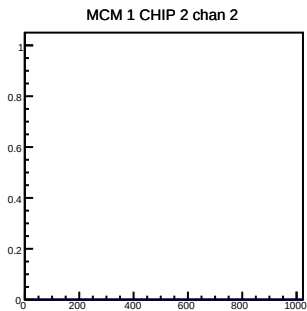
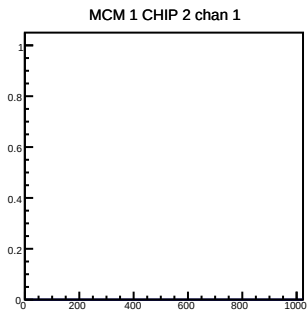
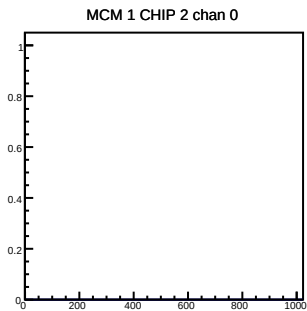


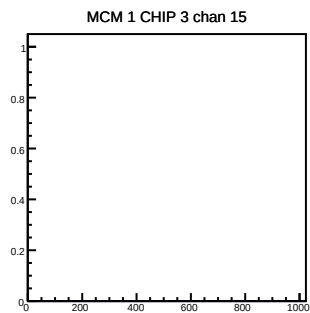
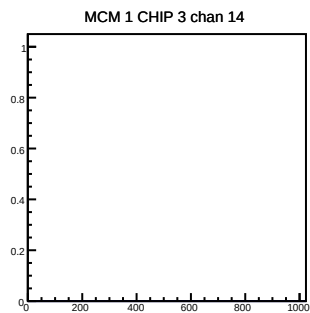
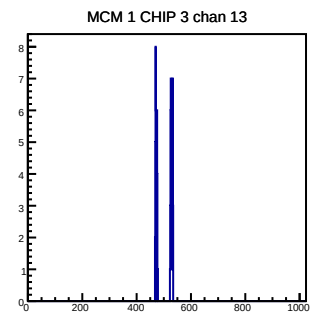
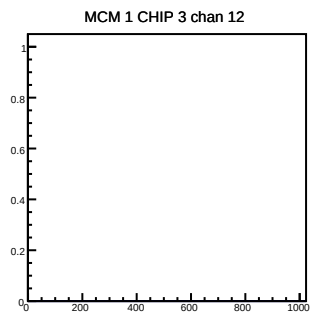
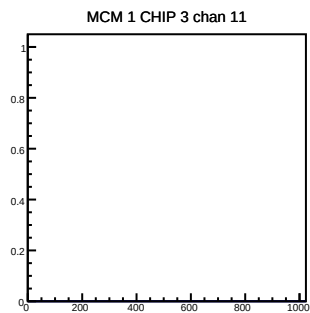
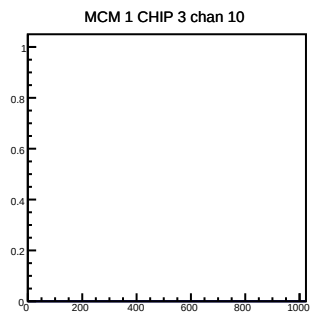
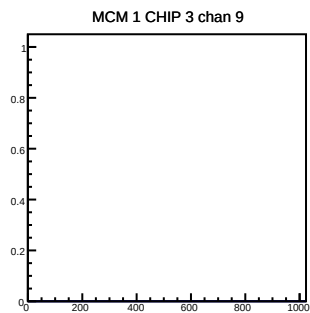
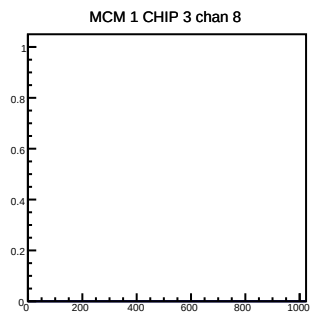
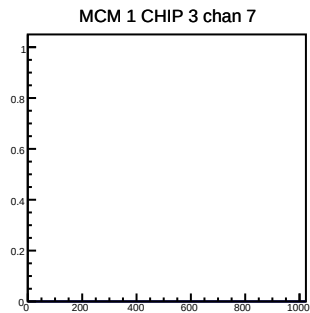
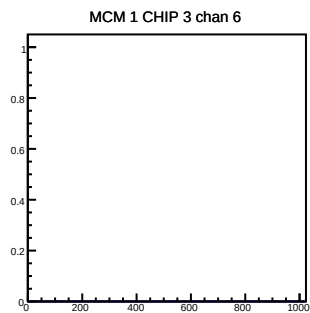
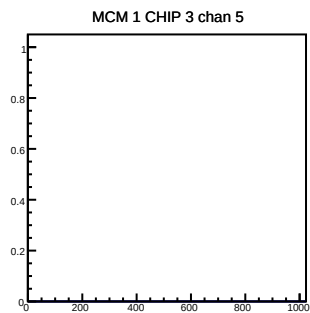
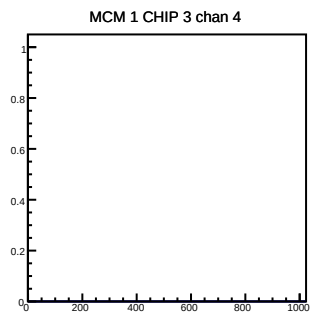
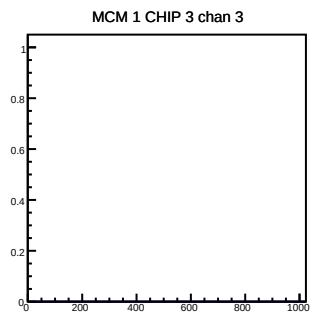
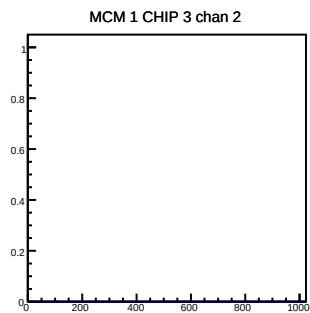
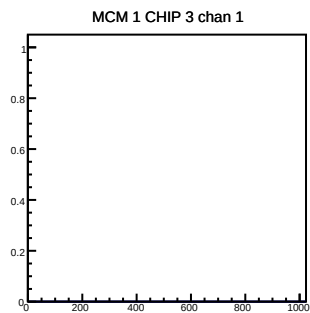
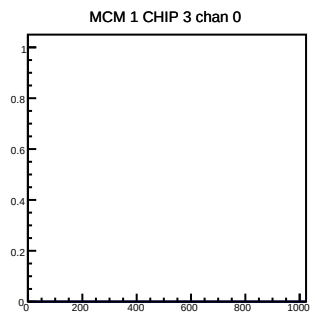
MCM 1 CHIP 1 chan 14

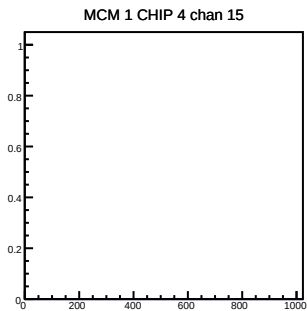
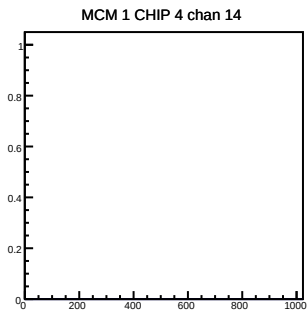
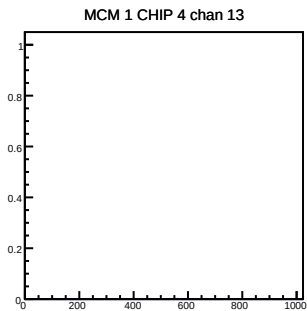
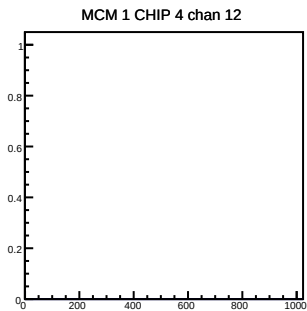
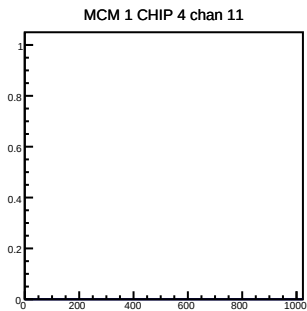
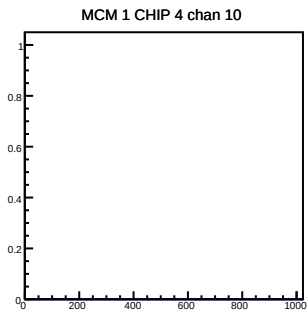
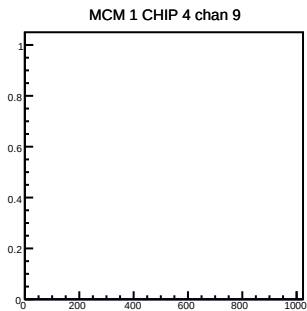
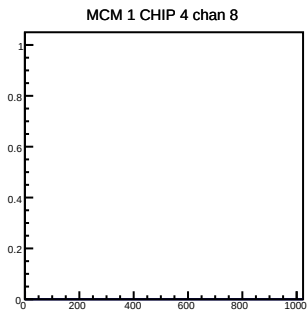
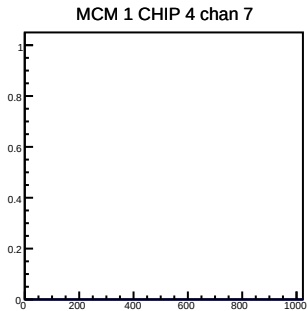
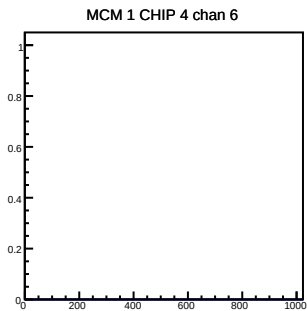
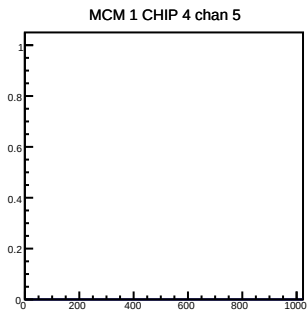
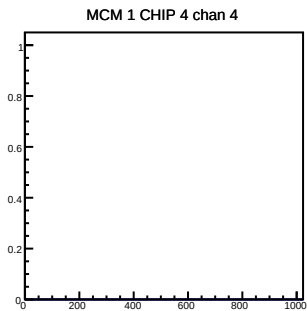
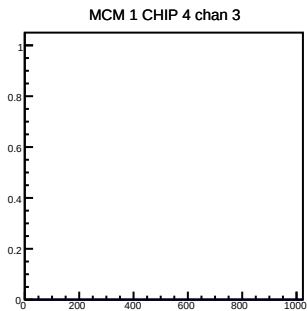
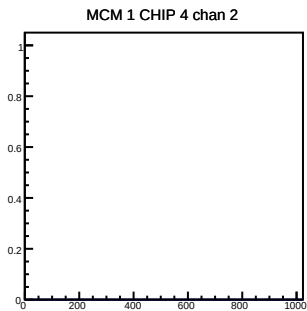
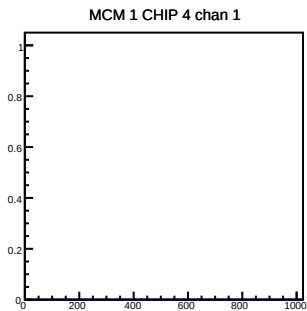
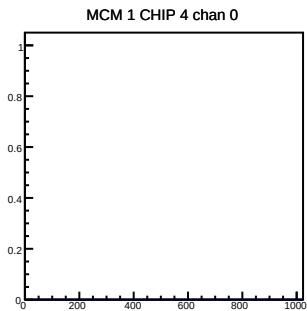


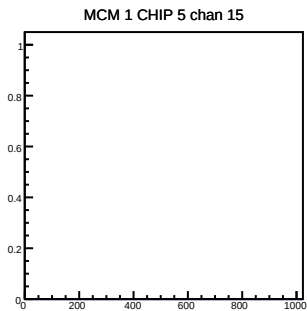
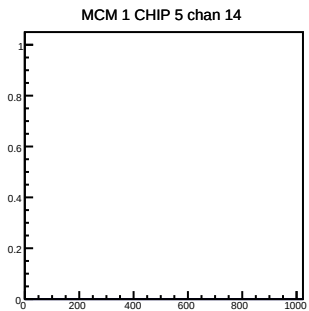
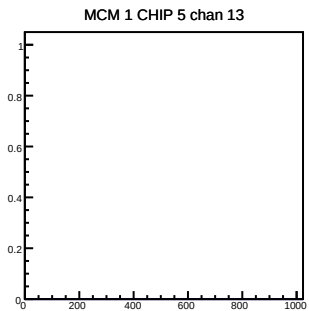
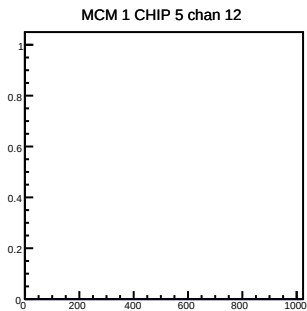
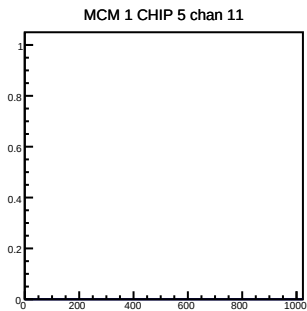
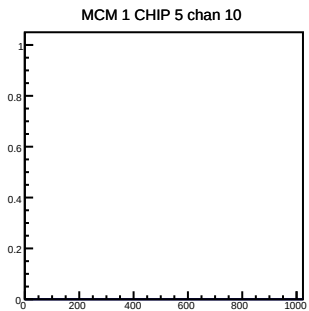
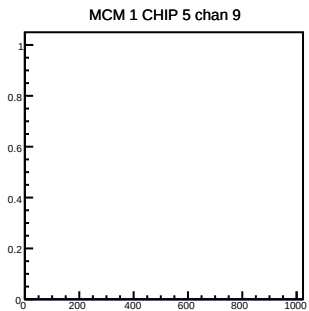
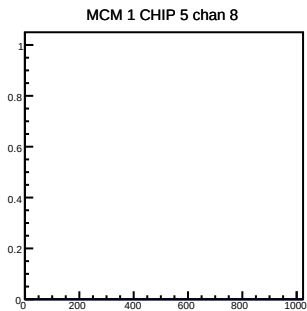
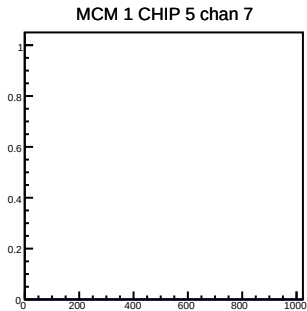
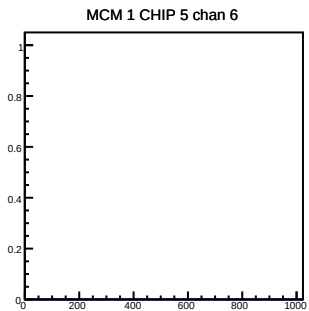
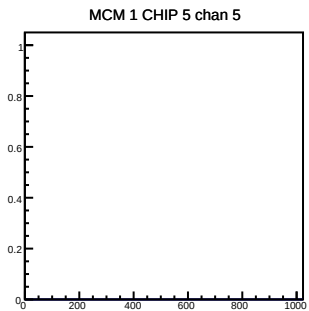
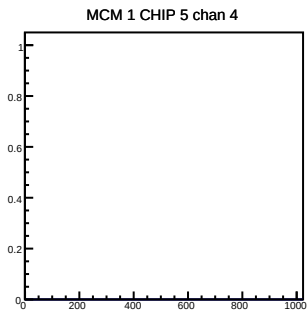
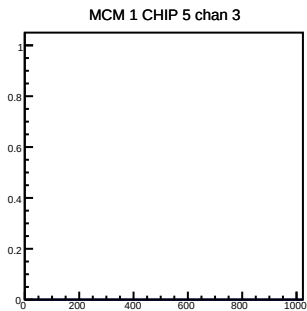
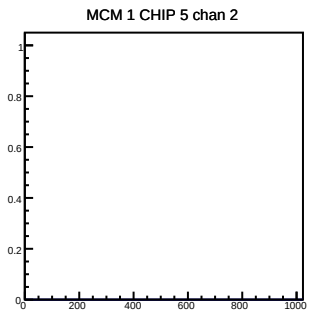
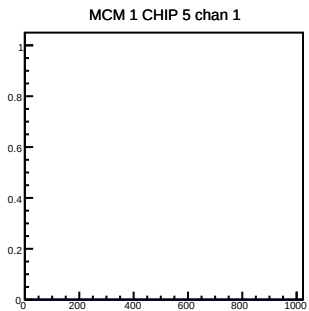
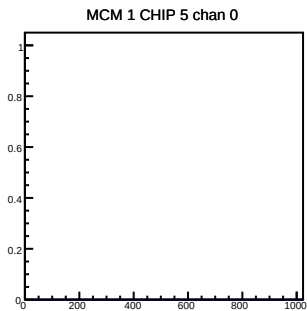
MCM 1 CHIP 1 chan 15



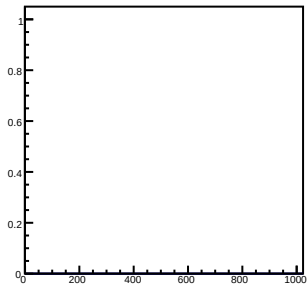




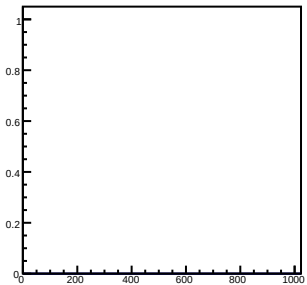




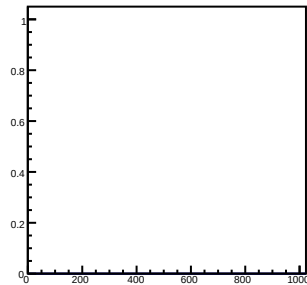
MCM 1 CHIP 6 chan 0



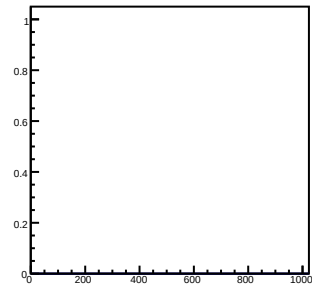
MCM 1 CHIP 6 chan 1



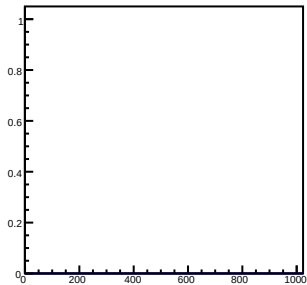
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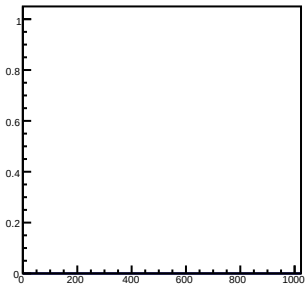
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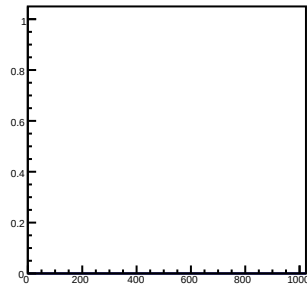
MCM 1 CHIP 6 chan 4



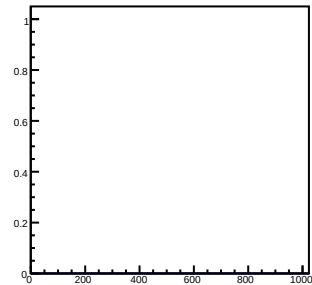
MCM 1 CHIP 6 chan 5



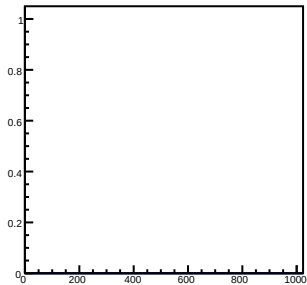
MCM 1 CHIP 6 chan 6



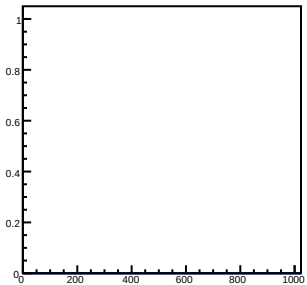
MCM 1 CHIP 6 chan 7



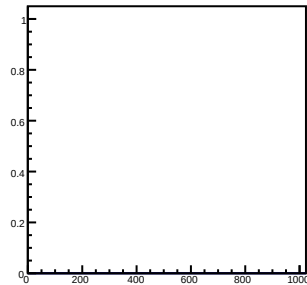
MCM 1 CHIP 6 chan 8



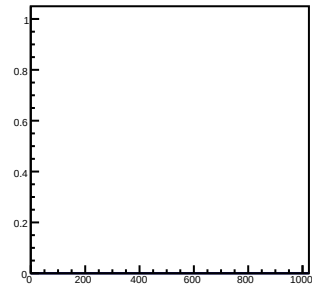
MCM 1 CHIP 6 chan 9



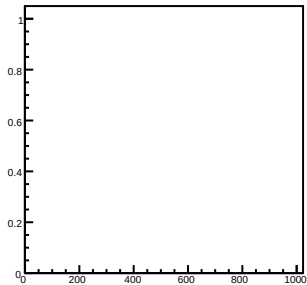
MCM 1 CHIP 6 chan 10



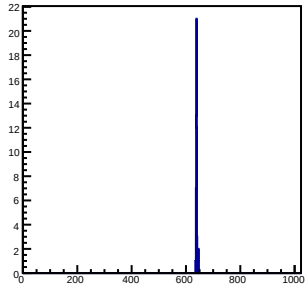
MCM 1 CHIP 6 chan 11



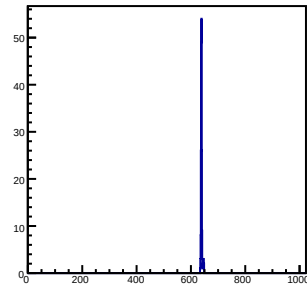
MCM 1 CHIP 6 chan 12



MCM 1 CHIP 6 chan 13



MCM 1 CHIP 6 chan 14



MCM 1 CHIP 6 chan 15

