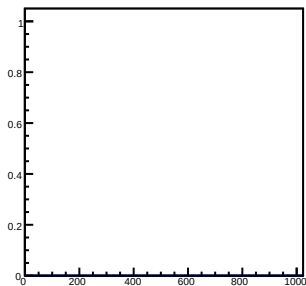
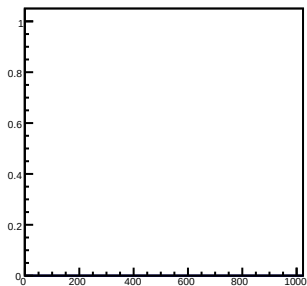


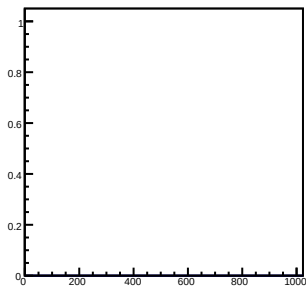
MCM 0 CHIP 0 chan 0



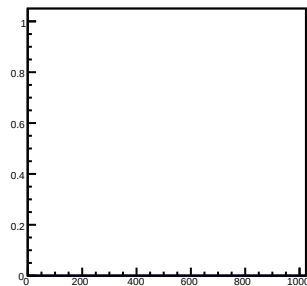
MCM 0 CHIP 0 chan 1



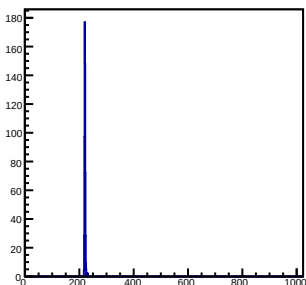
MCM 0 CHIP 0 chan 2



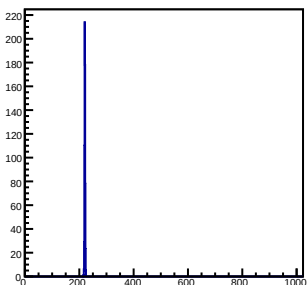
MCM 0 CHIP 0 chan 3



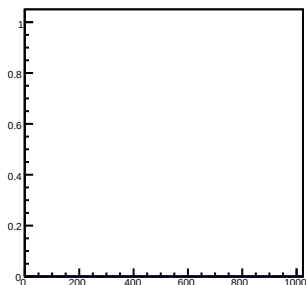
MCM 0 CHIP 0 chan 4



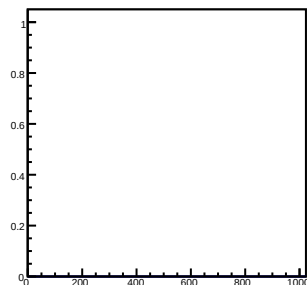
MCM 0 CHIP 0 chan 5



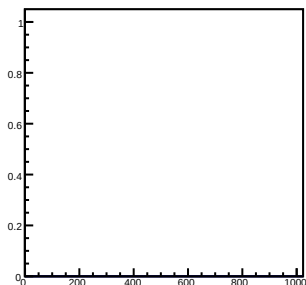
MCM 0 CHIP 0 chan 6



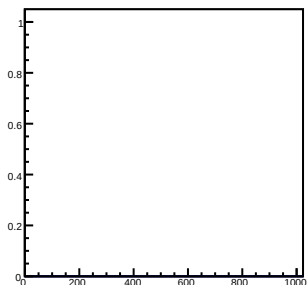
MCM 0 CHIP 0 chan 7



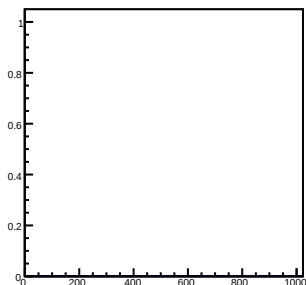
MCM 0 CHIP 0 chan 8



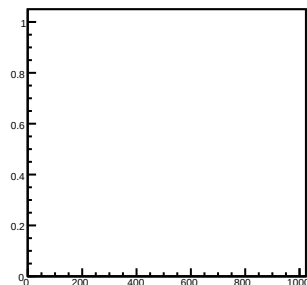
MCM 0 CHIP 0 chan 9



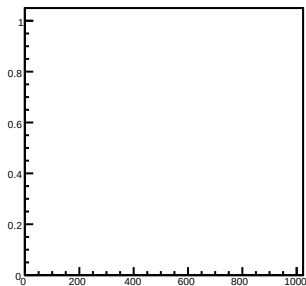
MCM 0 CHIP 0 chan 10



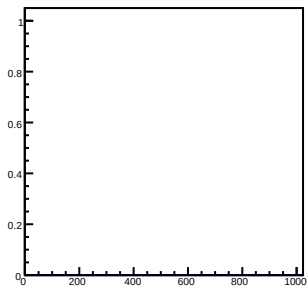
MCM 0 CHIP 0 chan 11



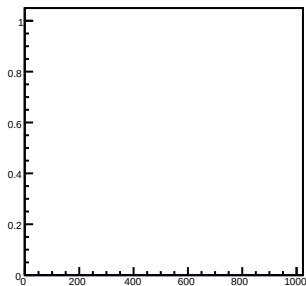
MCM 0 CHIP 0 chan 12



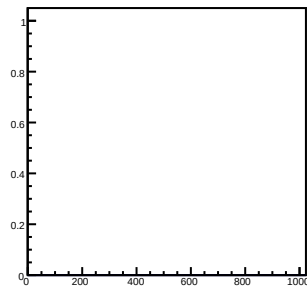
MCM 0 CHIP 0 chan 13



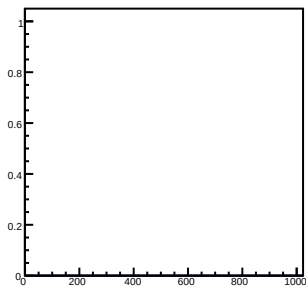
MCM 0 CHIP 0 chan 14



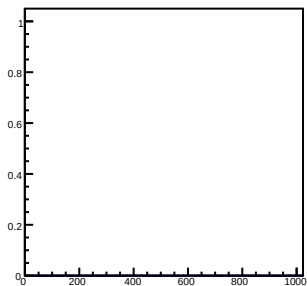
MCM 0 CHIP 0 chan 15



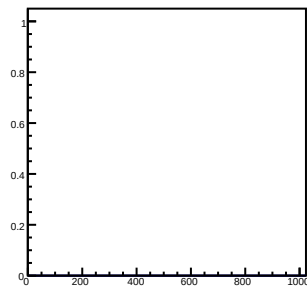
MCM 0 CHIP 1 chan 0



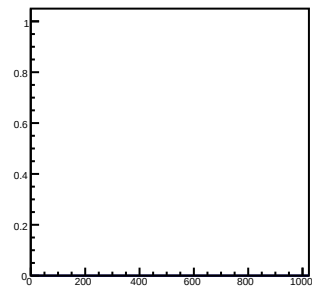
MCM 0 CHIP 1 chan 1



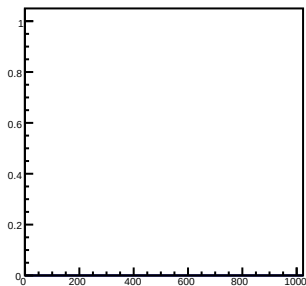
MCM 0 CHIP 1 chan 2



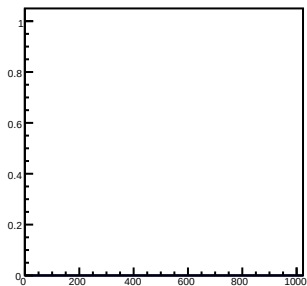
MCM 0 CHIP 1 chan 3



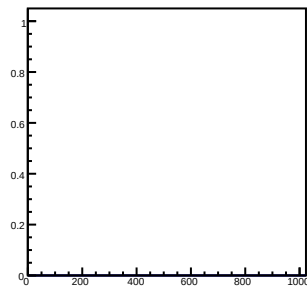
MCM 0 CHIP 1 chan 4



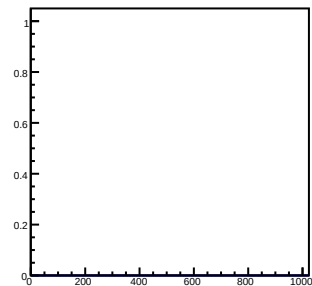
MCM 0 CHIP 1 chan 5



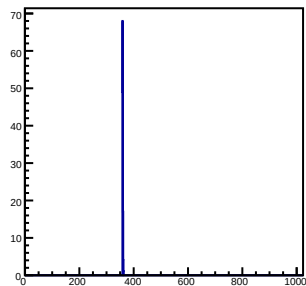
MCM 0 CHIP 1 chan 6



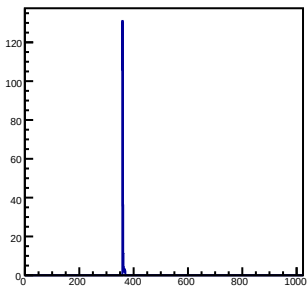
MCM 0 CHIP 1 chan 7



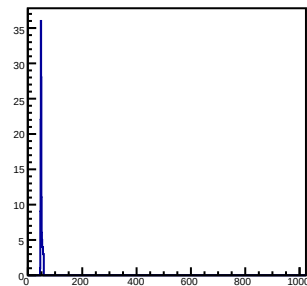
MCM 0 CHIP 1 chan 8



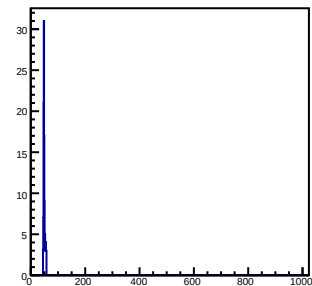
MCM 0 CHIP 1 chan 9



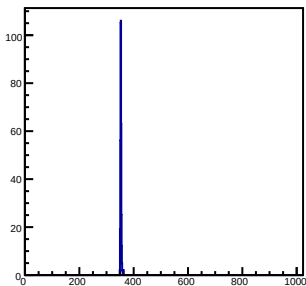
MCM 0 CHIP 1 chan 10



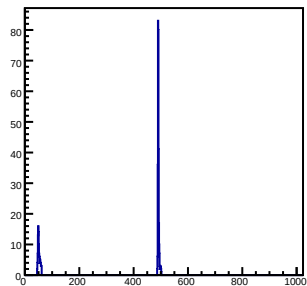
MCM 0 CHIP 1 chan 11



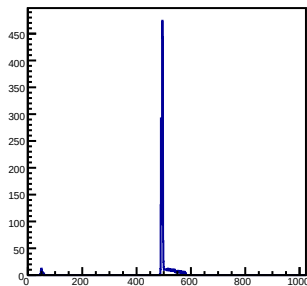
MCM 0 CHIP 1 chan 12



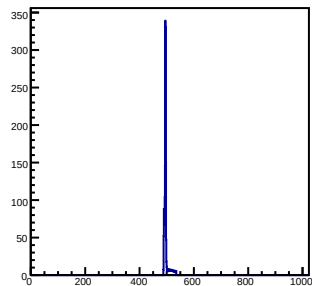
MCM 0 CHIP 1 chan 13



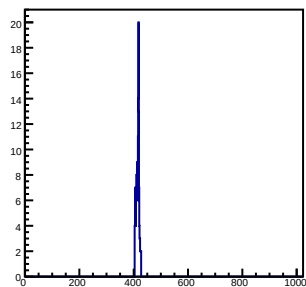
MCM 0 CHIP 1 chan 14



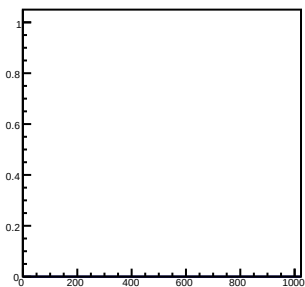
MCM 0 CHIP 1 chan 15



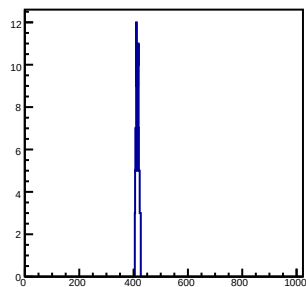
MCM 0 CHIP 2 chan 0



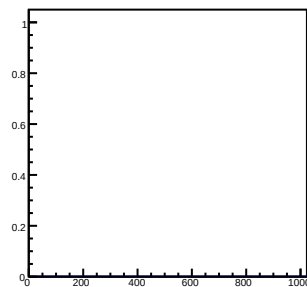
MCM 0 CHIP 2 chan 1



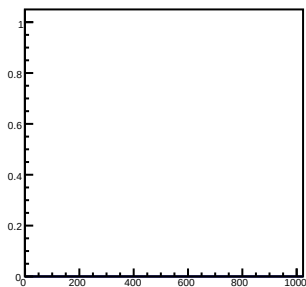
MCM 0 CHIP 2 chan 2



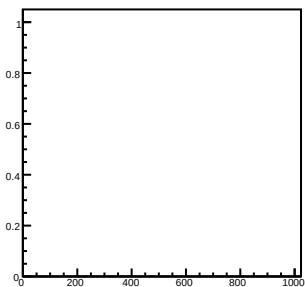
MCM 0 CHIP 2 chan 3



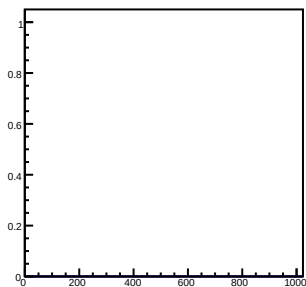
MCM 0 CHIP 2 chan 4



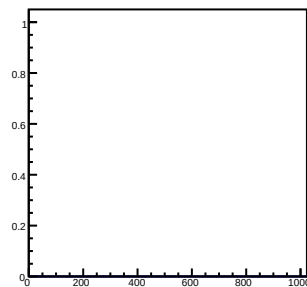
MCM 0 CHIP 2 chan 5



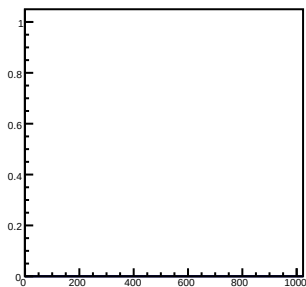
MCM 0 CHIP 2 chan 6



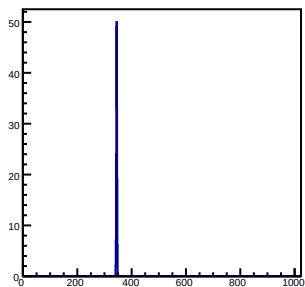
MCM 0 CHIP 2 chan 7



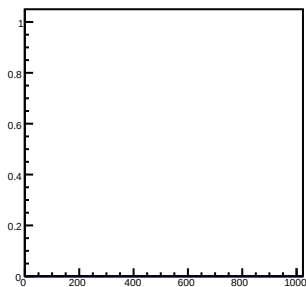
MCM 0 CHIP 2 chan 8



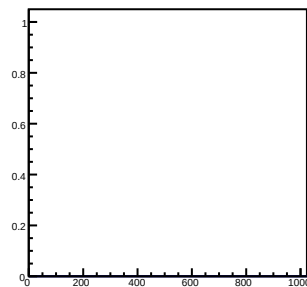
MCM 0 CHIP 2 chan 9



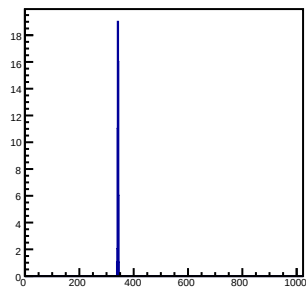
MCM 0 CHIP 2 chan 10



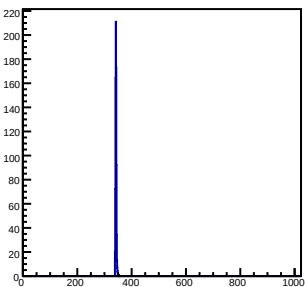
MCM 0 CHIP 2 chan 11



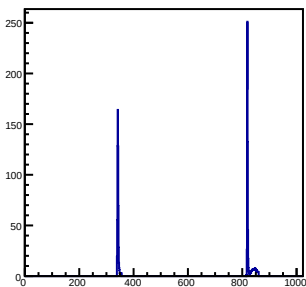
MCM 0 CHIP 2 chan 12



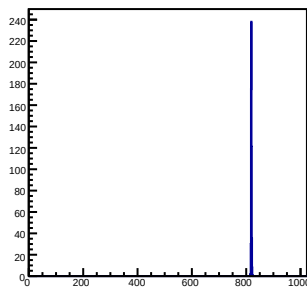
MCM 0 CHIP 2 chan 13

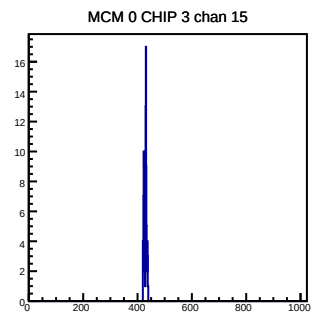
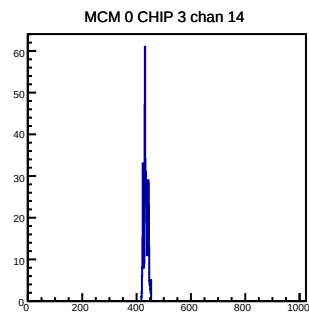
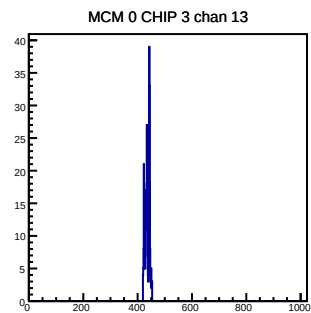
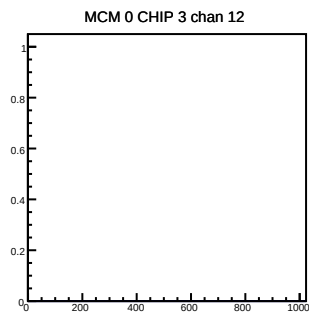
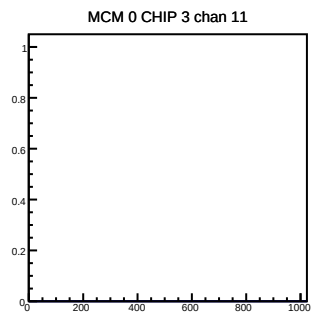
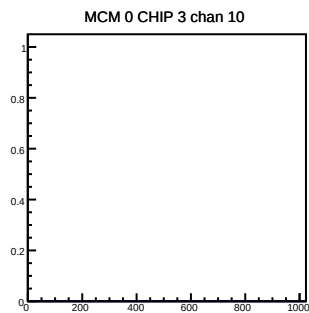
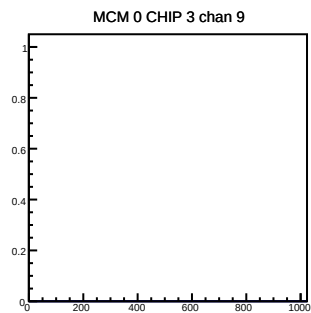
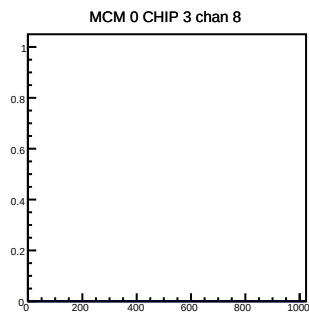
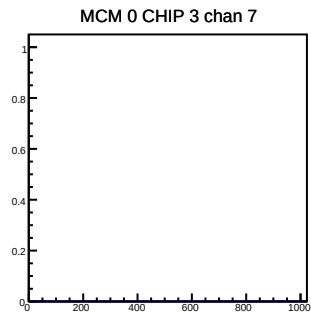
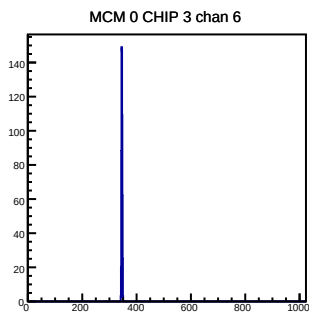
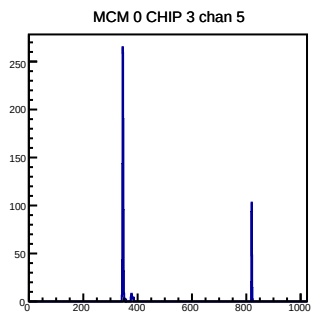
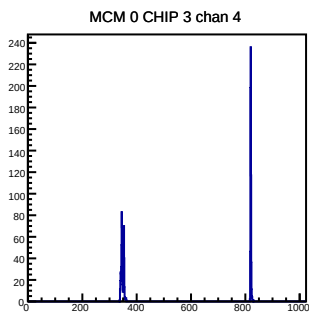
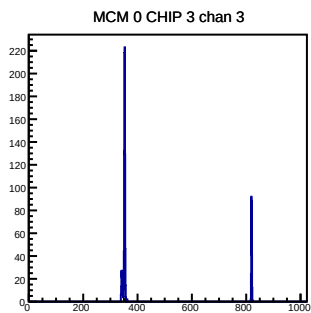
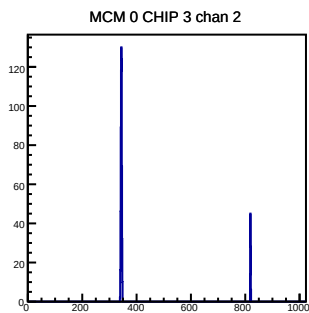
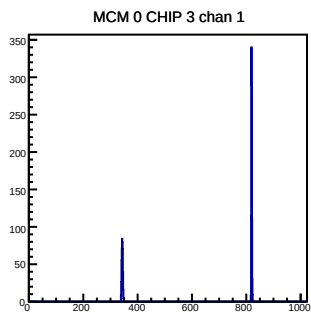
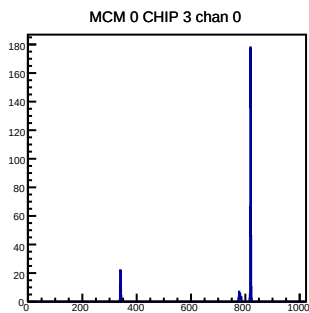


MCM 0 CHIP 2 chan 14

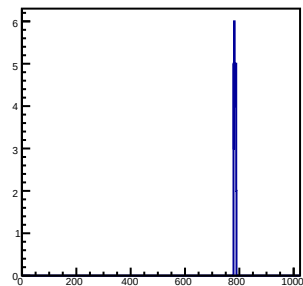


MCM 0 CHIP 2 chan 15

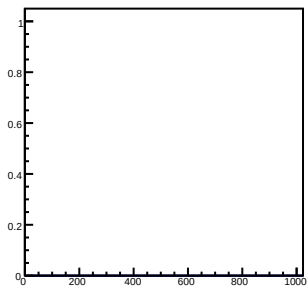




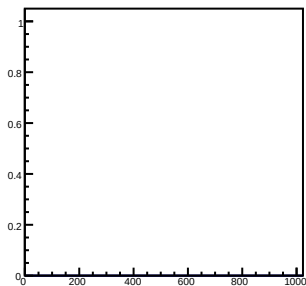
MCM 0 CHIP 4 chan 0



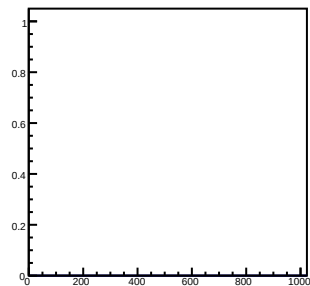
MCM 0 CHIP 4 chan 1



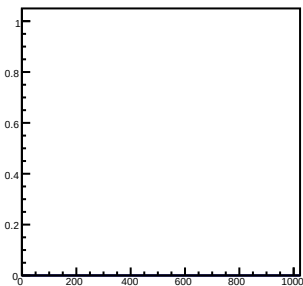
MCM 0 CHIP 4 chan 2



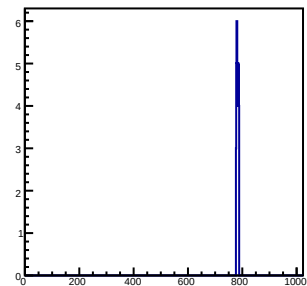
MCM 0 CHIP 4 chan 3



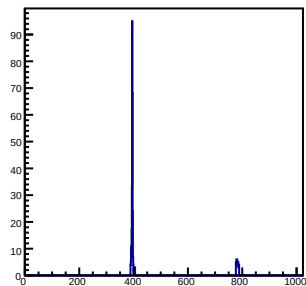
MCM 0 CHIP 4 chan 4



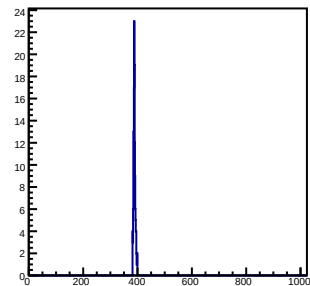
MCM 0 CHIP 4 chan 5



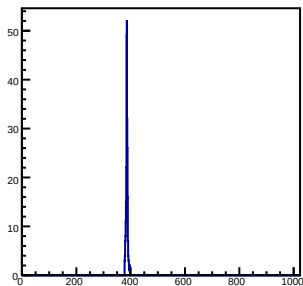
MCM 0 CHIP 4 chan 6



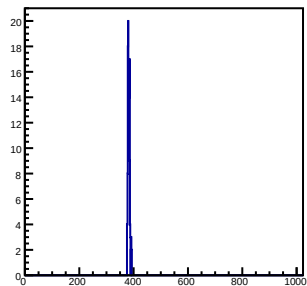
MCM 0 CHIP 4 chan 7



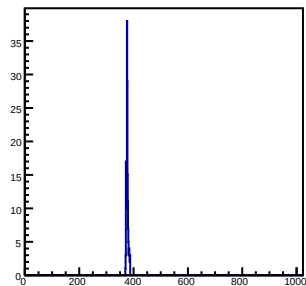
MCM 0 CHIP 4 chan 8



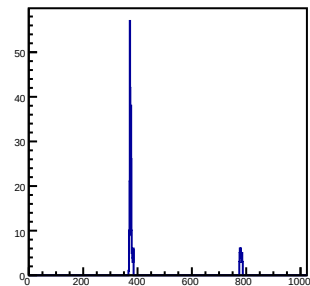
MCM 0 CHIP 4 chan 9



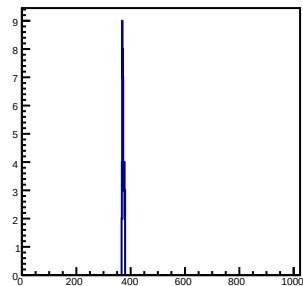
MCM 0 CHIP 4 chan 10



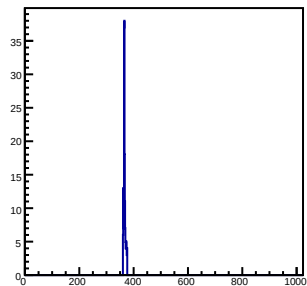
MCM 0 CHIP 4 chan 11



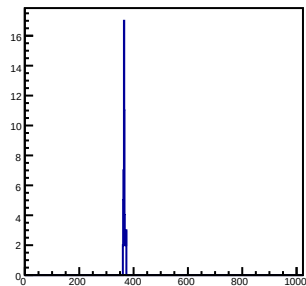
MCM 0 CHIP 4 chan 12



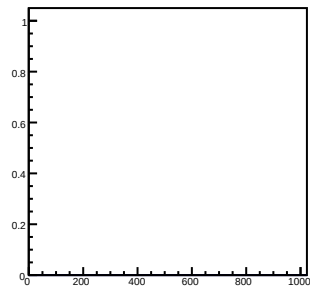
MCM 0 CHIP 4 chan 13



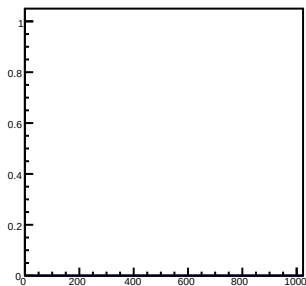
MCM 0 CHIP 4 chan 14



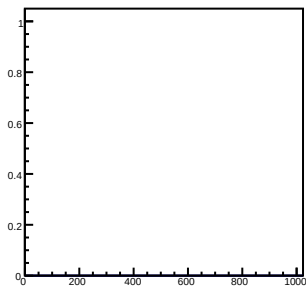
MCM 0 CHIP 4 chan 15



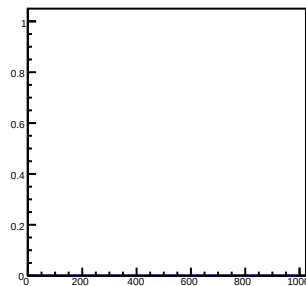
MCM 0 CHIP 5 chan 0



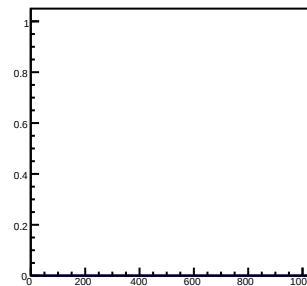
MCM 0 CHIP 5 chan 1



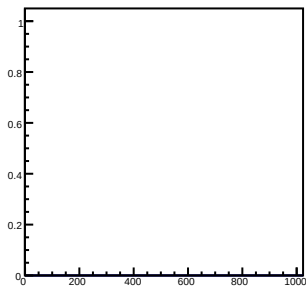
MCM 0 CHIP 5 chan 2



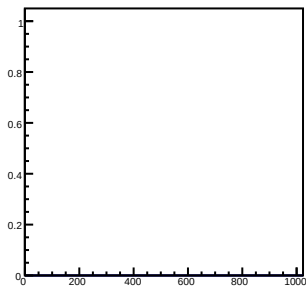
MCM 0 CHIP 5 chan 3



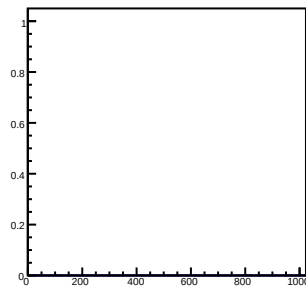
MCM 0 CHIP 5 chan 4



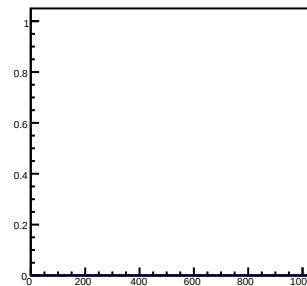
MCM 0 CHIP 5 chan 5



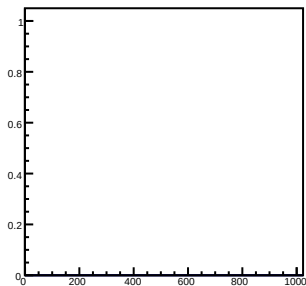
MCM 0 CHIP 5 chan 6



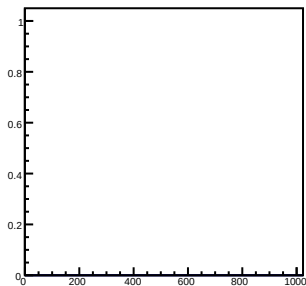
MCM 0 CHIP 5 chan 7



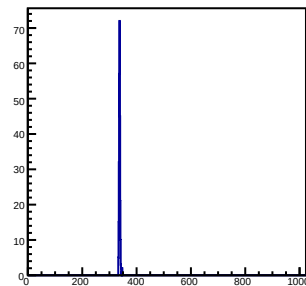
MCM 0 CHIP 5 chan 8



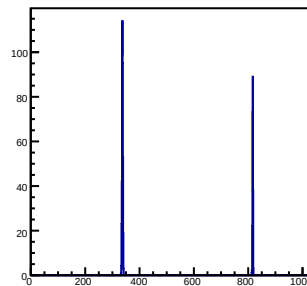
MCM 0 CHIP 5 chan 9



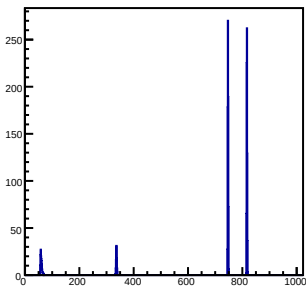
MCM 0 CHIP 5 chan 10



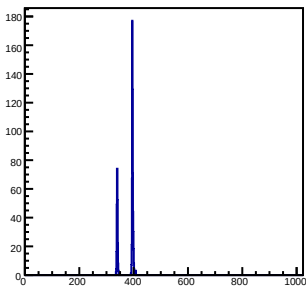
MCM 0 CHIP 5 chan 11



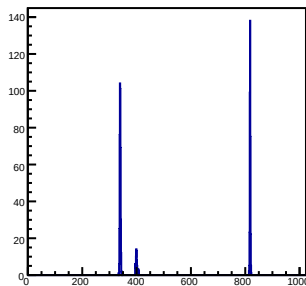
MCM 0 CHIP 5 chan 12



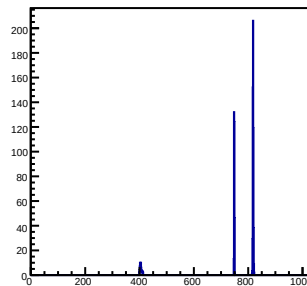
MCM 0 CHIP 5 chan 13



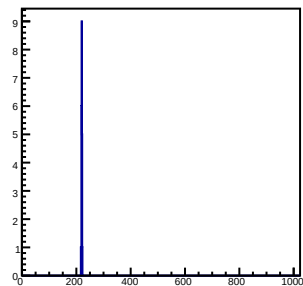
MCM 0 CHIP 5 chan 14



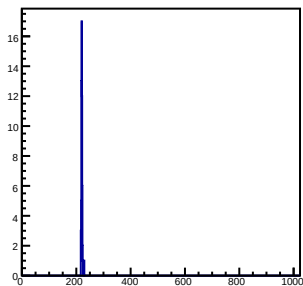
MCM 0 CHIP 5 chan 15



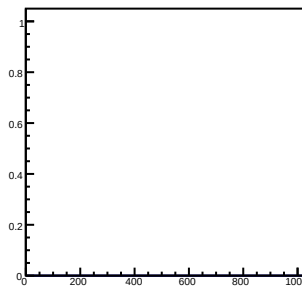
MCM 0 CHIP 6 chan 0



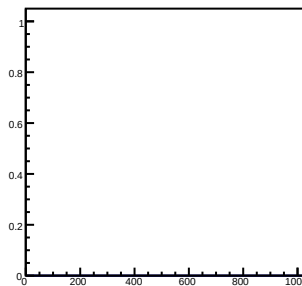
MCM 0 CHIP 6 chan 1



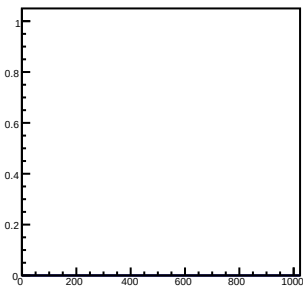
MCM 0 CHIP 6 chan 2



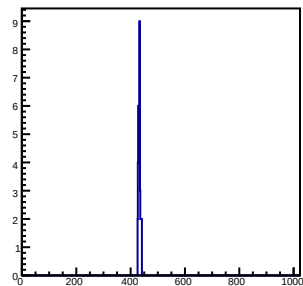
MCM 0 CHIP 6 chan 3



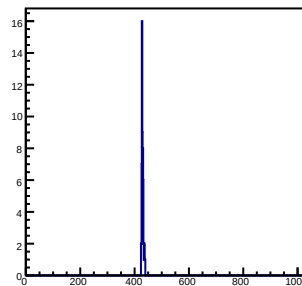
MCM 0 CHIP 6 chan 4



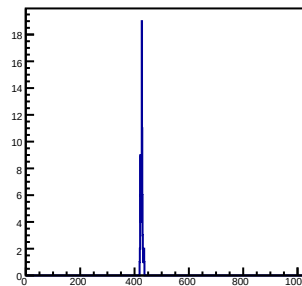
MCM 0 CHIP 6 chan 5



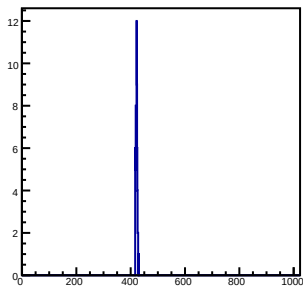
MCM 0 CHIP 6 chan 6



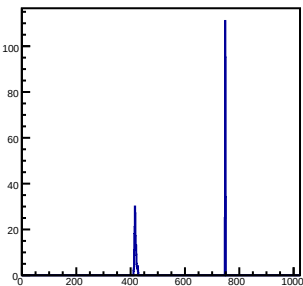
MCM 0 CHIP 6 chan 7



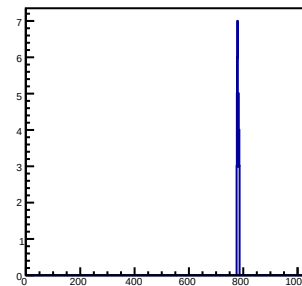
MCM 0 CHIP 6 chan 8



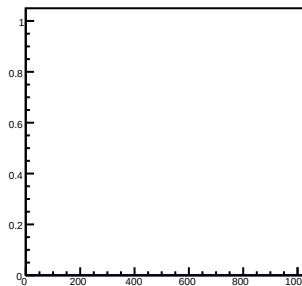
MCM 0 CHIP 6 chan 9



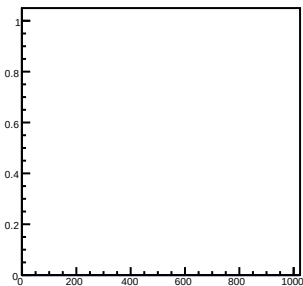
MCM 0 CHIP 6 chan 10



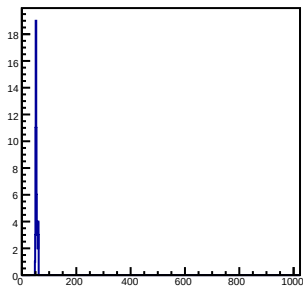
MCM 0 CHIP 6 chan 11



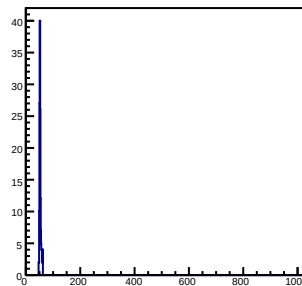
MCM 0 CHIP 6 chan 12



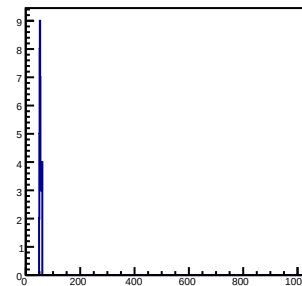
MCM 0 CHIP 6 chan 13



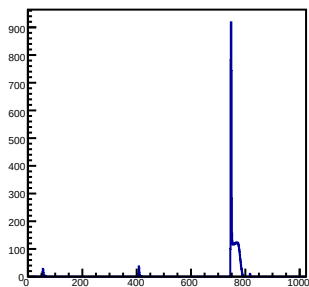
MCM 0 CHIP 6 chan 14



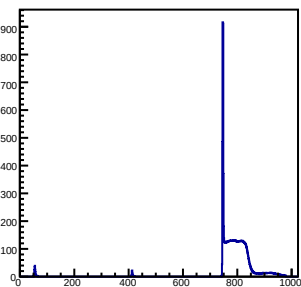
MCM 0 CHIP 6 chan 15



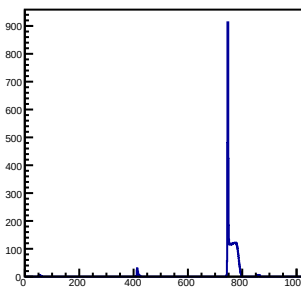
MCM 0 CHIP 7 chan 0



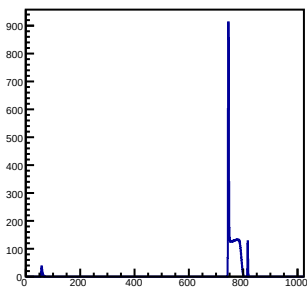
MCM 0 CHIP 7 chan 1



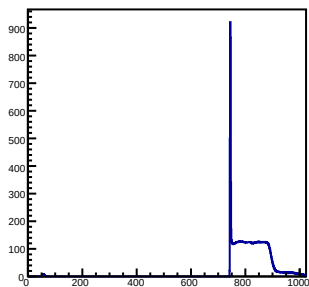
MCM 0 CHIP 7 chan 2



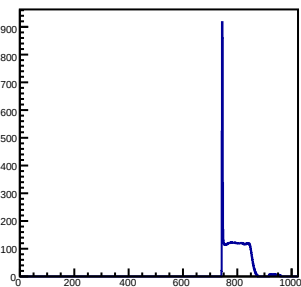
MCM 0 CHIP 7 chan 3



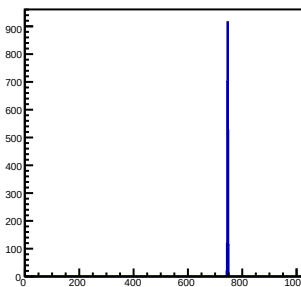
MCM 0 CHIP 7 chan 4



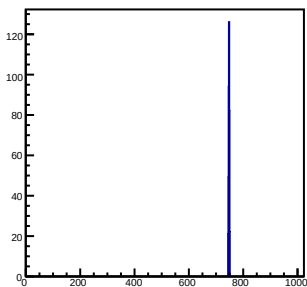
MCM 0 CHIP 7 chan 5



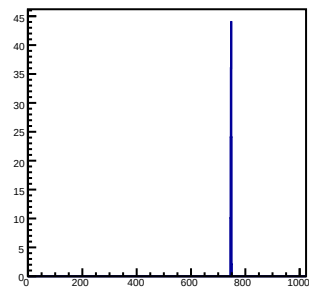
MCM 0 CHIP 7 chan 6



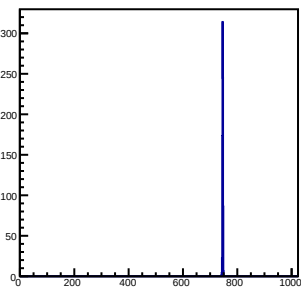
MCM 0 CHIP 7 chan 7



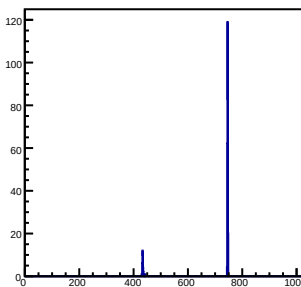
MCM 0 CHIP 7 chan 8



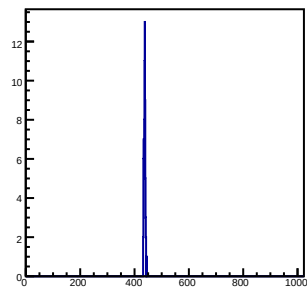
MCM 0 CHIP 7 chan 9



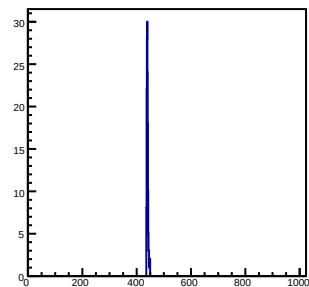
MCM 0 CHIP 7 chan 10



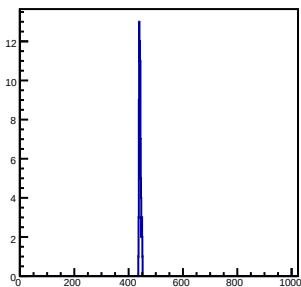
MCM 0 CHIP 7 chan 11



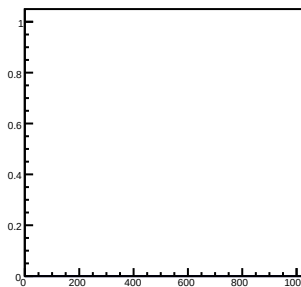
MCM 0 CHIP 7 chan 12



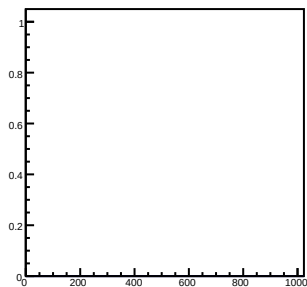
MCM 0 CHIP 7 chan 13



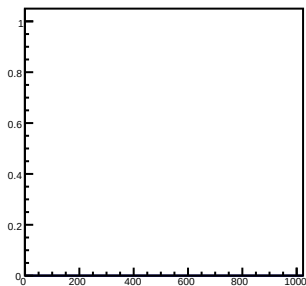
MCM 0 CHIP 7 chan 14



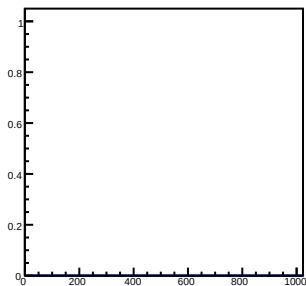
MCM 0 CHIP 7 chan 15



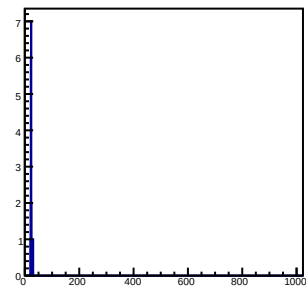
MCM 1 CHIP 0 chan 0



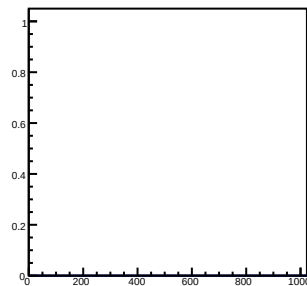
MCM 1 CHIP 0 chan 1



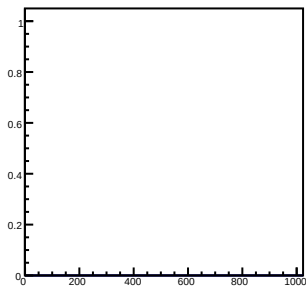
MCM 1 CHIP 0 chan 2



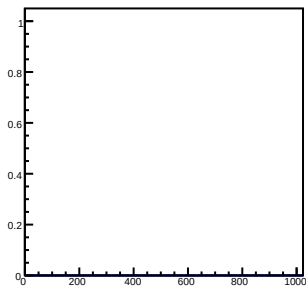
MCM 1 CHIP 0 chan 3



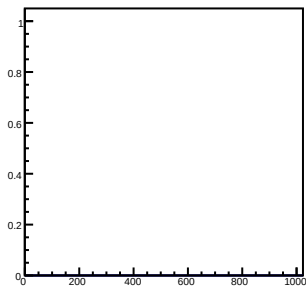
MCM 1 CHIP 0 chan 4



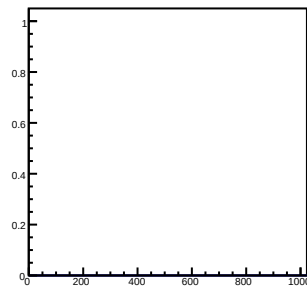
MCM 1 CHIP 0 chan 5



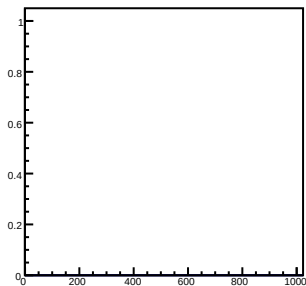
MCM 1 CHIP 0 chan 6



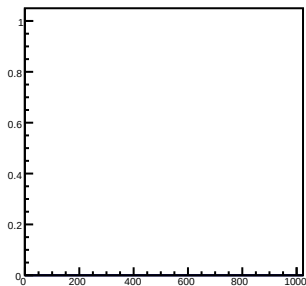
MCM 1 CHIP 0 chan 7



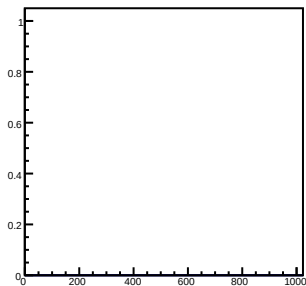
MCM 1 CHIP 0 chan 8



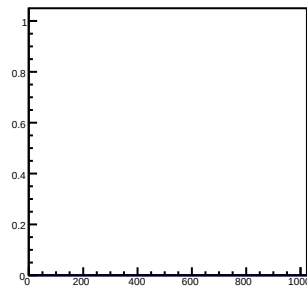
MCM 1 CHIP 0 chan 9



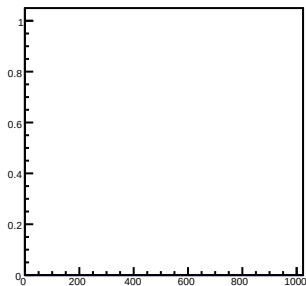
MCM 1 CHIP 0 chan 10



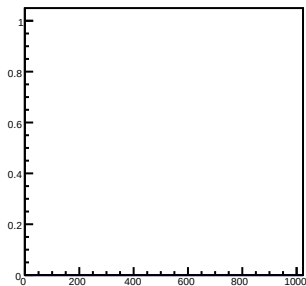
MCM 1 CHIP 0 chan 11



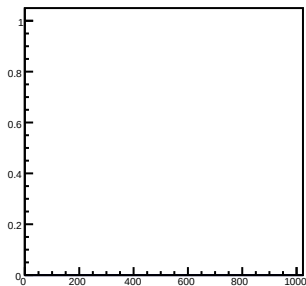
MCM 1 CHIP 0 chan 12



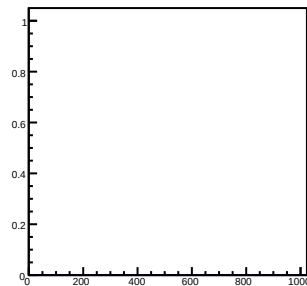
MCM 1 CHIP 0 chan 13



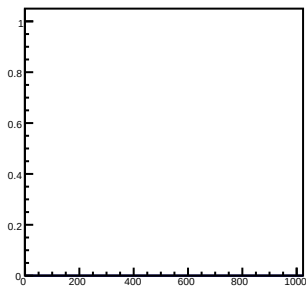
MCM 1 CHIP 0 chan 14



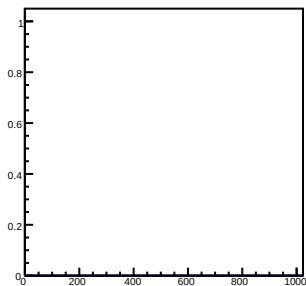
MCM 1 CHIP 0 chan 15



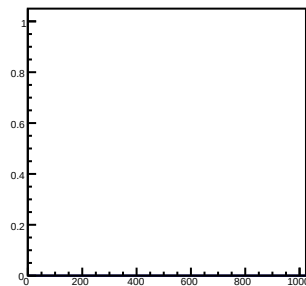
MCM 1 CHIP 1 chan 0



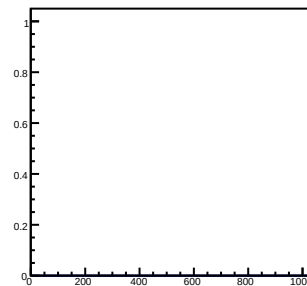
MCM 1 CHIP 1 chan 1



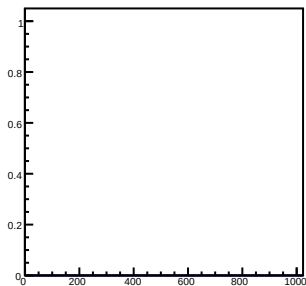
MCM 1 CHIP 1 chan 2



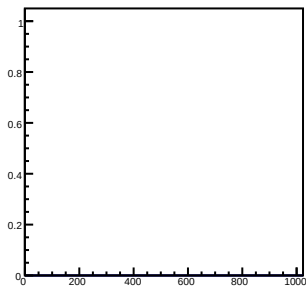
MCM 1 CHIP 1 chan 3



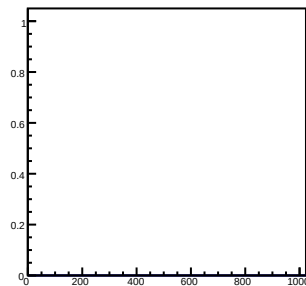
MCM 1 CHIP 1 chan 4



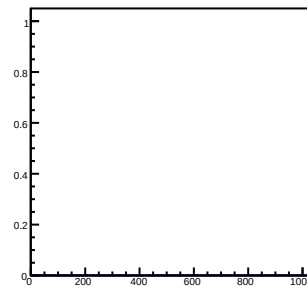
MCM 1 CHIP 1 chan 5



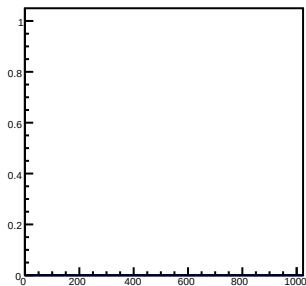
MCM 1 CHIP 1 chan 6



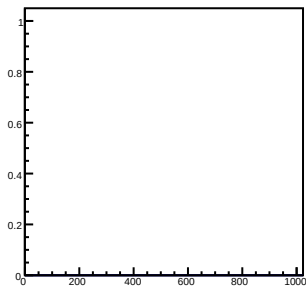
MCM 1 CHIP 1 chan 7



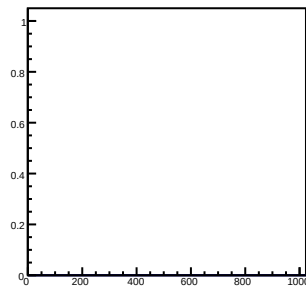
MCM 1 CHIP 1 chan 8



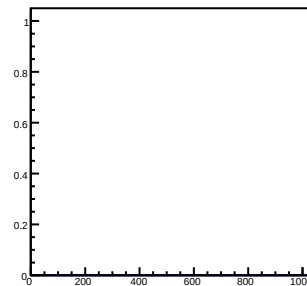
MCM 1 CHIP 1 chan 9



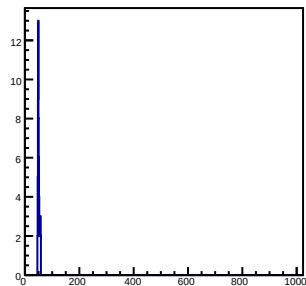
MCM 1 CHIP 1 chan 10



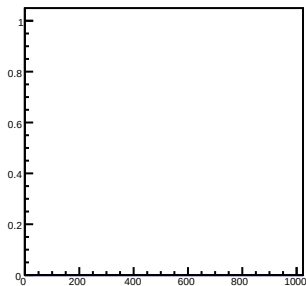
MCM 1 CHIP 1 chan 11



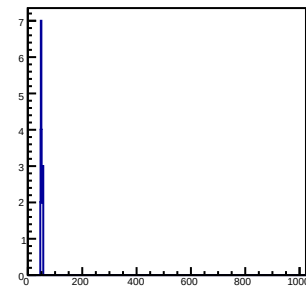
MCM 1 CHIP 1 chan 12



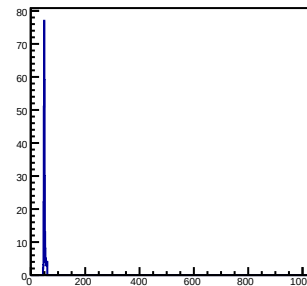
MCM 1 CHIP 1 chan 13

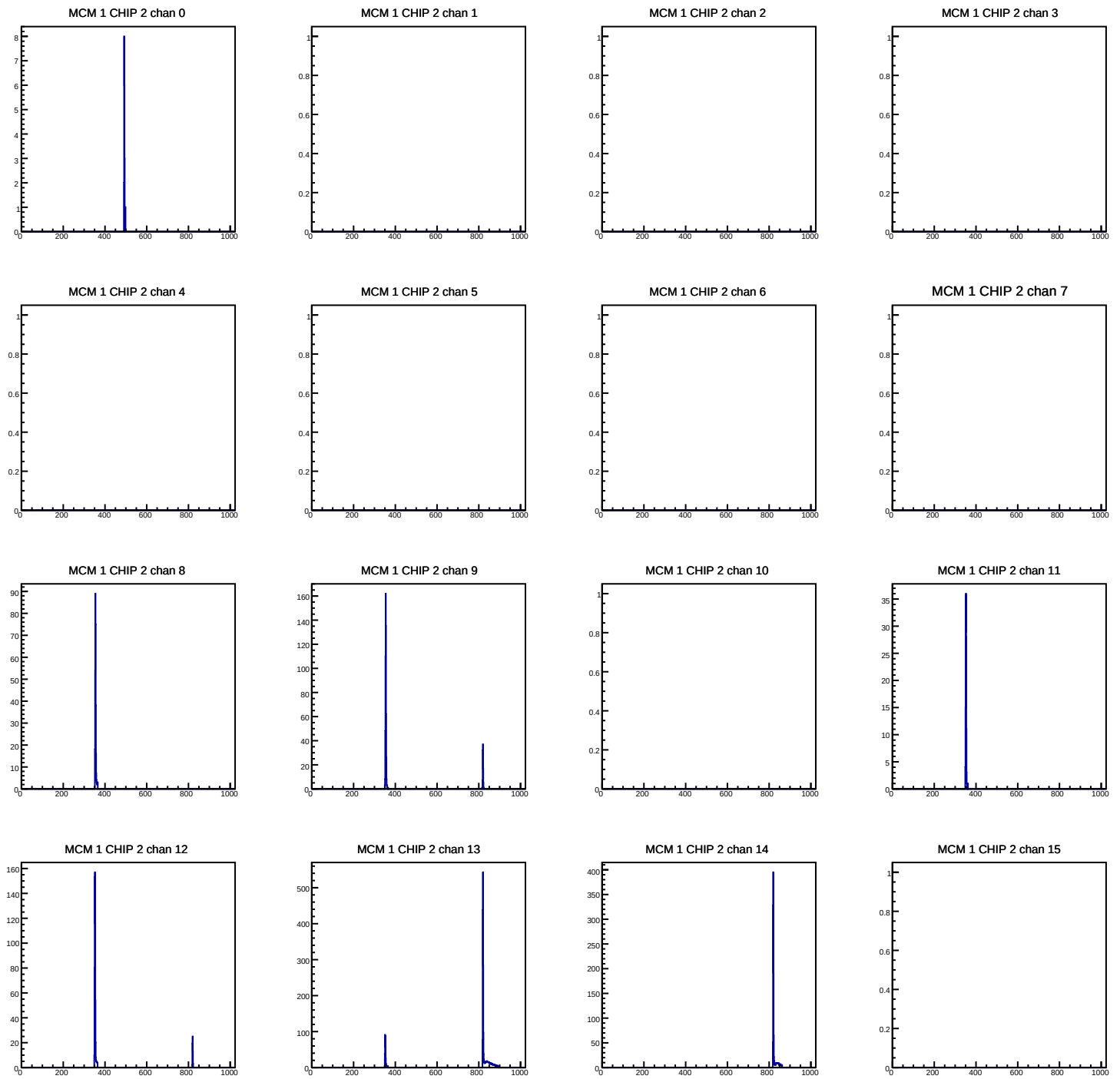


MCM 1 CHIP 1 chan 14

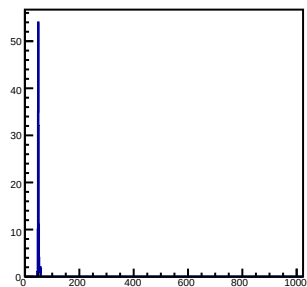


MCM 1 CHIP 1 chan 15

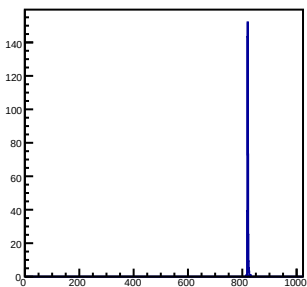




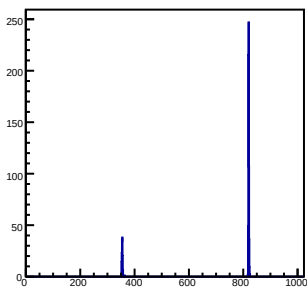
MCM 1 CHIP 3 chan 0



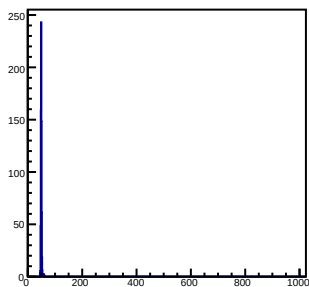
MCM 1 CHIP 3 chan 1



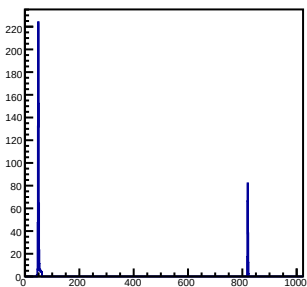
MCM 1 CHIP 3 chan 2



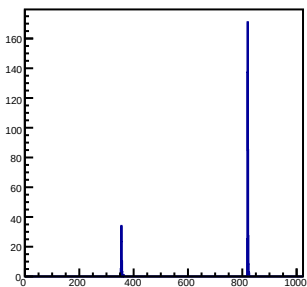
MCM 1 CHIP 3 chan 3



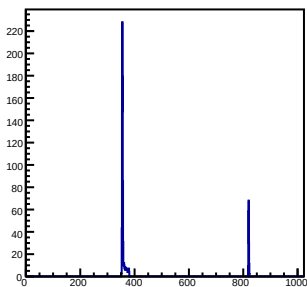
MCM 1 CHIP 3 chan 4



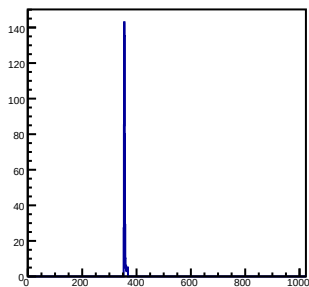
MCM 1 CHIP 3 chan 5



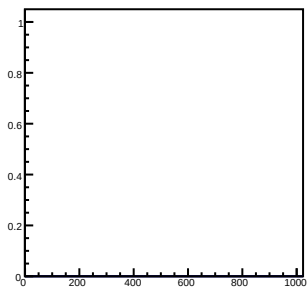
MCM 1 CHIP 3 chan 6



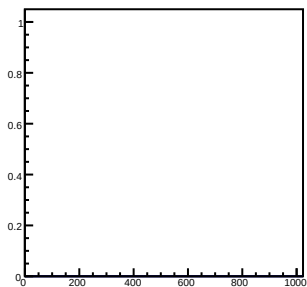
MCM 1 CHIP 3 chan 7



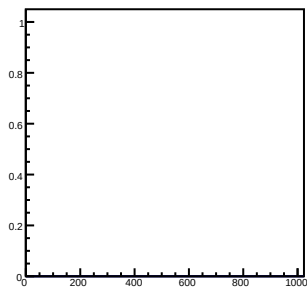
MCM 1 CHIP 3 chan 8



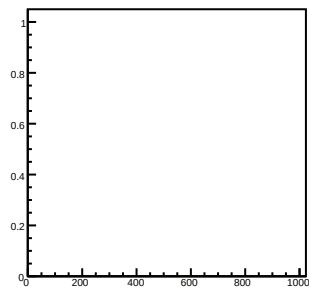
MCM 1 CHIP 3 chan 9



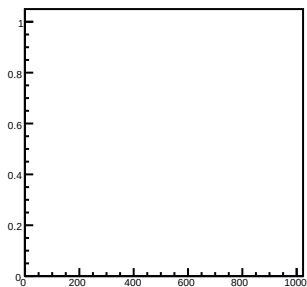
MCM 1 CHIP 3 chan 10



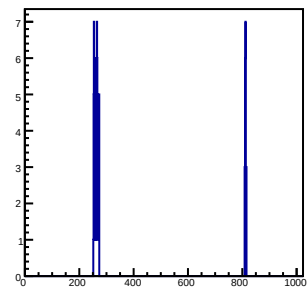
MCM 1 CHIP 3 chan 11



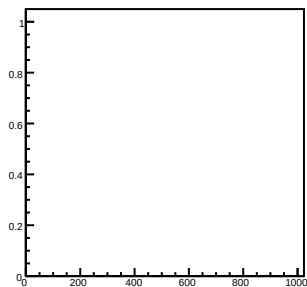
MCM 1 CHIP 3 chan 12



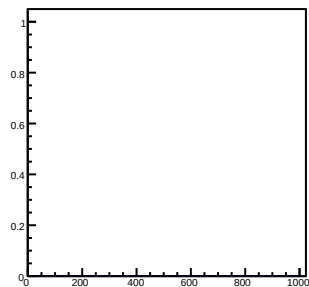
MCM 1 CHIP 3 chan 13



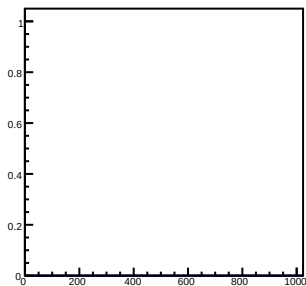
MCM 1 CHIP 3 chan 14



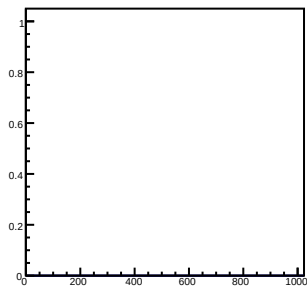
MCM 1 CHIP 3 chan 15



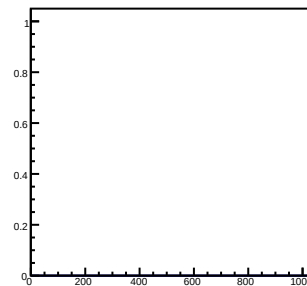
MCM 1 CHIP 4 chan 0



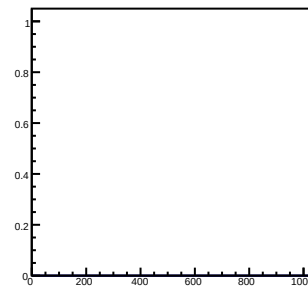
MCM 1 CHIP 4 chan 1



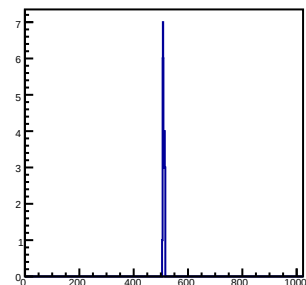
MCM 1 CHIP 4 chan 2



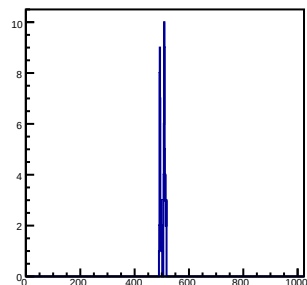
MCM 1 CHIP 4 chan 3



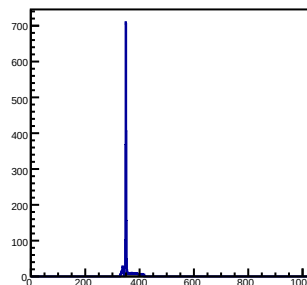
MCM 1 CHIP 4 chan 4



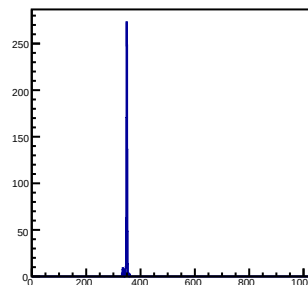
MCM 1 CHIP 4 chan 5



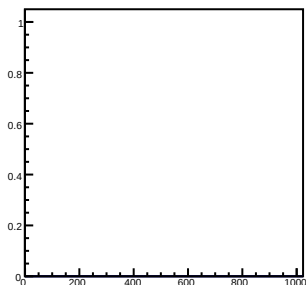
MCM 1 CHIP 4 chan 6



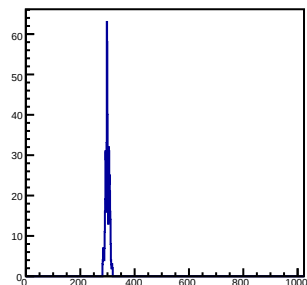
MCM 1 CHIP 4 chan 7



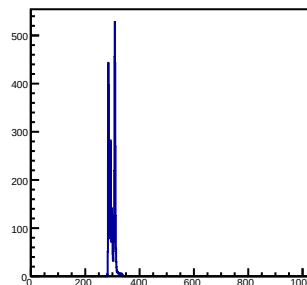
MCM 1 CHIP 4 chan 8



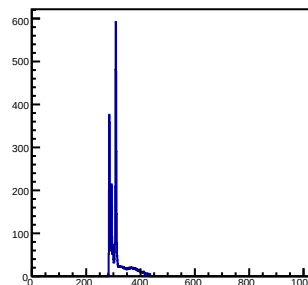
MCM 1 CHIP 4 chan 9



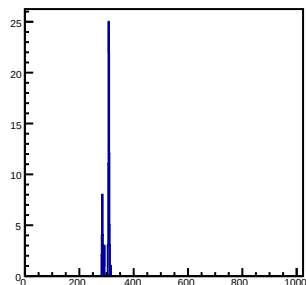
MCM 1 CHIP 4 chan 10



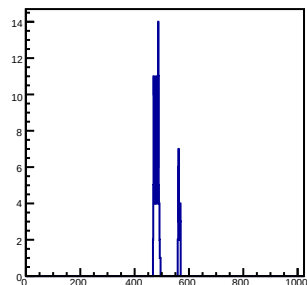
MCM 1 CHIP 4 chan 11



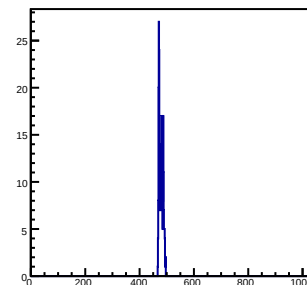
MCM 1 CHIP 4 chan 12



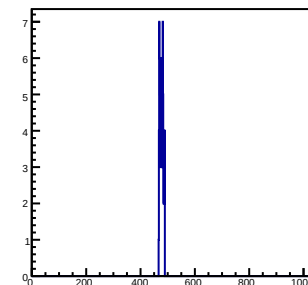
MCM 1 CHIP 4 chan 13



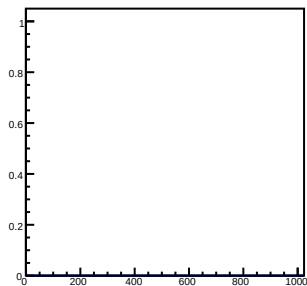
MCM 1 CHIP 4 chan 14



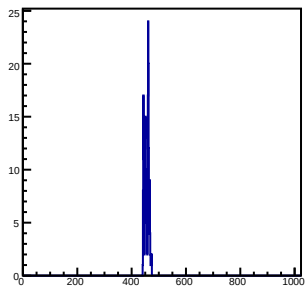
MCM 1 CHIP 4 chan 15



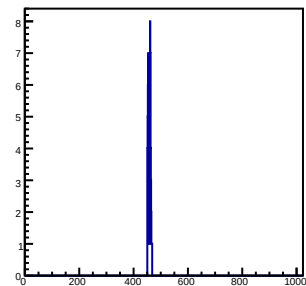
MCM 1 CHIP 5 chan 0



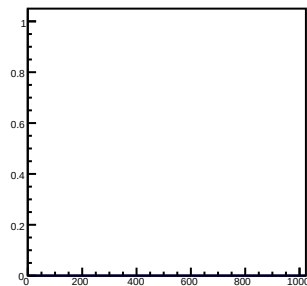
MCM 1 CHIP 5 chan 1



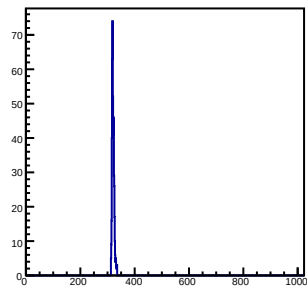
MCM 1 CHIP 5 chan 2



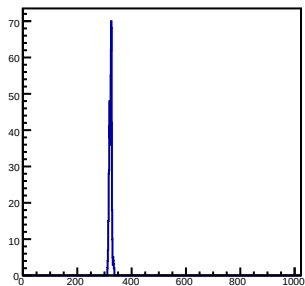
MCM 1 CHIP 5 chan 3



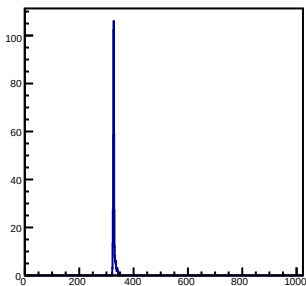
MCM 1 CHIP 5 chan 4



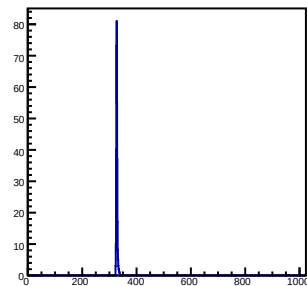
MCM 1 CHIP 5 chan 5



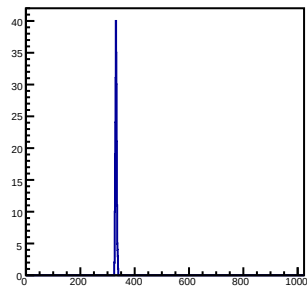
MCM 1 CHIP 5 chan 6



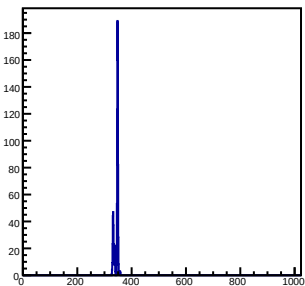
MCM 1 CHIP 5 chan 7



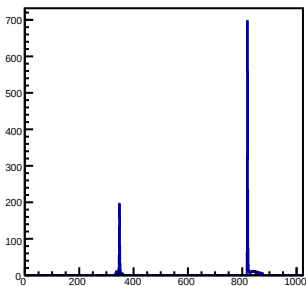
MCM 1 CHIP 5 chan 8



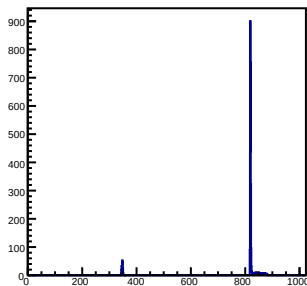
MCM 1 CHIP 5 chan 9



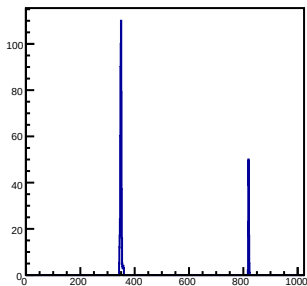
MCM 1 CHIP 5 chan 10



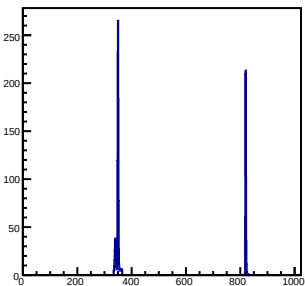
MCM 1 CHIP 5 chan 11



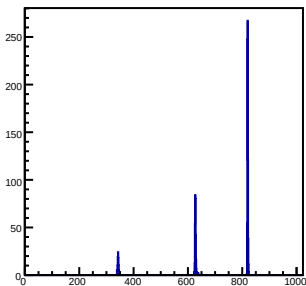
MCM 1 CHIP 5 chan 12



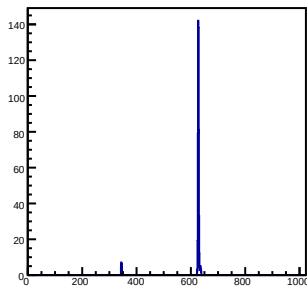
MCM 1 CHIP 5 chan 13



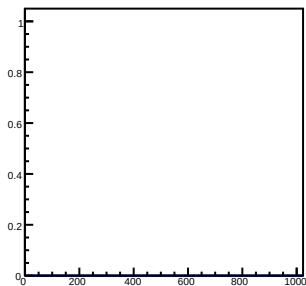
MCM 1 CHIP 5 chan 14



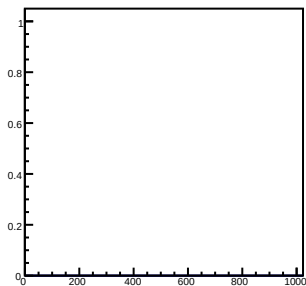
MCM 1 CHIP 5 chan 15



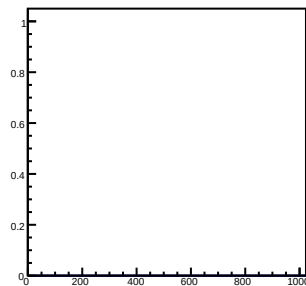
MCM 1 CHIP 6 chan 0



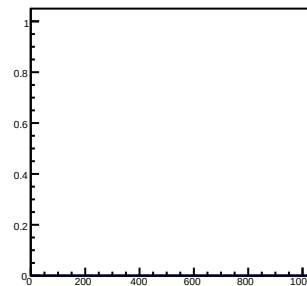
MCM 1 CHIP 6 chan 1



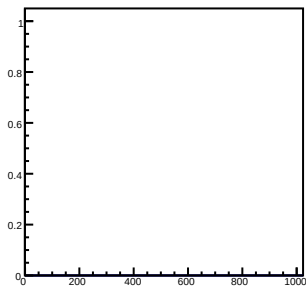
MCM 1 CHIP 6 chan 2



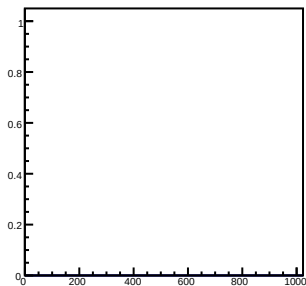
MCM 1 CHIP 6 chan 3



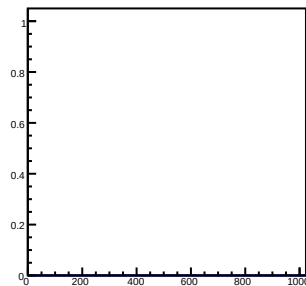
MCM 1 CHIP 6 chan 4



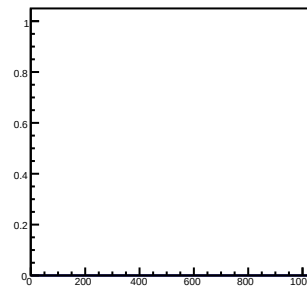
MCM 1 CHIP 6 chan 5



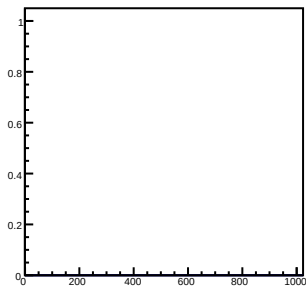
MCM 1 CHIP 6 chan 6



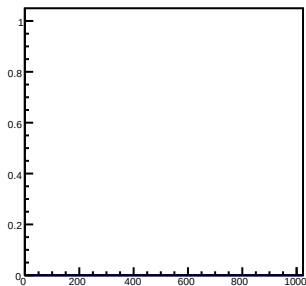
MCM 1 CHIP 6 chan 7



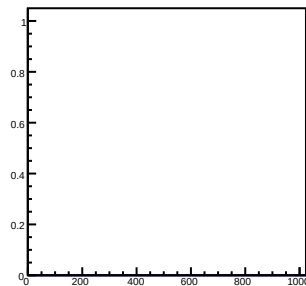
MCM 1 CHIP 6 chan 8



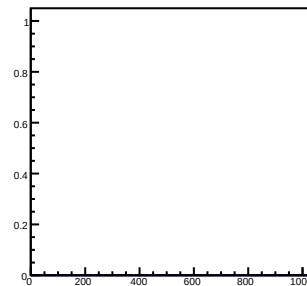
MCM 1 CHIP 6 chan 9



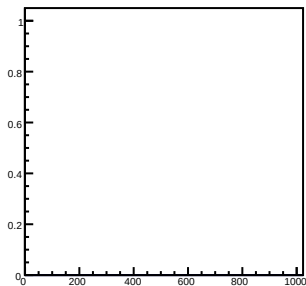
MCM 1 CHIP 6 chan 10



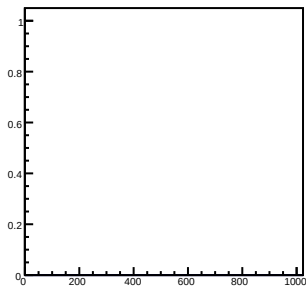
MCM 1 CHIP 6 chan 11



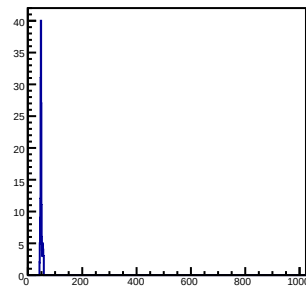
MCM 1 CHIP 6 chan 12



MCM 1 CHIP 6 chan 13



MCM 1 CHIP 6 chan 14



MCM 1 CHIP 6 chan 15

