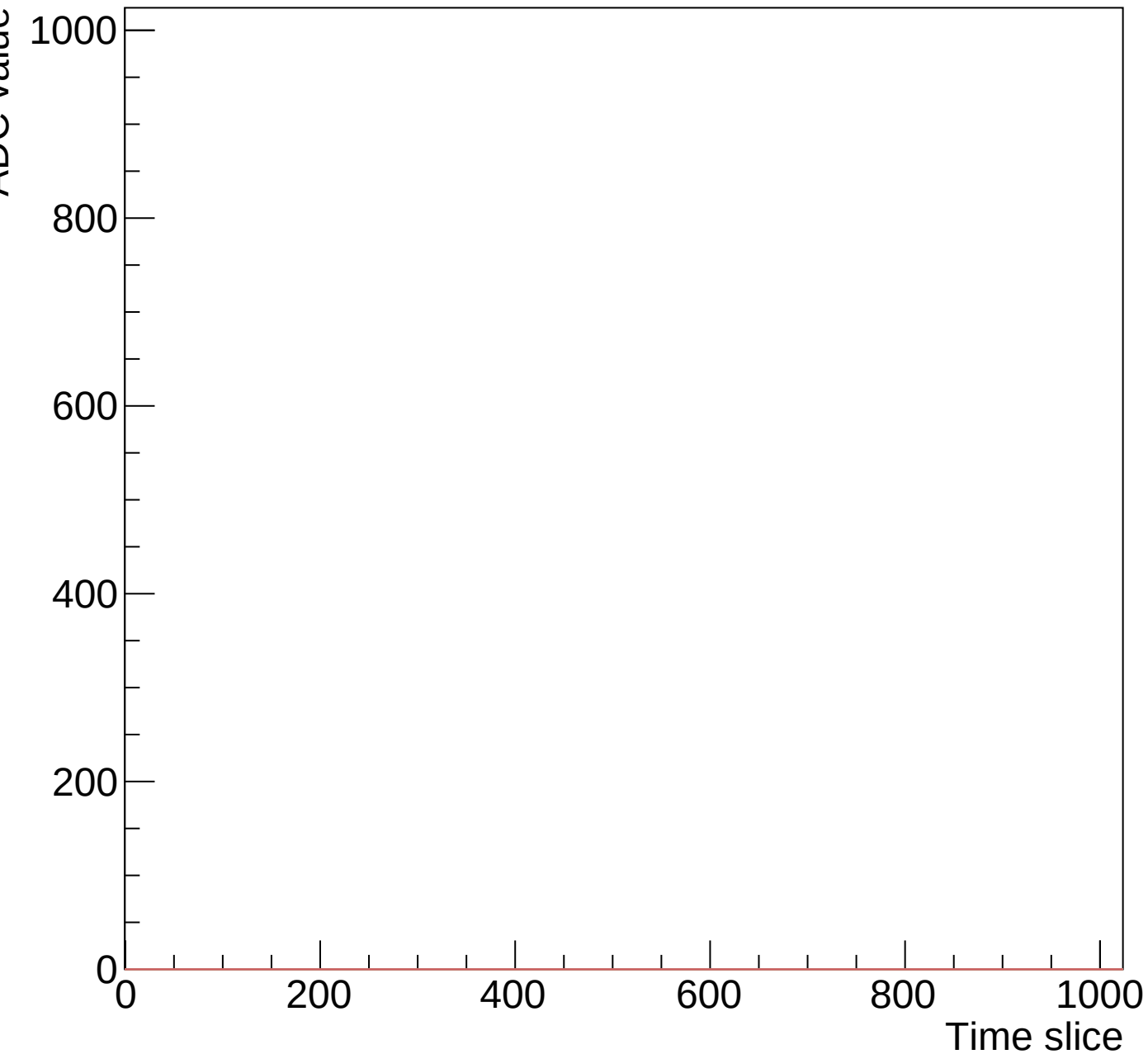
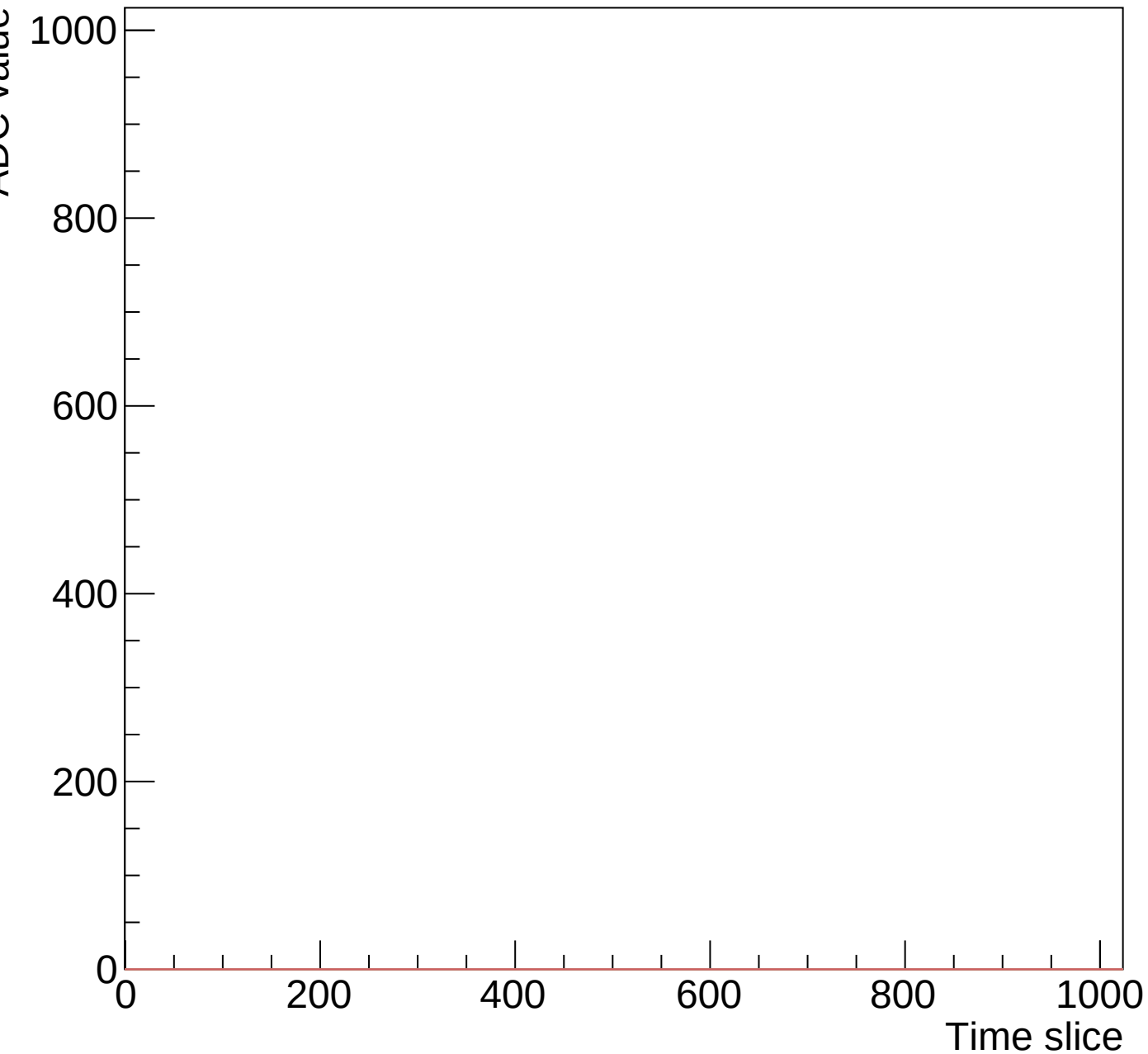


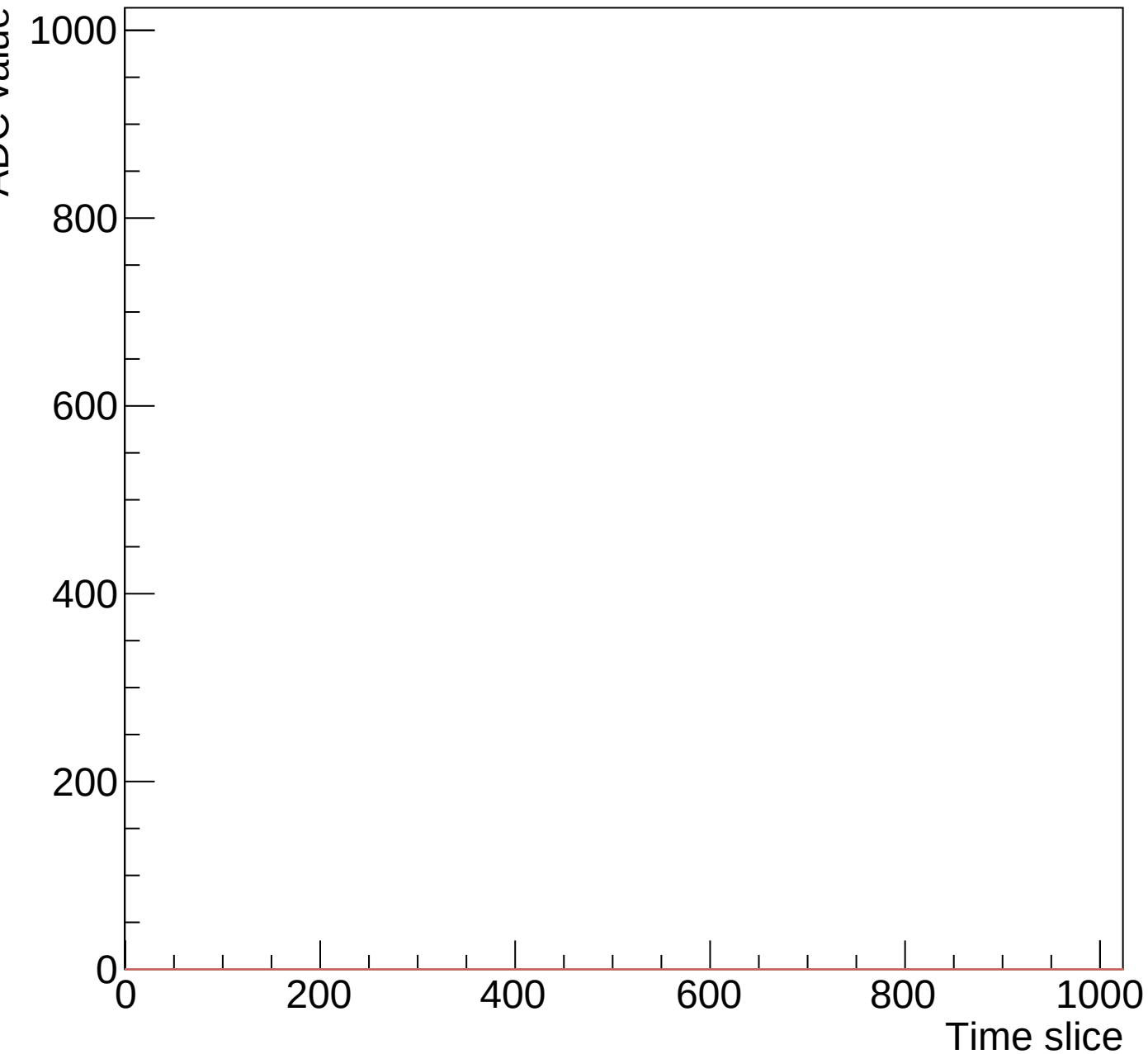
Run 0 event 0 mcm 0 chip 0 chamber-1



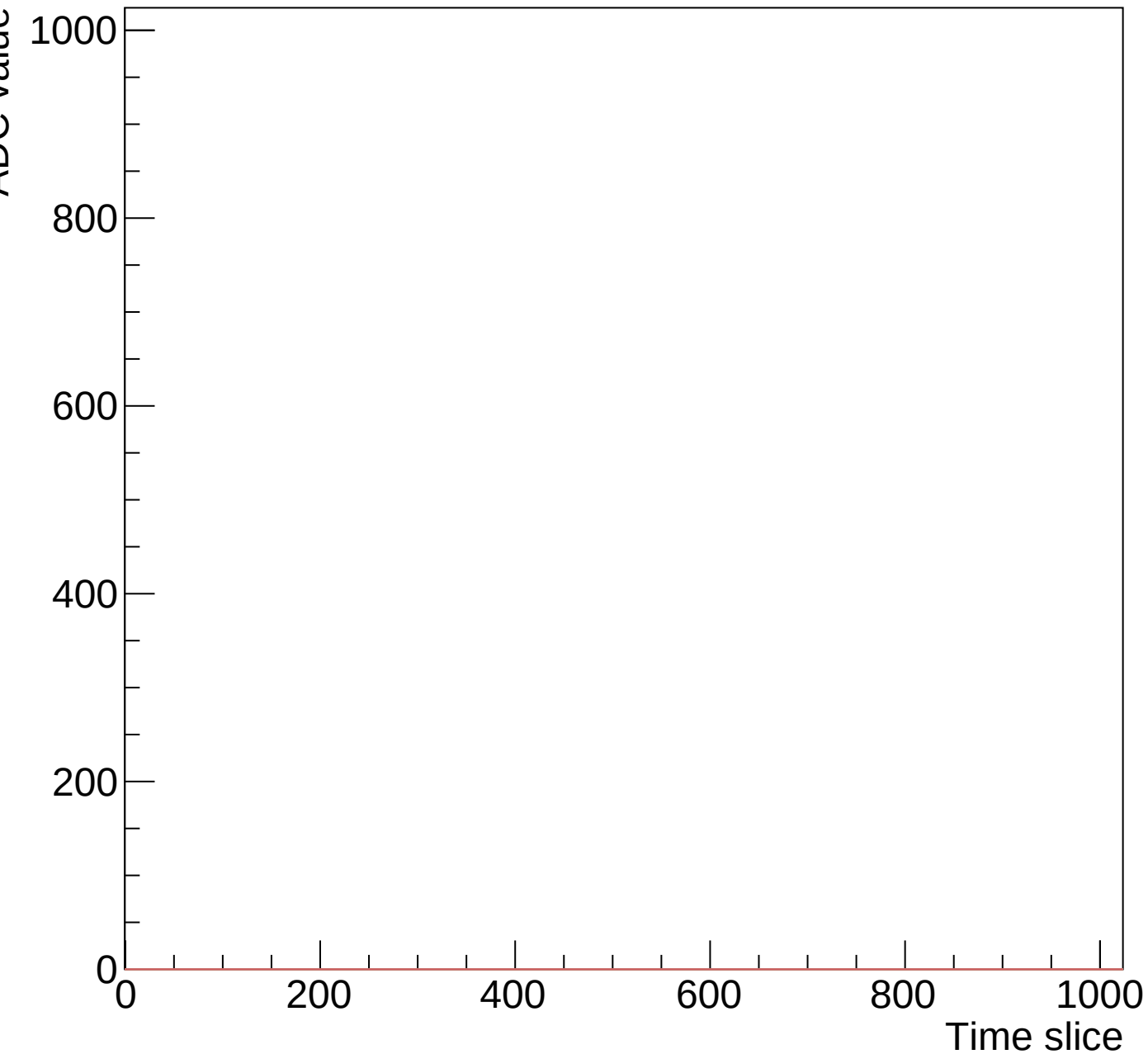
Run 0 event 0 mcm 0 chip 1 chamber-1



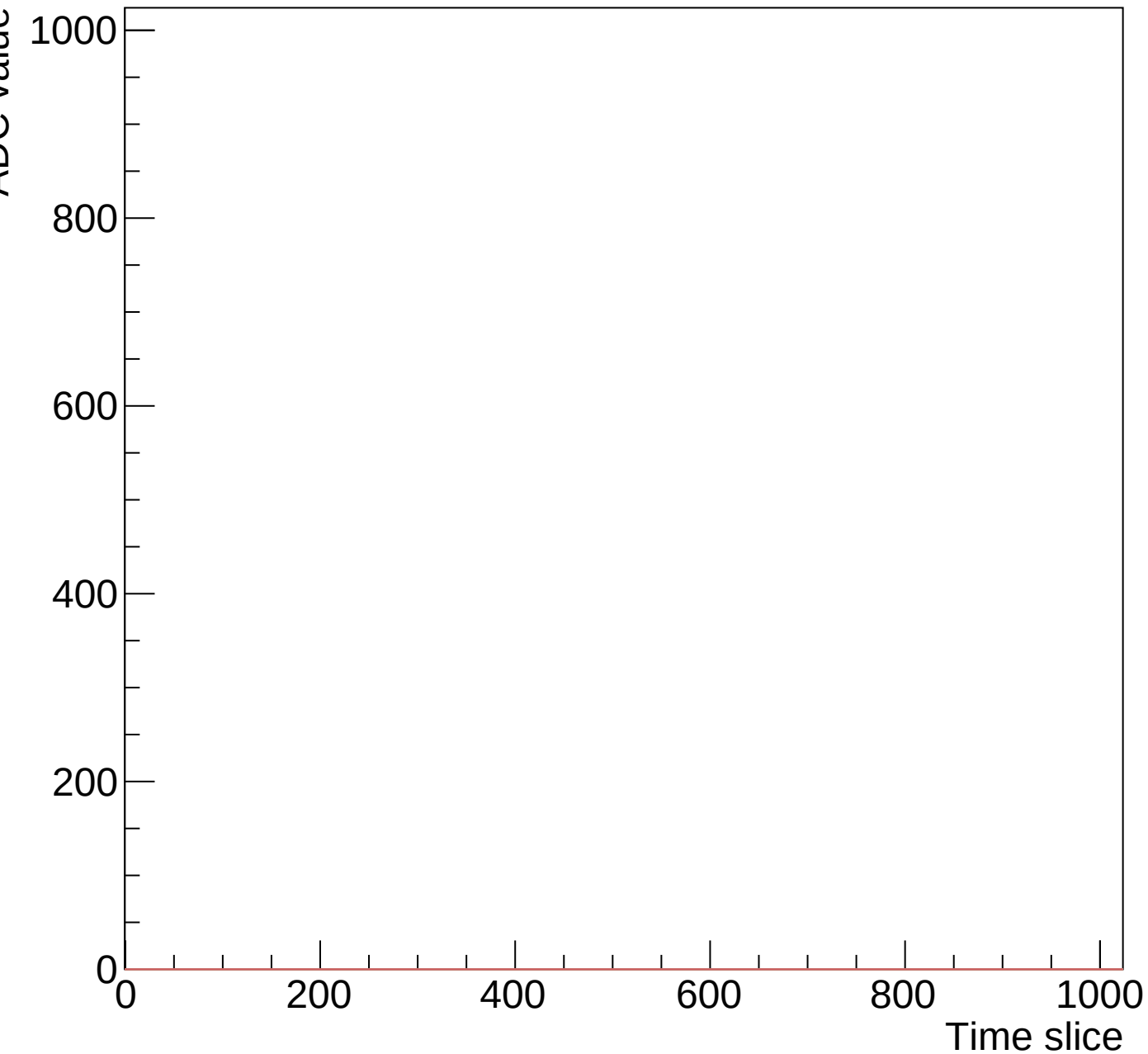
Run 0 event 0 mcm 0 chip 2 chamber-1



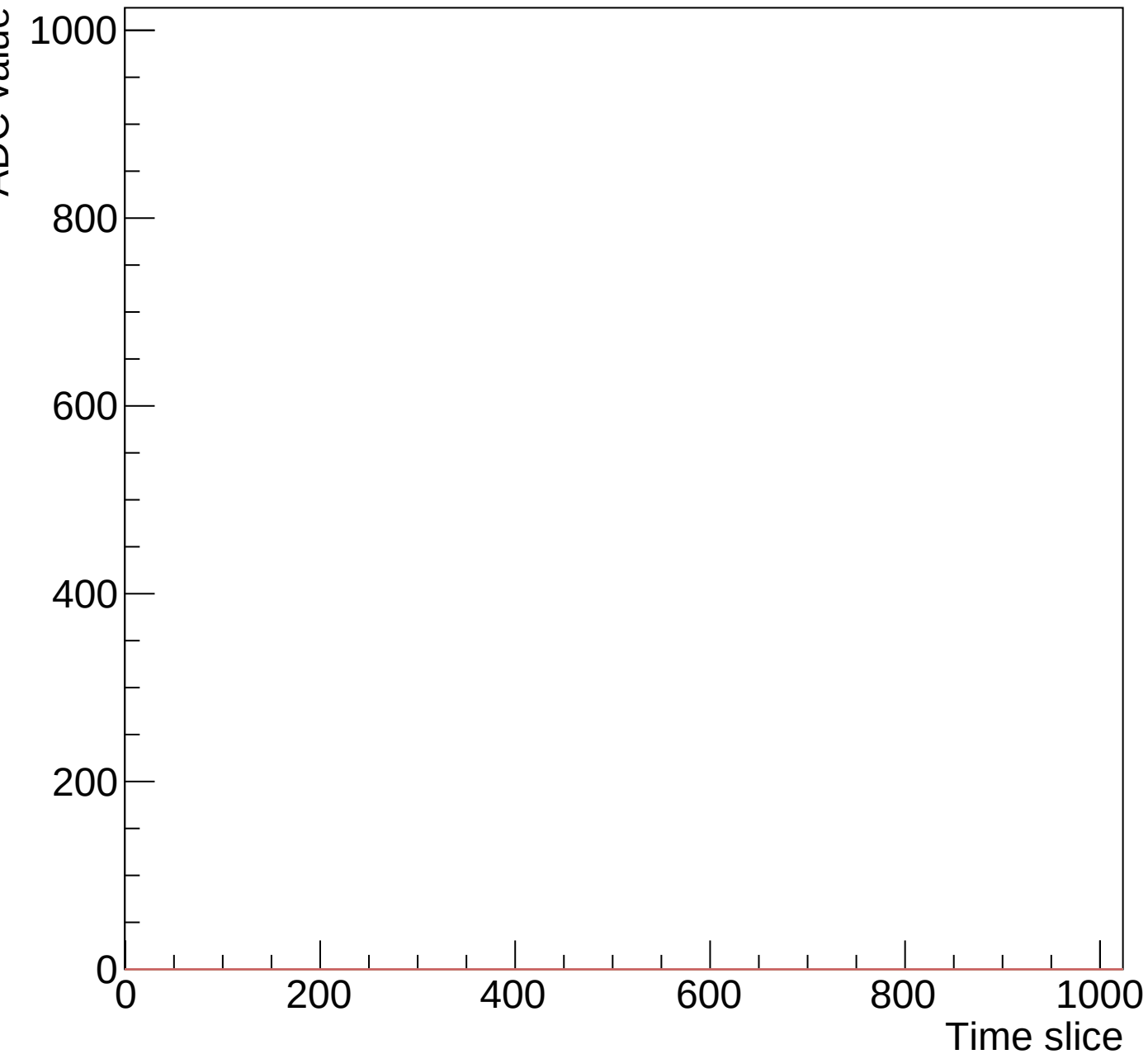
Run 0 event 0 mcm 0 chip 3 chamber-1



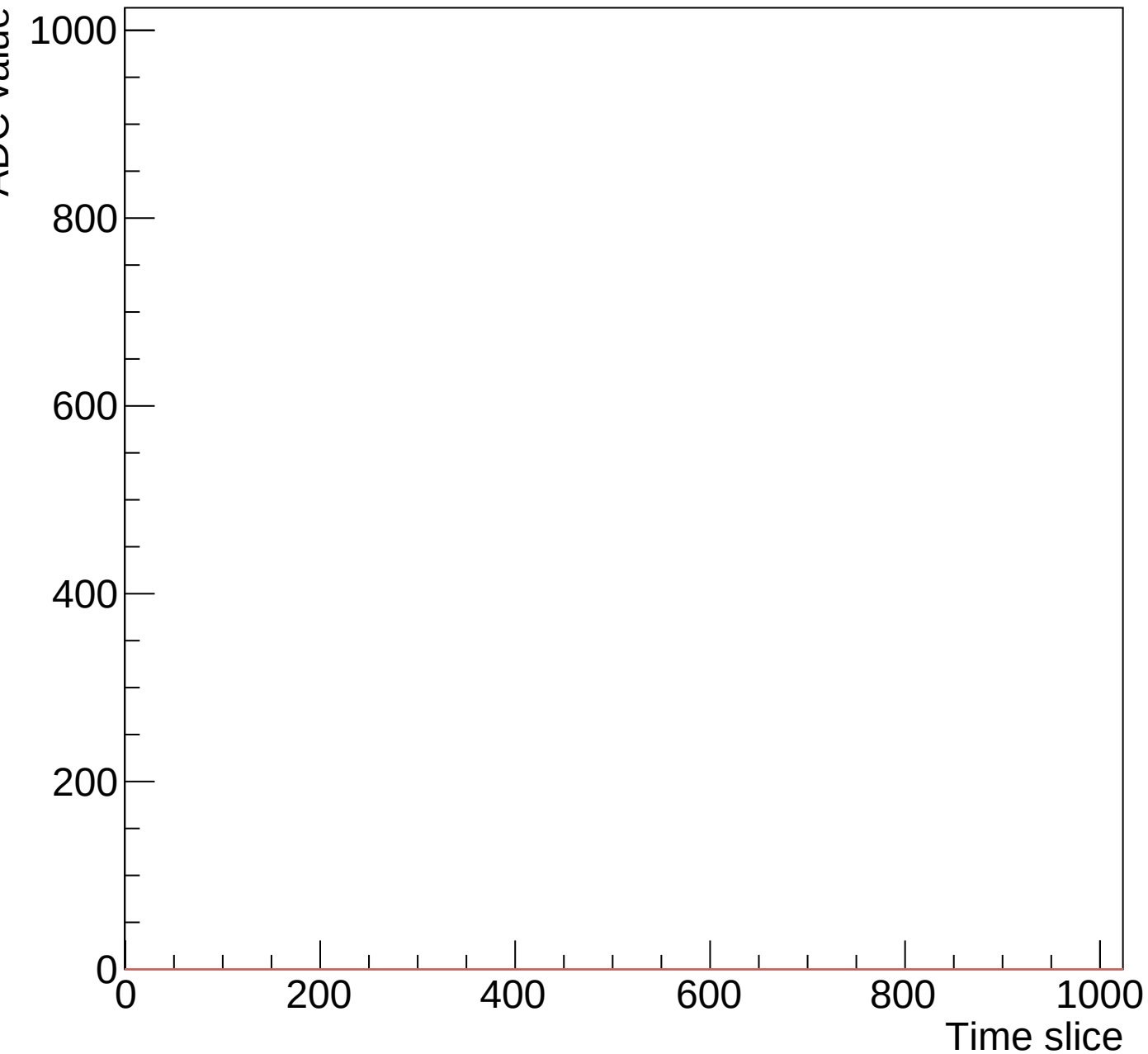
Run 0 event 0 mcm 0 chip 4 chamber-1



Run 0 event 0 mcm 0 chip 5 chamber-1



Run 0 event 0 mcm 0 chip 6 chamber-1



Run 0 event 0 mcm 0 chip 7 chamber-1

