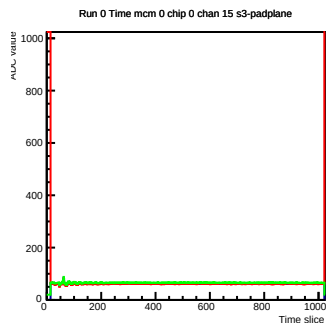
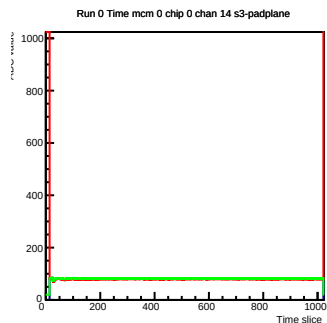
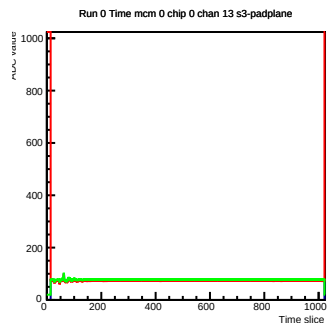
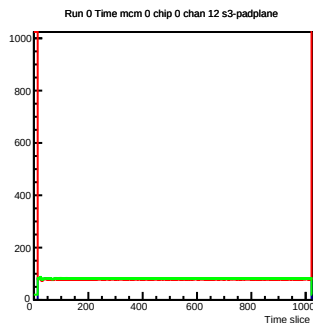
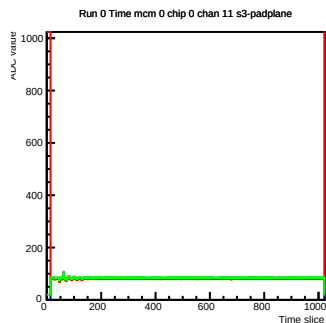
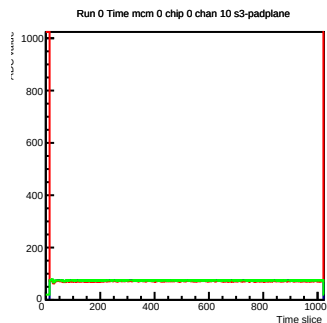
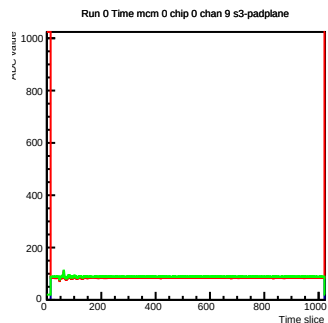
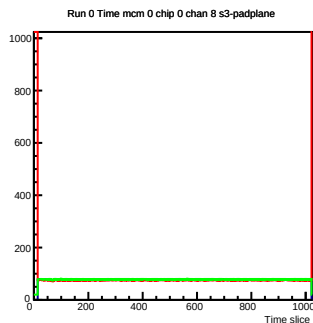
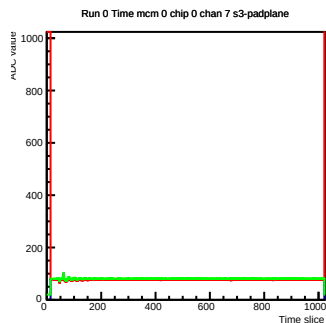
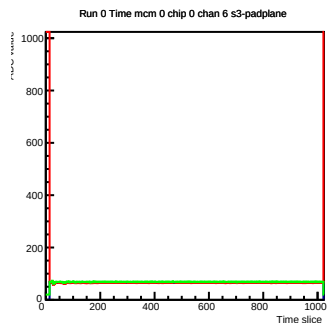
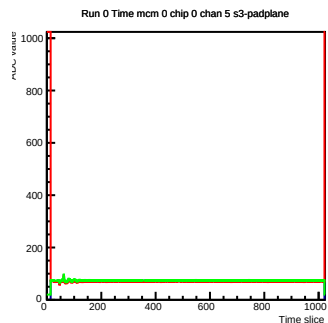
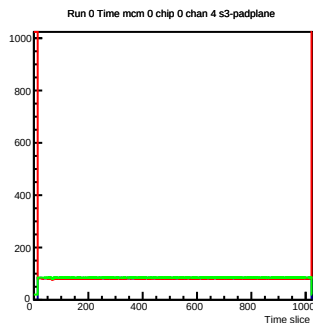
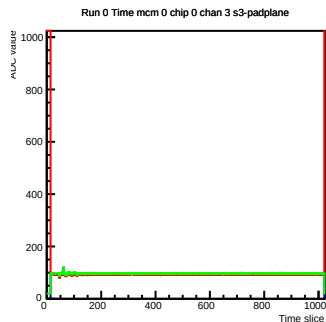
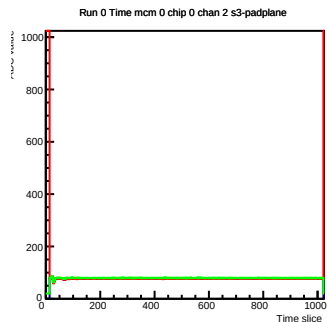
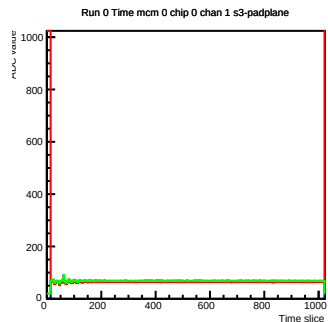
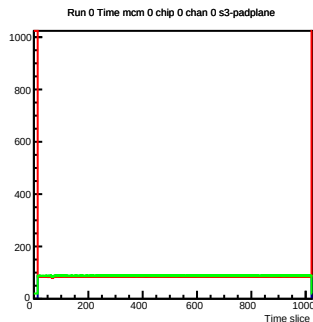
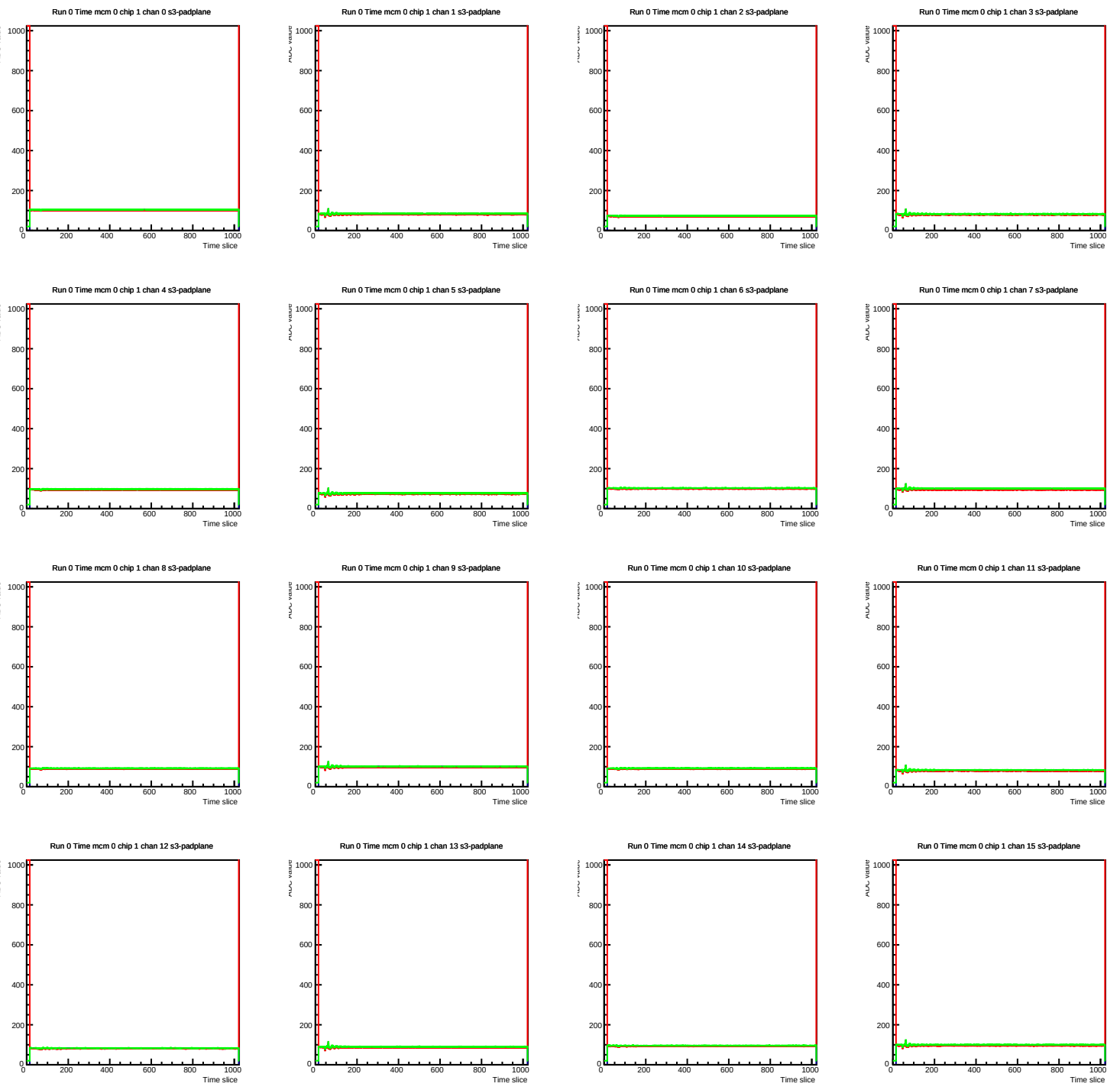
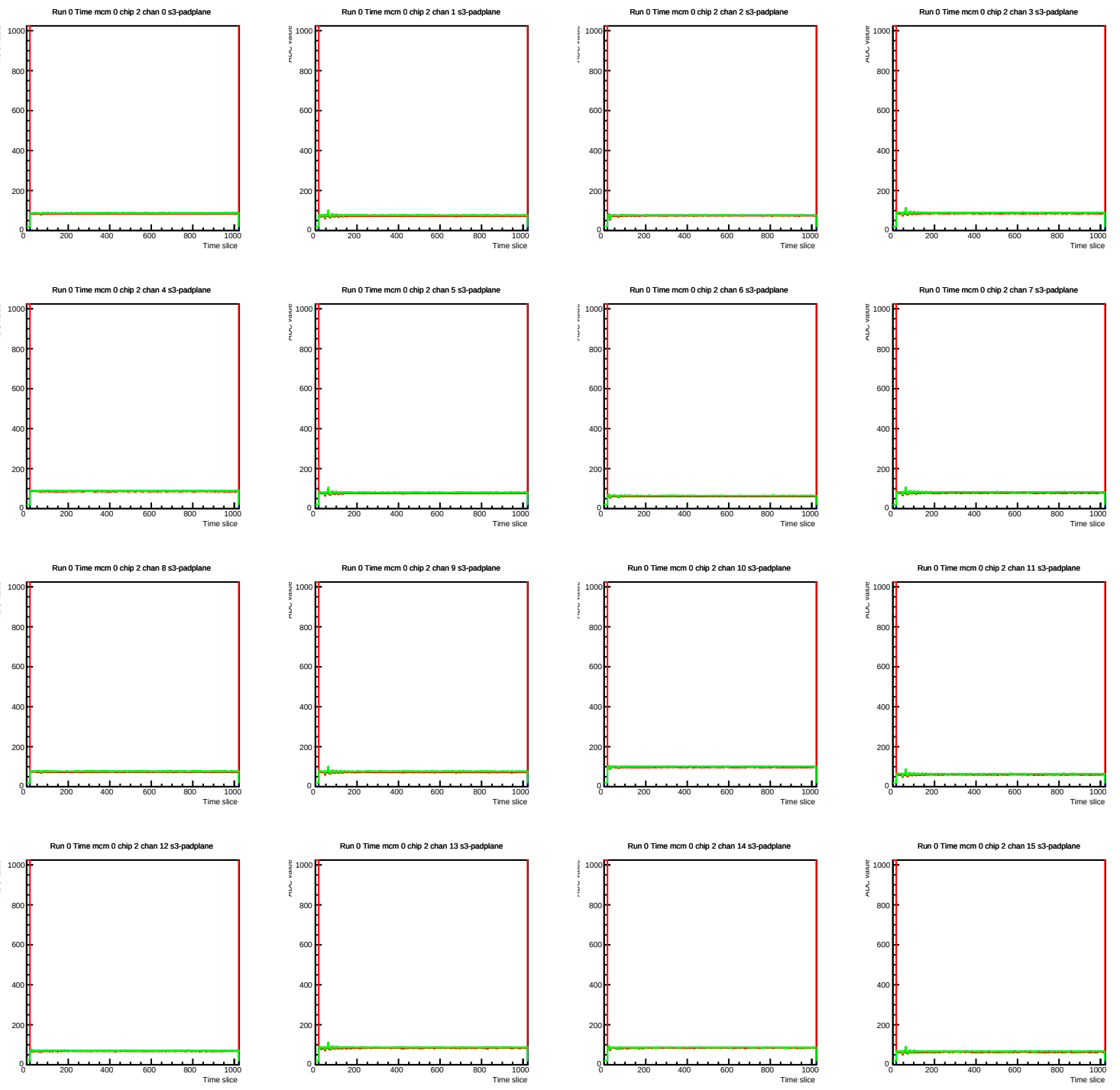
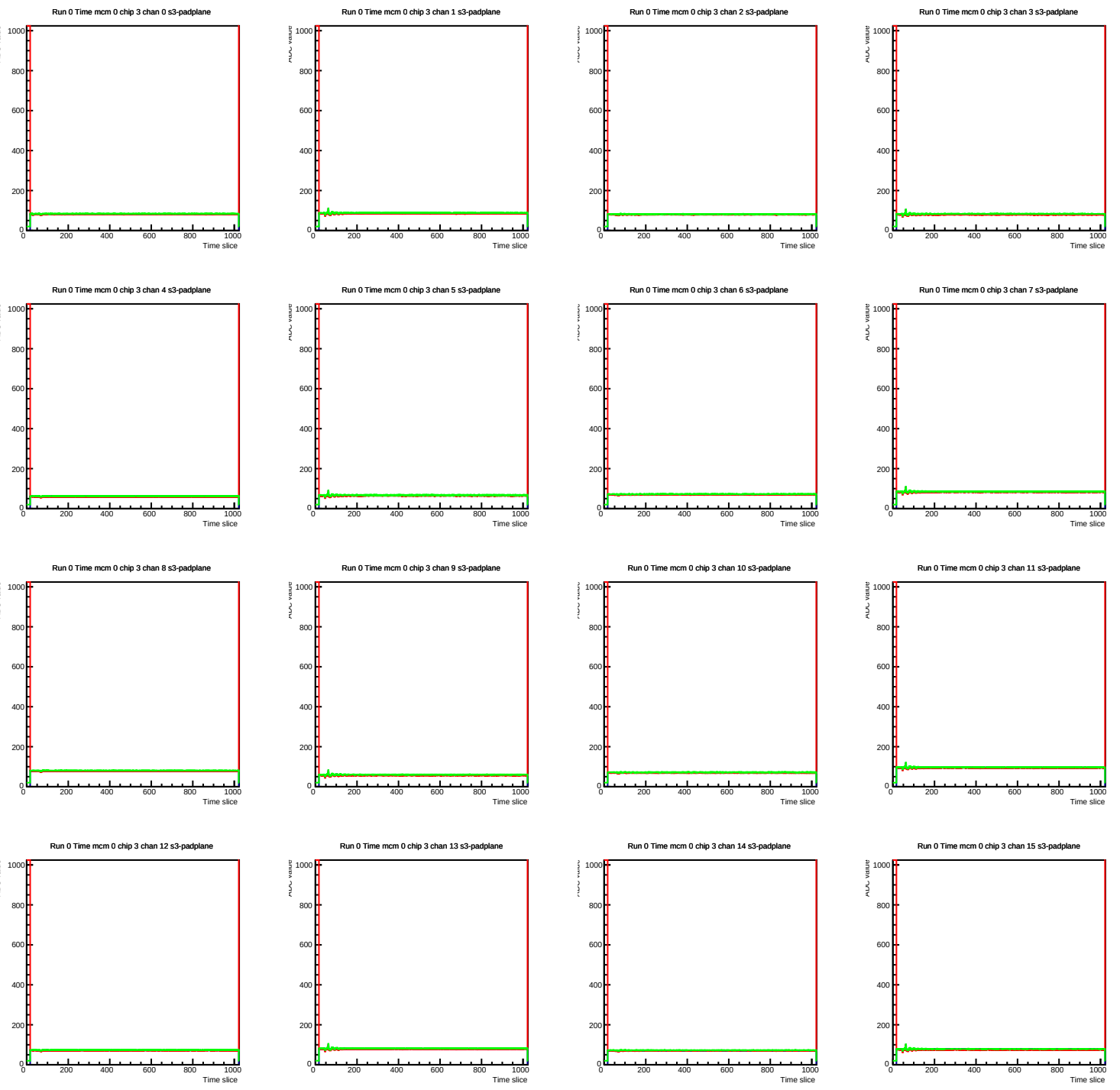


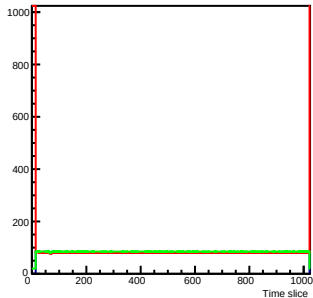




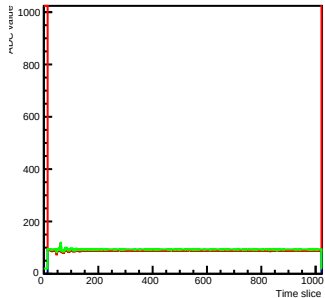




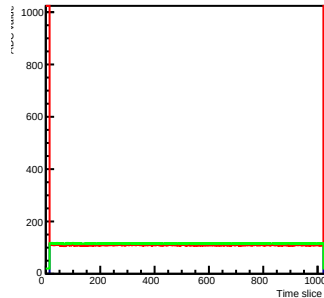
Run 0 Time mcm 0 chip 4 chan 0 s3-padplane



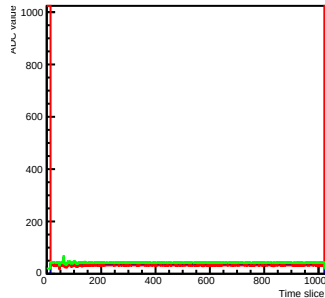
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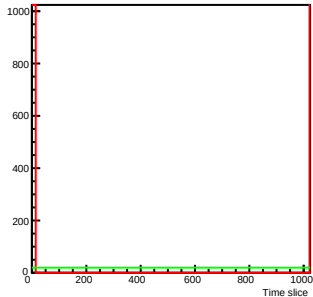
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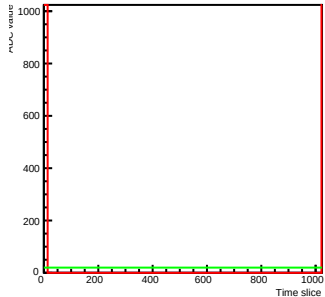
Run 0 Time mcm 0 chip 4 chan 3 s3-padplane



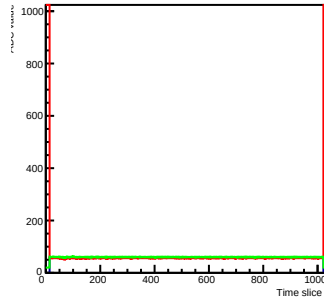
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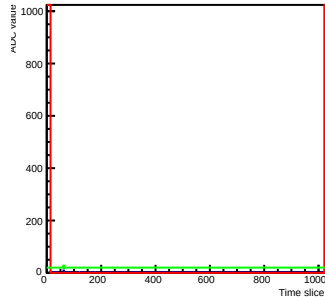
Run 0 Time mcm 0 chip 4 chan 5 s3-padplane



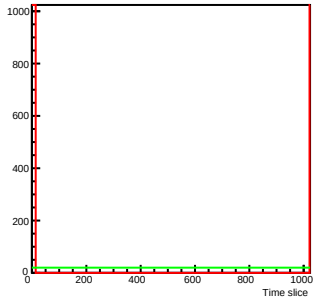
Run 0 Time mcm 0 chip 4 chan 6 s3-padplane



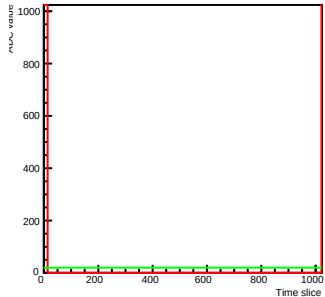
Run 0 Time mcm 0 chip 4 chan 7 s3-padplane



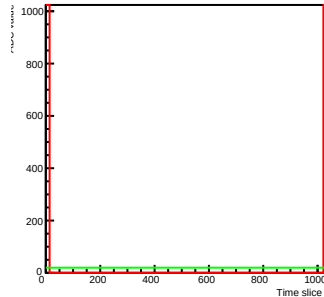
Run 0 Time mcm 0 chip 4 chan 8 s3-padplane



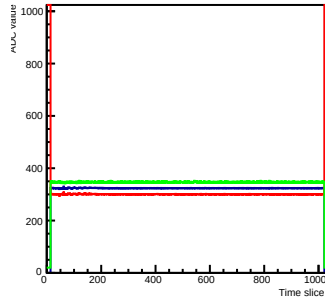
Run 0 Time mcm 0 chip 4 chan 9 s3-padplane



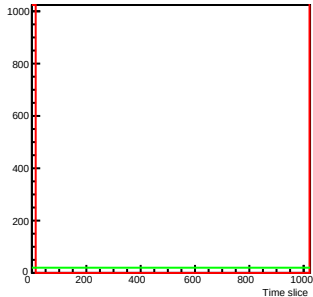
Run 0 Time mcm 0 chip 4 chan 10 s3-padplane



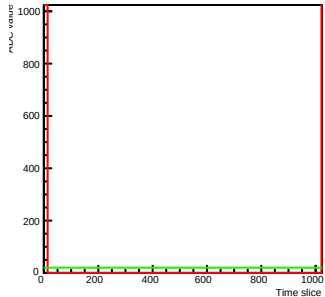
Run 0 Time mcm 0 chip 4 chan 11 s3-padplane



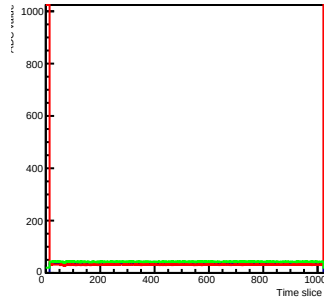
Run 0 Time mcm 0 chip 4 chan 12 s3-padplane



Run 0 Time mcm 0 chip 4 chan 13 s3-padplane



Run 0 Time mcm 0 chip 4 chan 14 s3-padplane



Run 0 Time mcm 0 chip 4 chan 15 s3-padplane

