

Här är listan på alla in- och utsignaler till SRUn:

SPARE (TRIG No)	input	AD24/AE24	Event number	LVDS	COAX_FPGA_IO_p /n<3>	PX18
CLK	input	U23/V23	DTC clock	LVDS	COAX_FPGA_IO_p /n<2>	PX17
BUSY	output	T24/T23	BUSY/VETO	LVDS	COAX_FPGA_IO_p /n<0>	PX15
TRIG	input	AD22	Trigger	NIM	NIM_TO_TTL<3>	PX14
CONT	input	AC22	SPILL	NIM	NIM_TO_TTL<2>	PX13
TRIG_Out	Output		Trigger (Synced and resolved)	NIM	TTL_TO_NIM<3>	PX10
Test_Pulse	Output	AC24	TestPulse, created by trigger engine	NIM	TTL_TO_NIM<1>	PX8
BUSY (copy)	output	AC23	BUSY/VETO	NIM	TTL_TO_NIM<0>	PX7