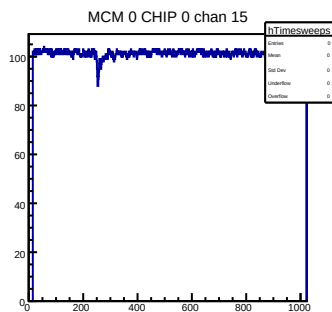
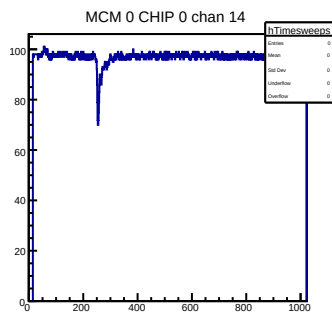
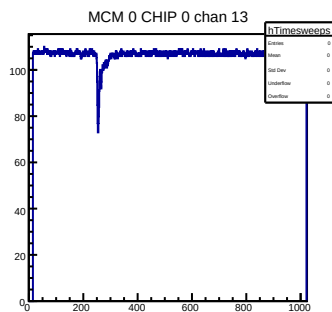
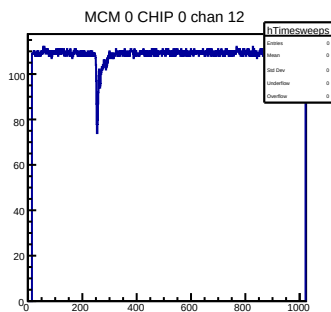
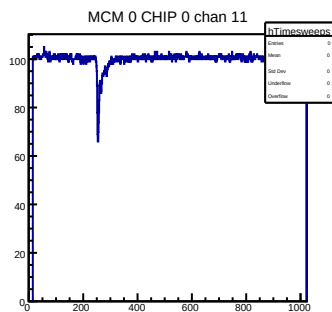
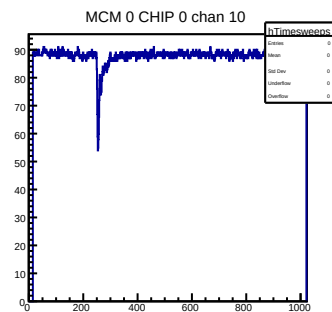
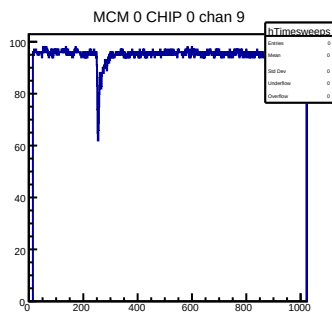
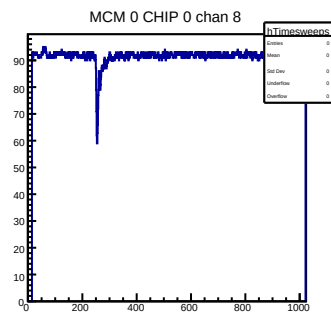
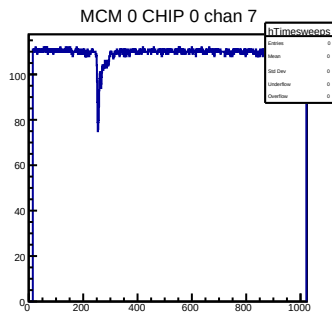
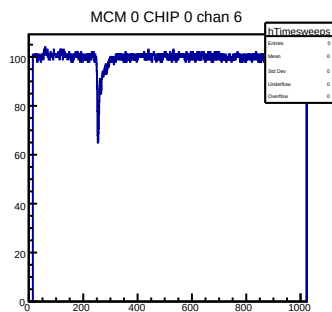
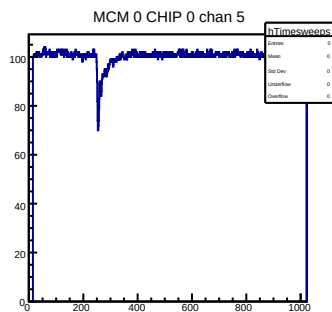
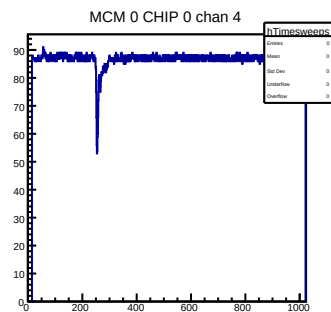
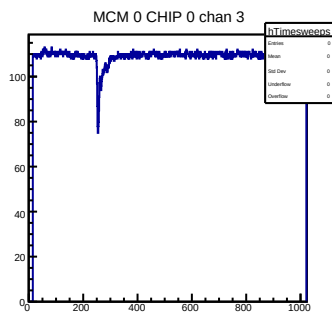
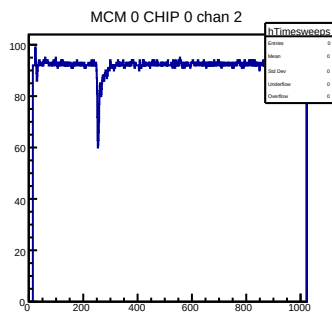
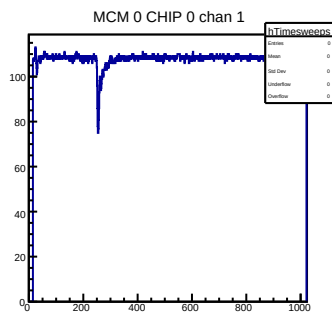
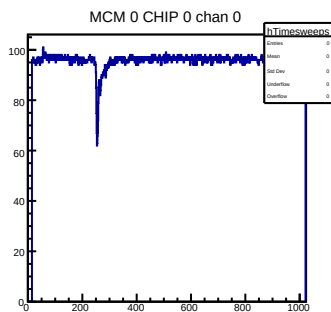
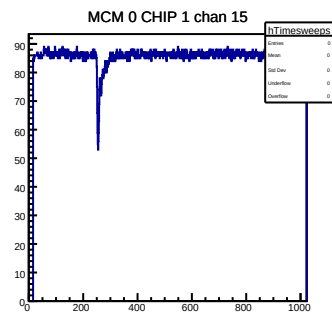
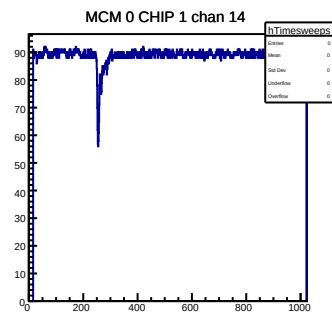
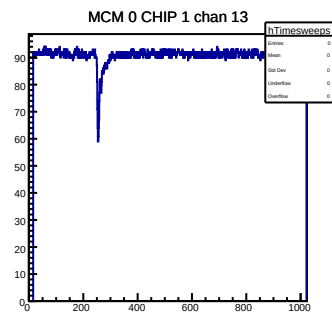
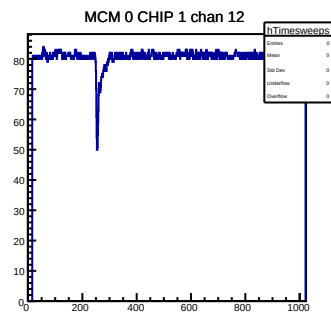
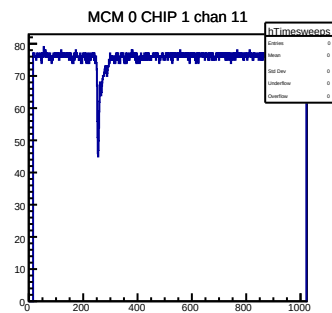
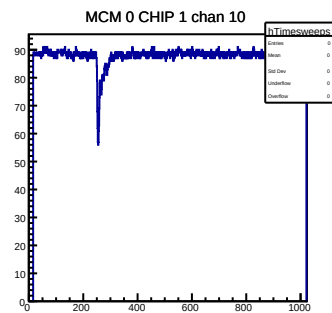
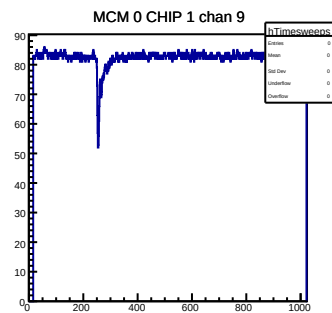
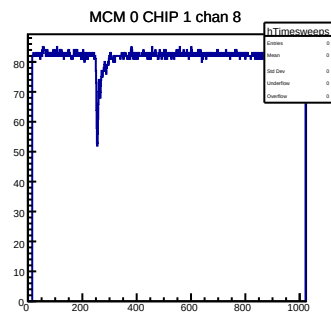
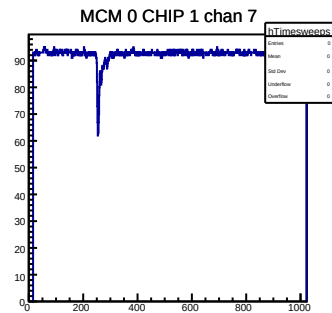
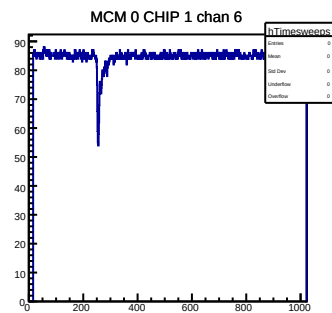
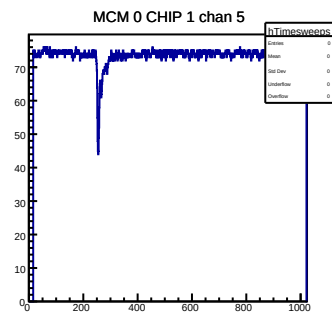
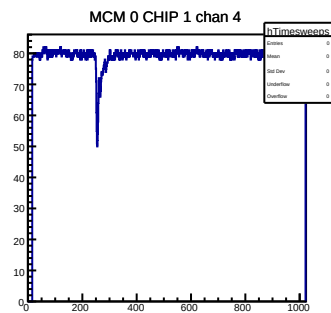
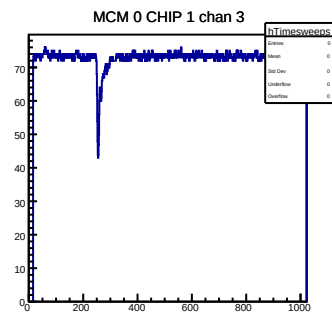
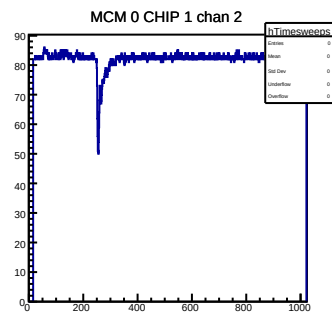
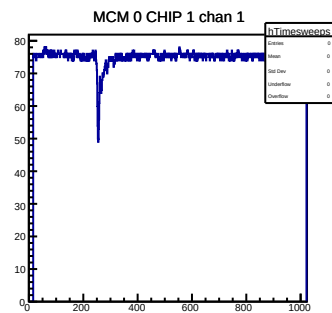
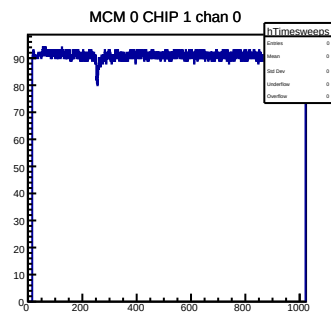
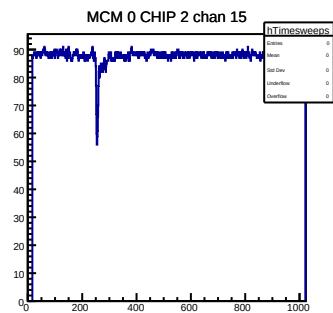
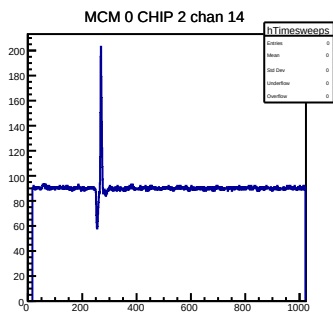
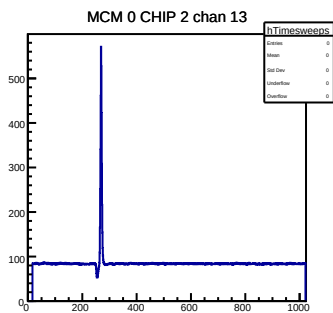
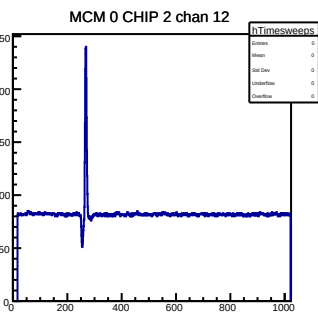
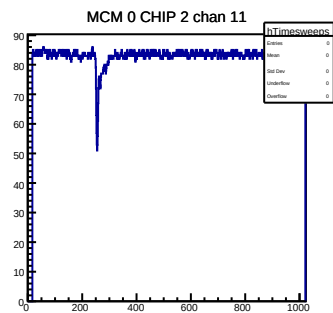
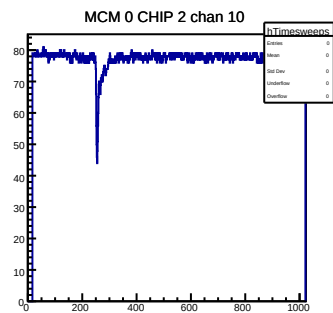
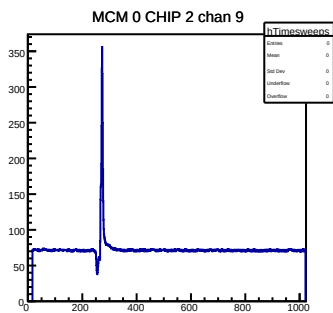
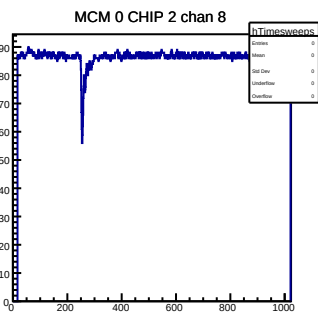
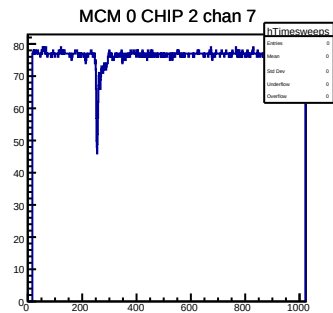
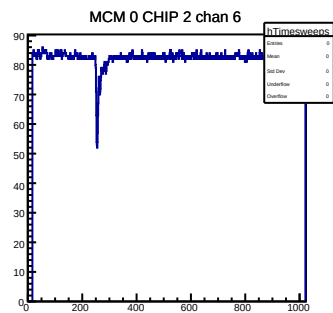
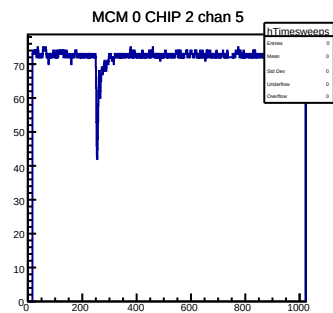
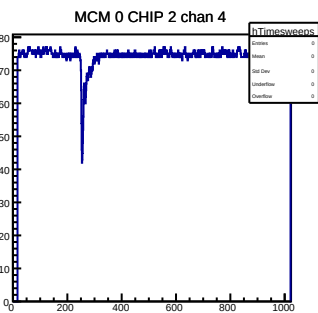
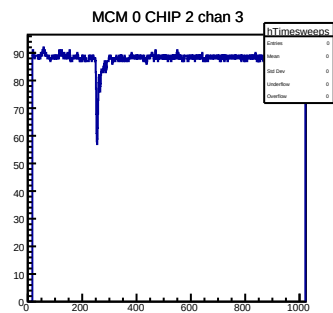
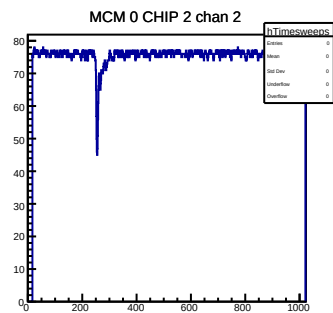
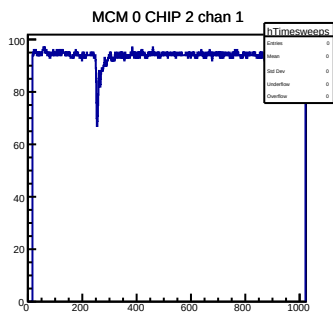
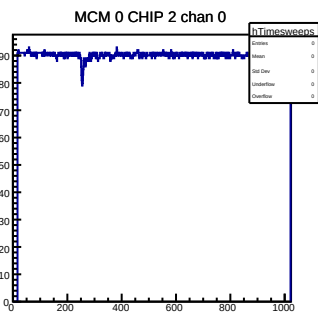
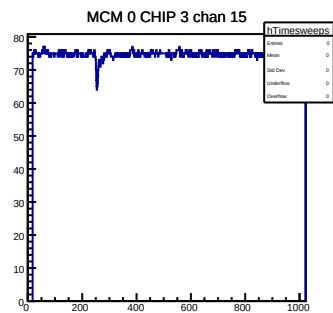
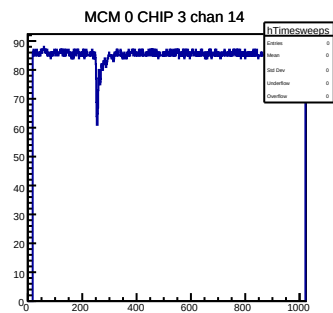
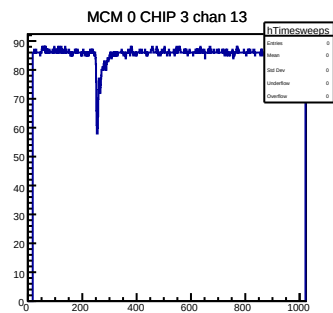
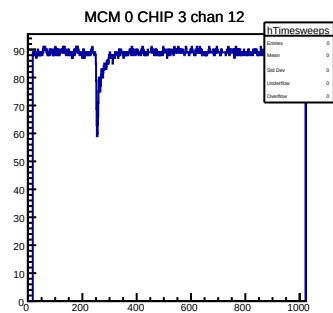
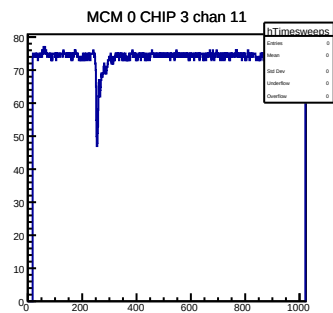
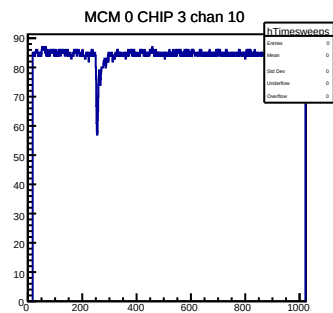
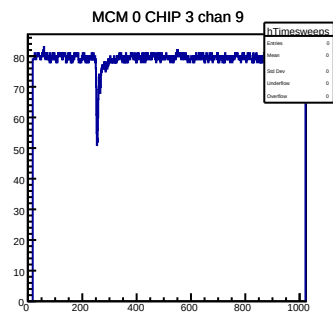
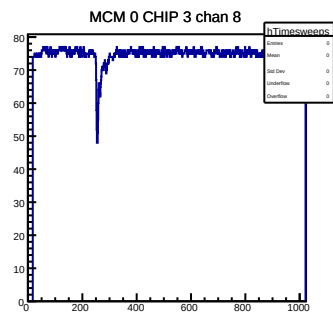
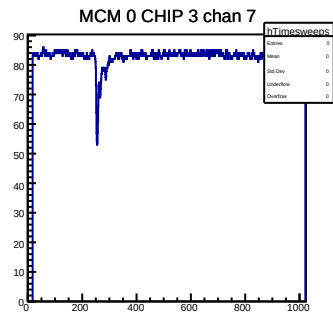
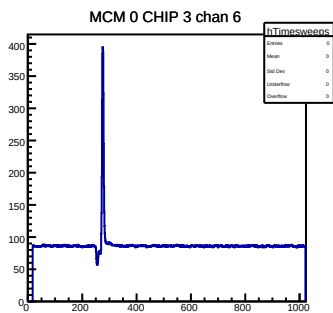
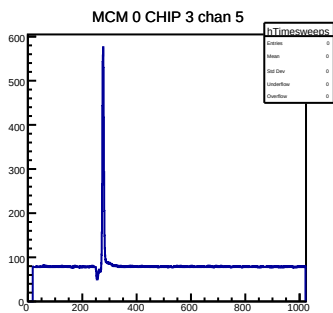
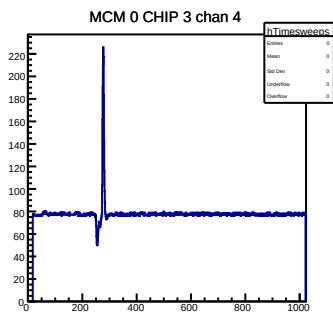
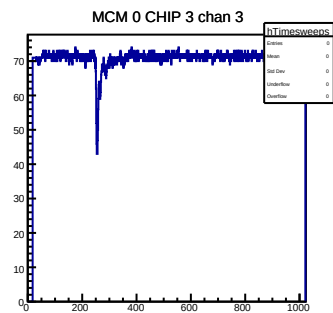
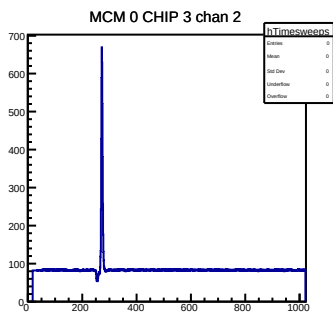
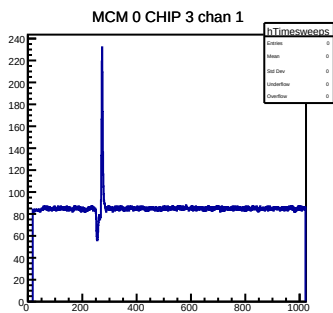
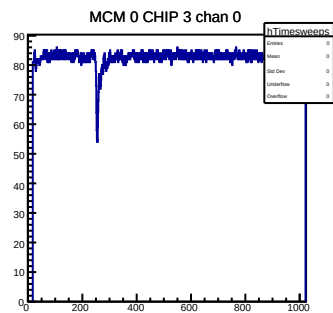


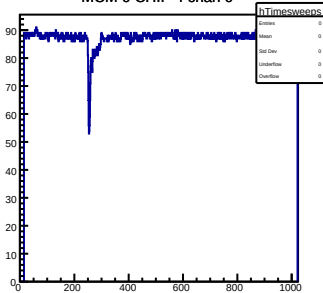




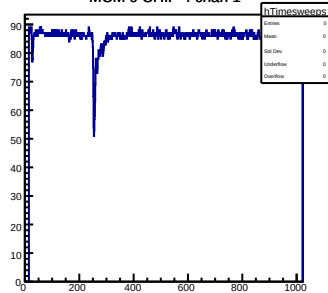




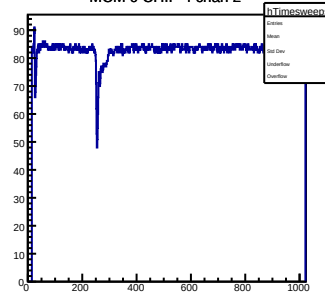
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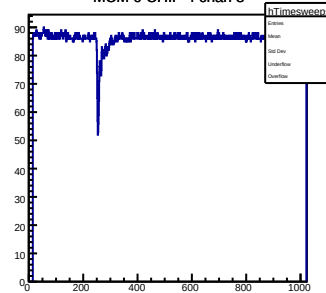
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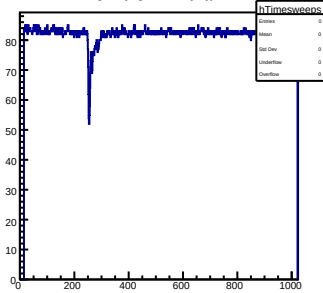
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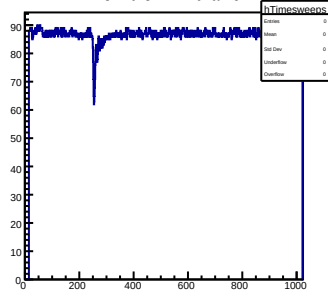
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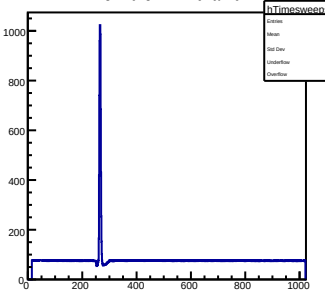
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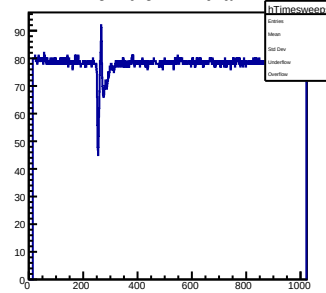
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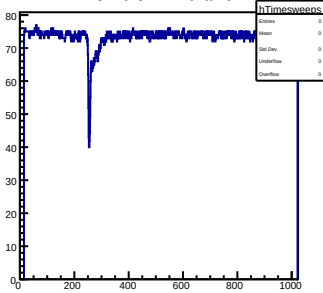
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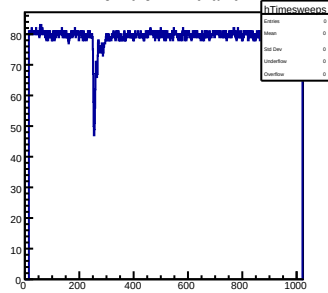
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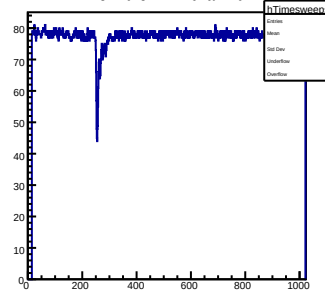
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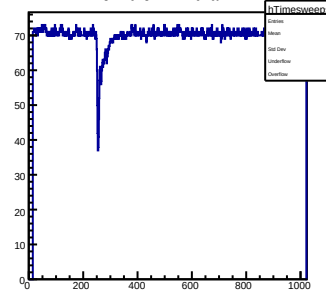
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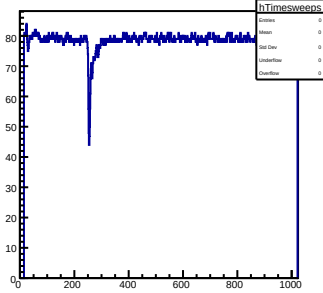
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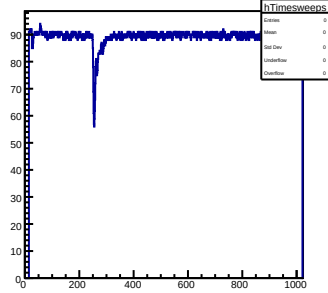
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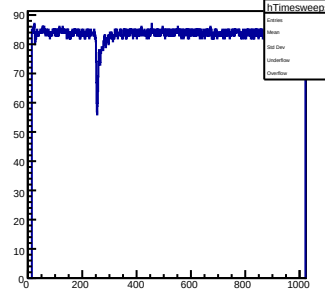
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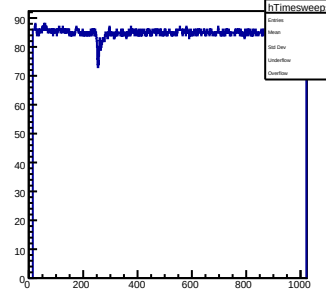
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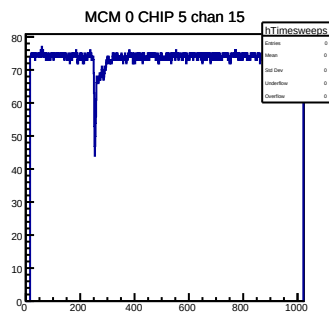
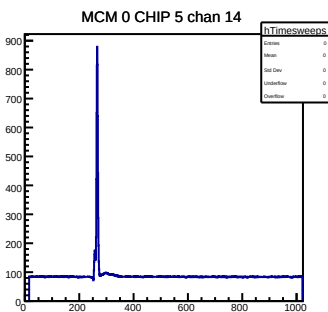
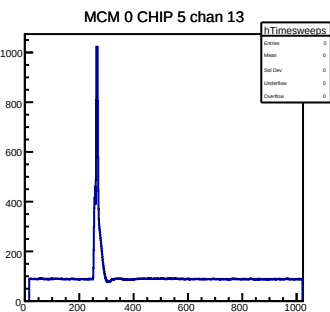
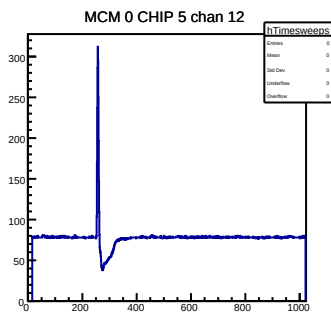
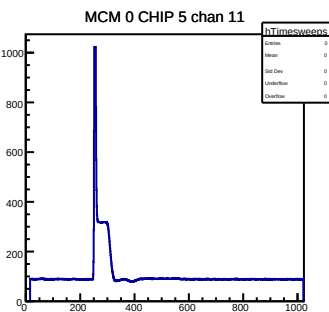
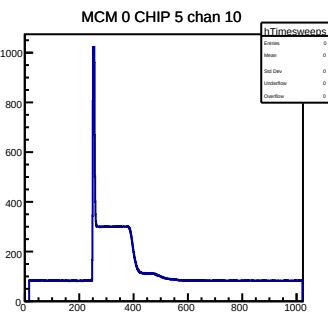
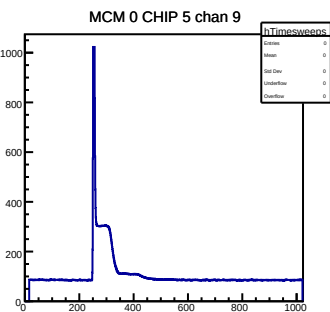
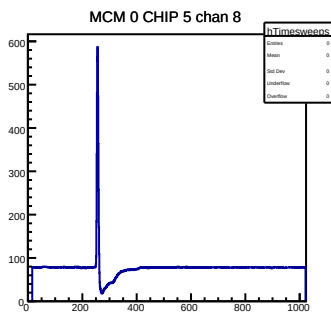
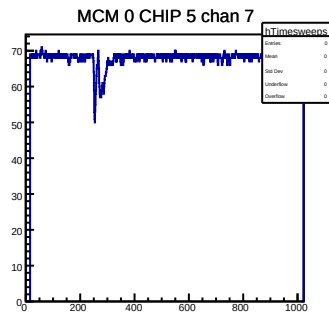
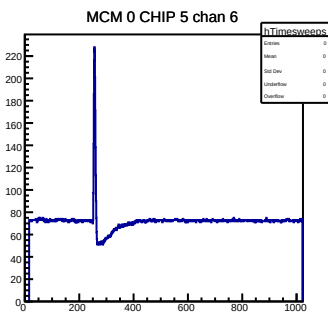
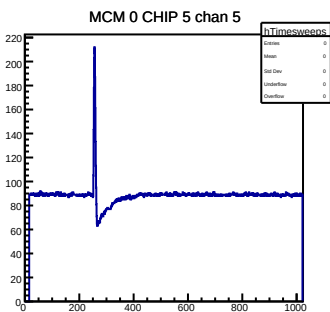
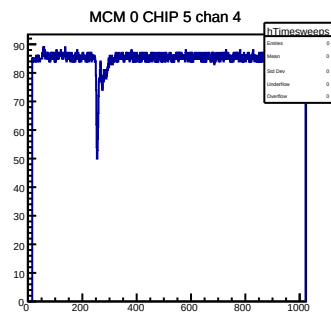
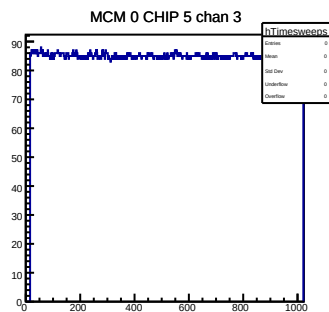
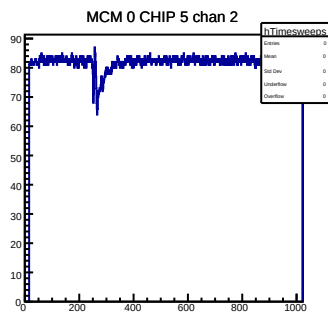
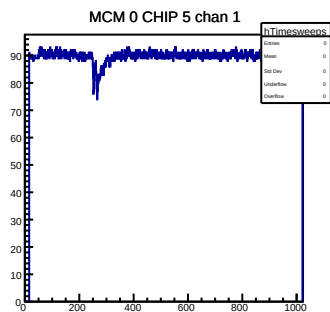
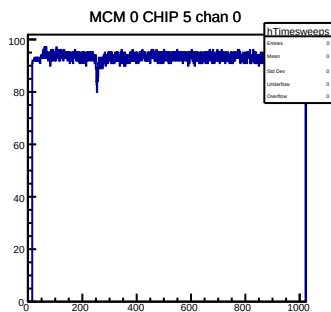


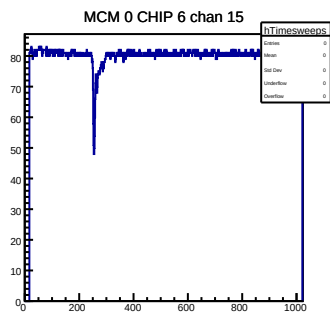
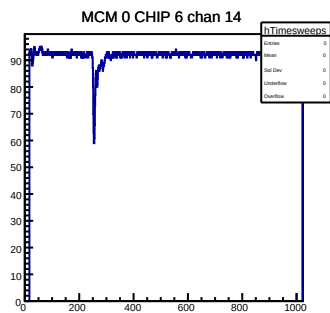
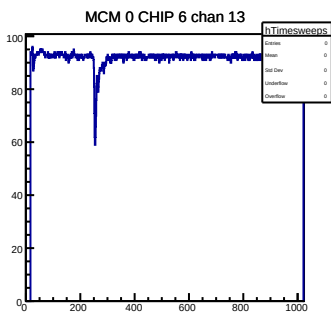
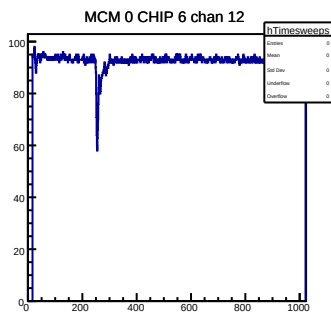
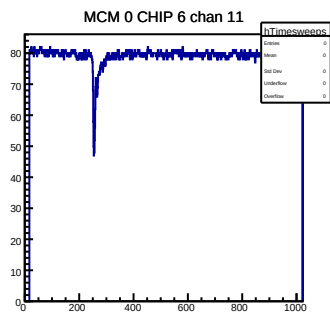
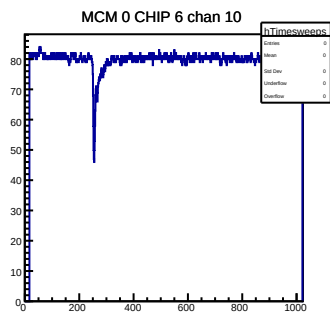
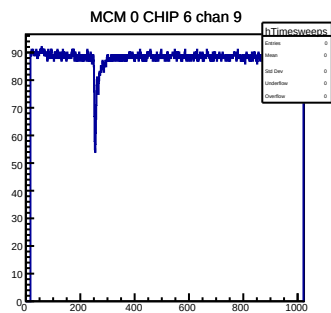
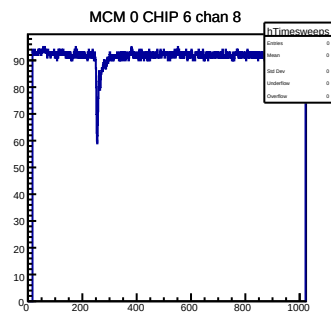
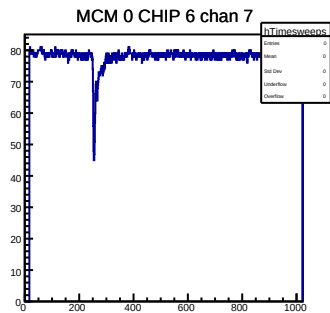
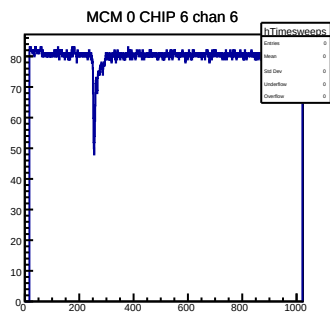
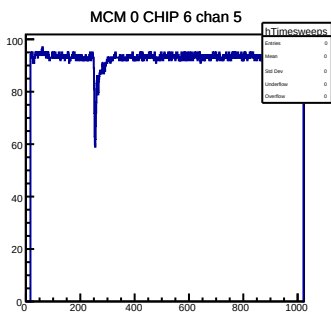
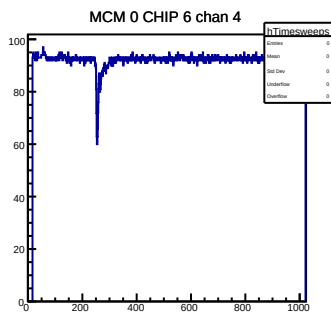
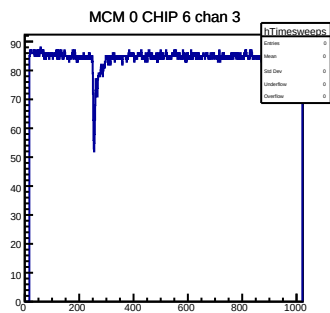
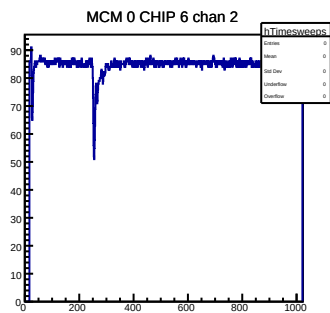
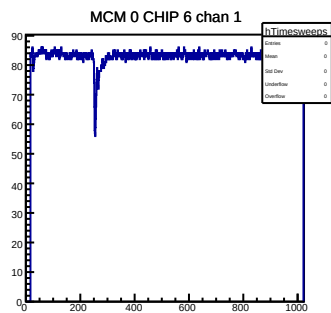
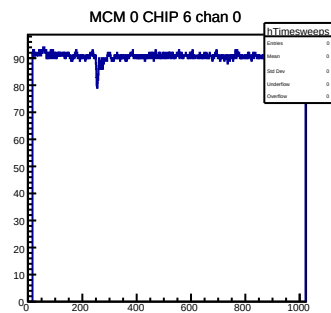
MCM 0 CHIP 4 chan 14

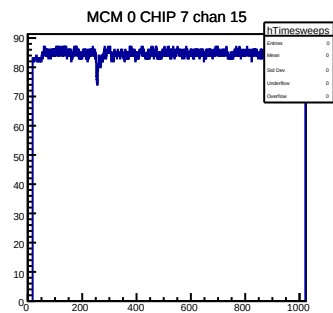
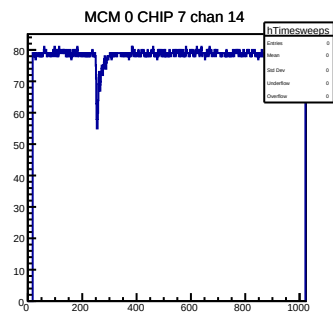
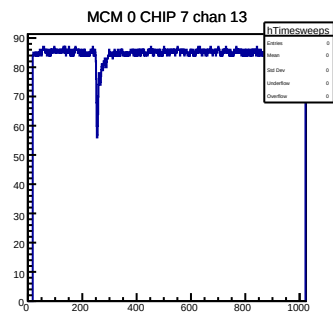
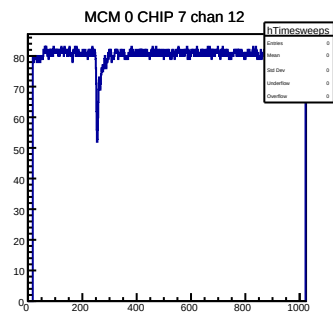
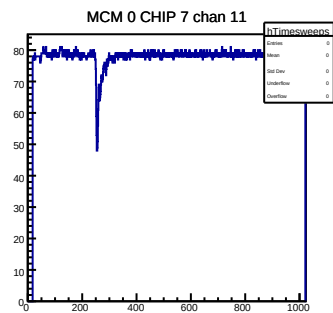
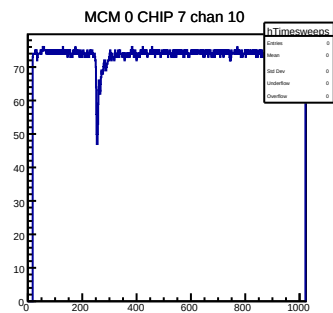
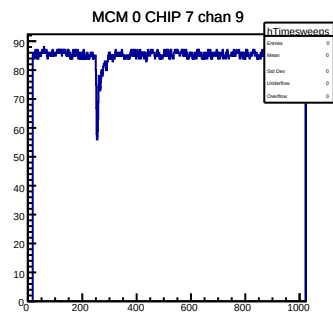
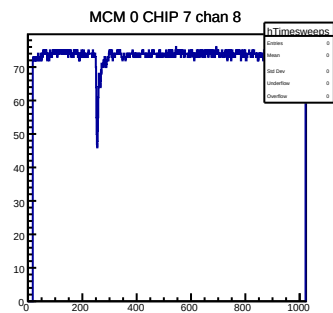
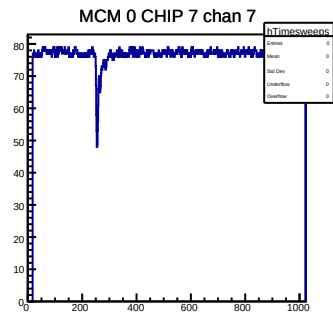
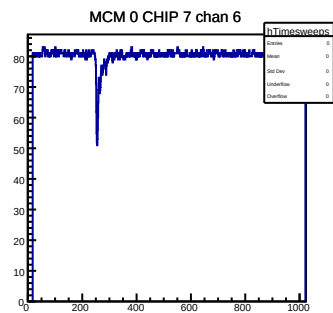
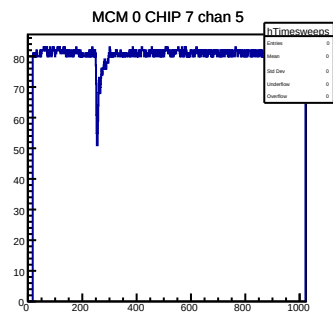
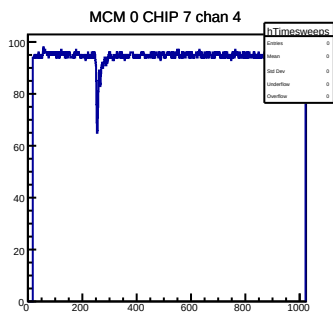
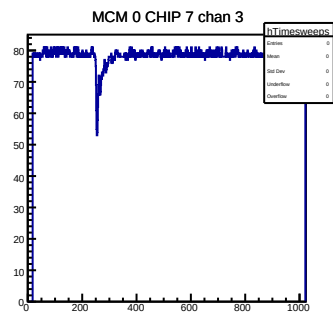
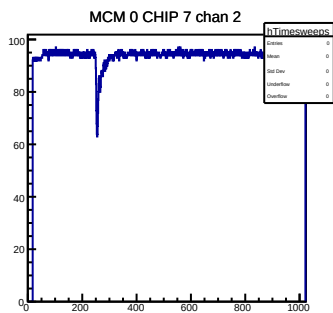
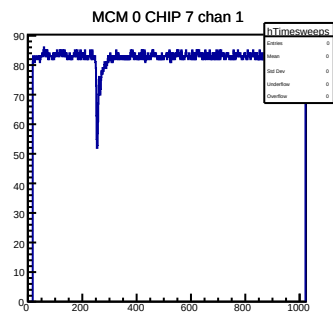
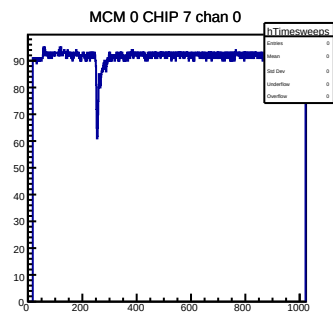


MCM 0 CHIP 4 chan 15

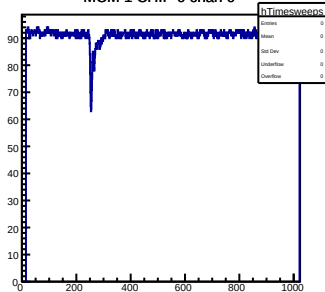




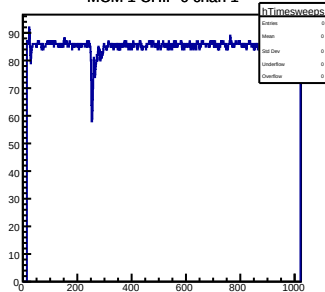




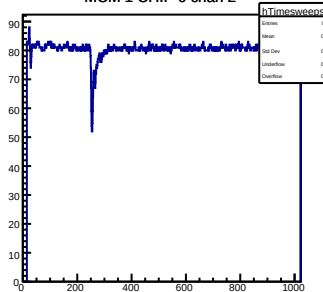
MCM 1 CHIP 0 chan 0



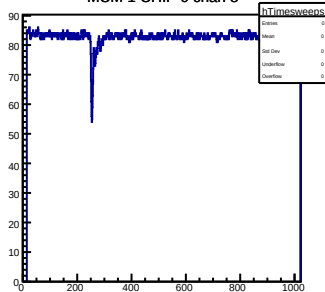
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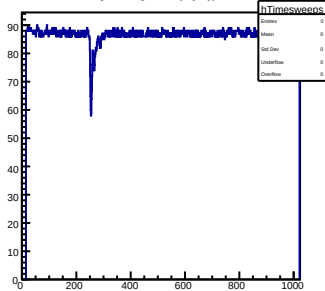
MCM 1 CHIP 0 chan 2



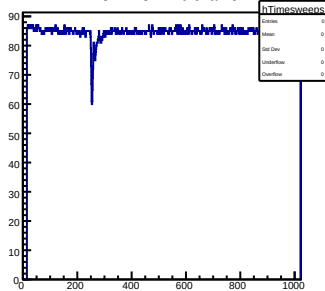
MCM 1 CHIP 0 chan 3



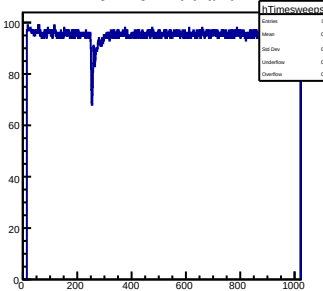
MCM 1 CHIP 0 chan 4



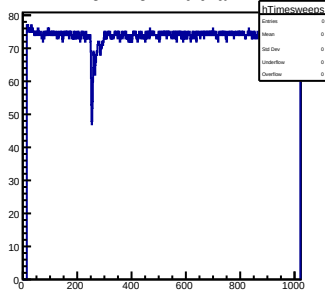
MCM 1 CHIP 0 chan 5



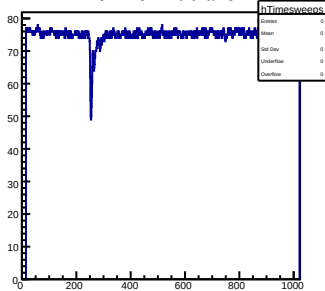
MCM 1 CHIP 0 chan 6



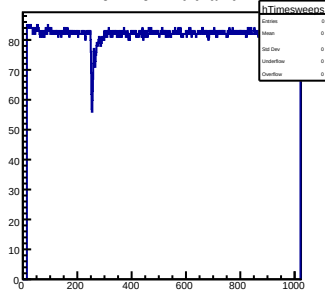
MCM 1 CHIP 0 chan 7



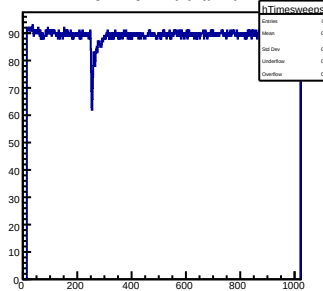
MCM 1 CHIP 0 chan 8



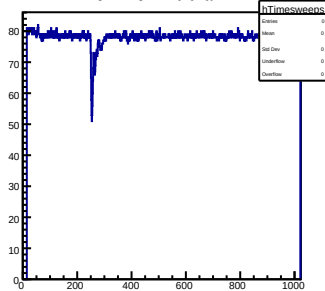
MCM 1 CHIP 0 chan 9



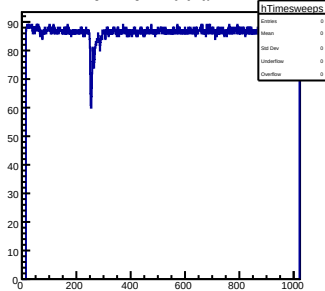
MCM 1 CHIP 0 chan 10



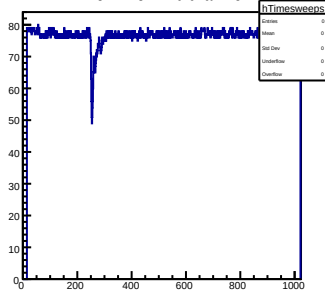
MCM 1 CHIP 0 chan 11



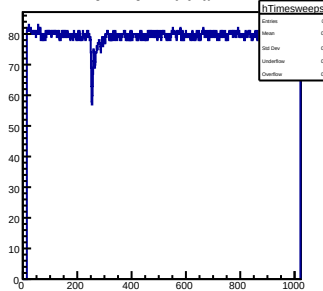
MCM 1 CHIP 0 chan 12



MCM 1 CHIP 0 chan 13



MCM 1 CHIP 0 chan 14



MCM 1 CHIP 0 chan 15

