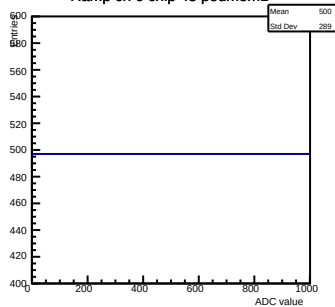
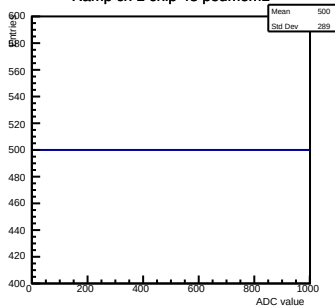


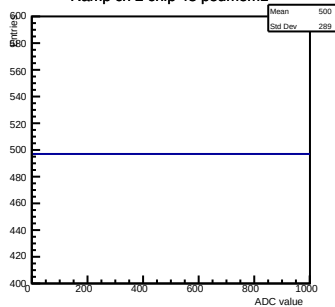
Ramp ch 0 chip 48 pedmem1



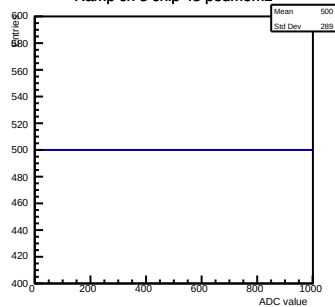
Ramp ch 1 chip 48 pedmem1



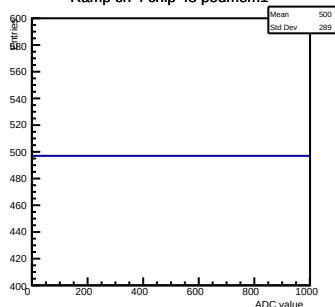
Ramp ch 2 chip 48 pedmem1



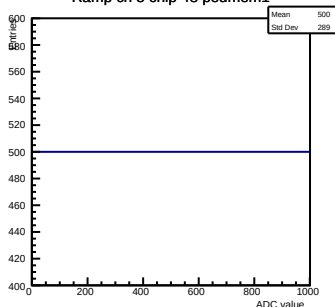
Ramp ch 3 chip 48 pedmem1



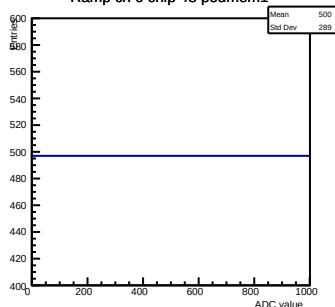
Ramp ch 4 chip 48 pedmem1



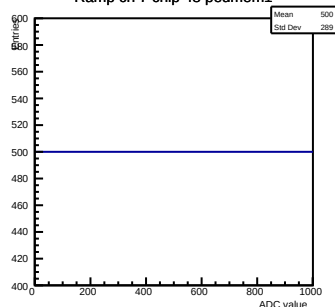
Ramp ch 5 chip 48 pedmem1



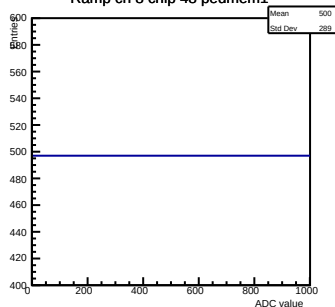
Ramp ch 6 chip 48 pedmem1



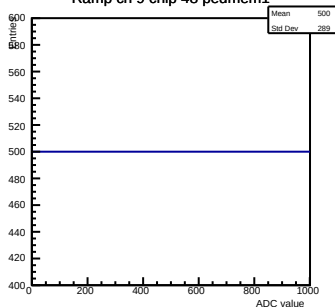
Ramp ch 7 chip 48 pedmem1



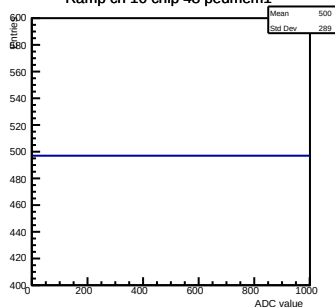
Ramp ch 8 chip 48 pedmem1



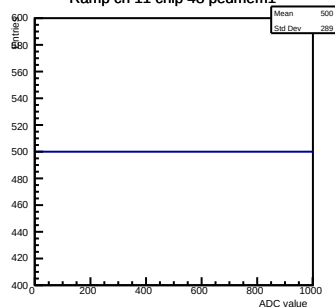
Ramp ch 9 chip 48 pedmem1



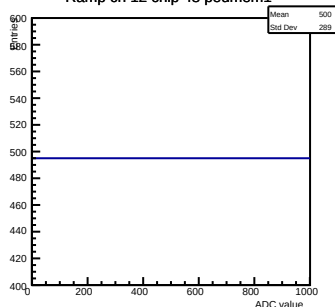
Ramp ch 10 chip 48 pedmem1



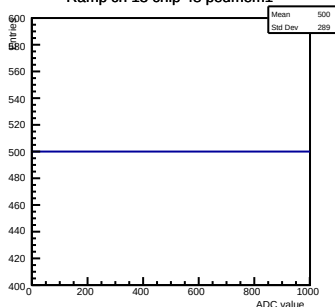
Ramp ch 11 chip 48 pedmem.



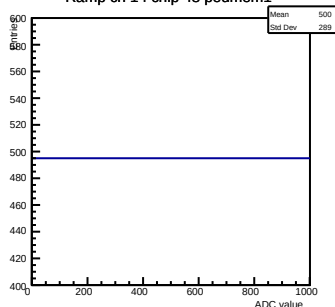
Ramp ch 12 chip 48 pedmem1



Ramp ch 13 chip 48 pedmem1



Ramp ch 14 chip 48 pedmem1



Ramp ch 15 chip 48 pedmem.

