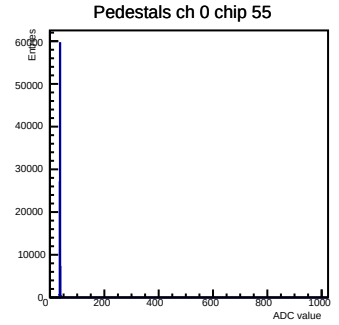
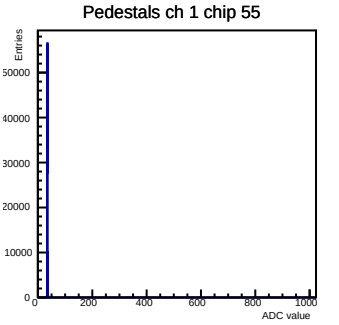
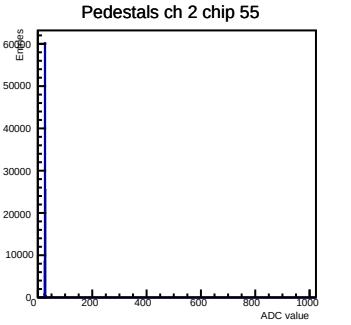
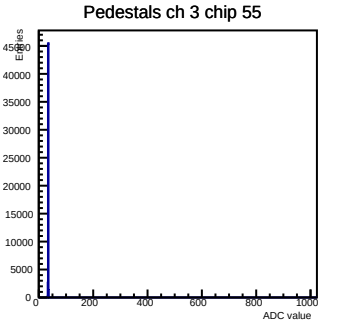
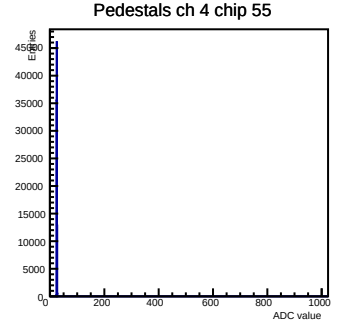
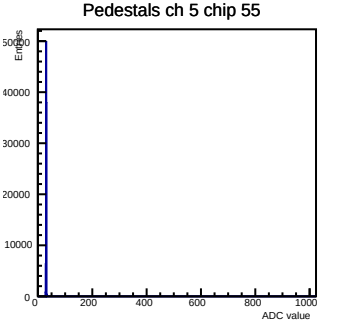

Pedestals ch 0 chip 55
Entries
ADC value

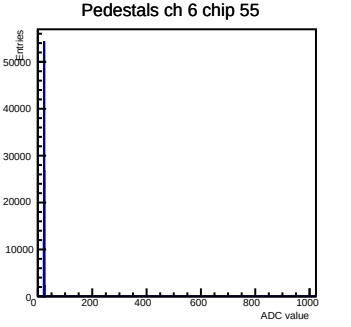

Pedestals ch 1 chip 55
Entries
ADC value

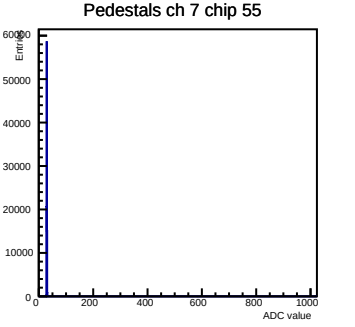

Pedestals ch 2 chip 55
Entries
ADC value

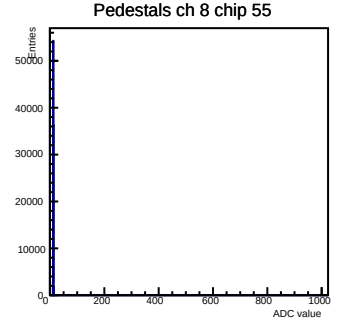

Pedestals ch 3 chip 55
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ADC value

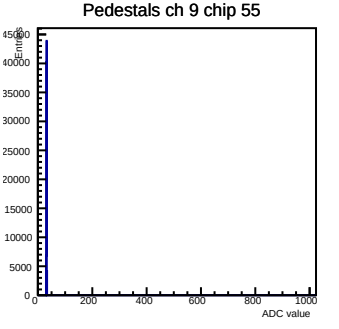

Pedestals ch 4 chip 55
Entries
ADC value

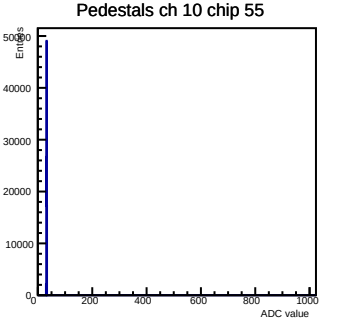

Pedestals ch 5 chip 55
Entries
ADC value

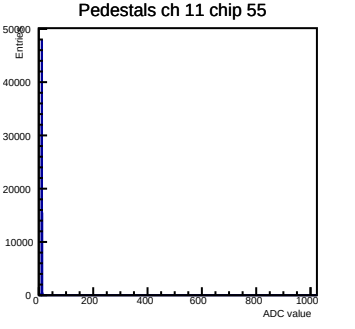

Pedestals ch 6 chip 55
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ADC value

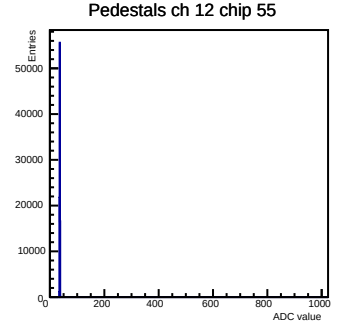

Pedestals ch 7 chip 55
Entries
ADC value

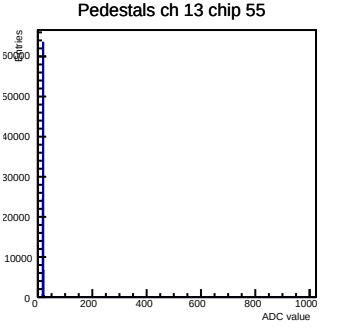

Pedestals ch 8 chip 55
Entries
ADC value

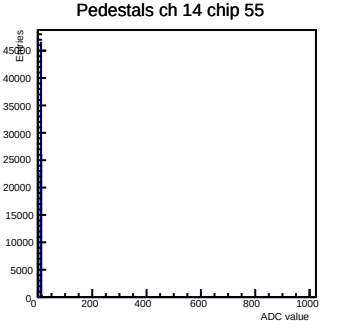

Pedestals ch 9 chip 55
Entries
ADC value

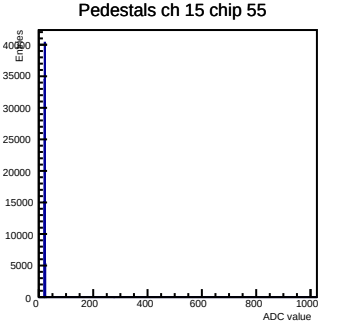

Pedestals ch 10 chip 55
Entries
ADC value


Pedestals ch 11 chip 55
Entries
ADC value


Pedestals ch 12 chip 55
Entries
ADC value


Pedestals ch 13 chip 55
Entries
ADC value


Pedestals ch 14 chip 55
Entries
ADC value


Pedestals ch 15 chip 55
Entries
ADC value