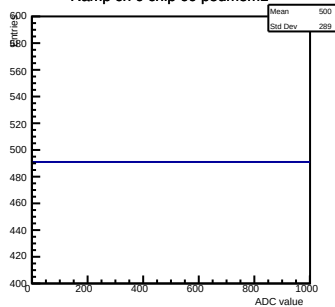
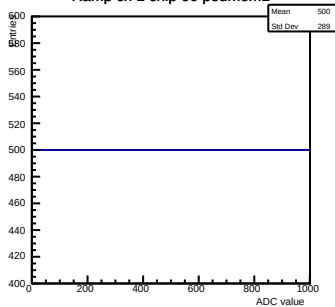


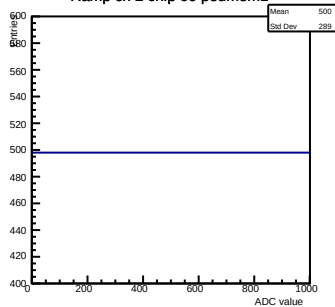
Ramp ch 0 chip 60 pedmem1



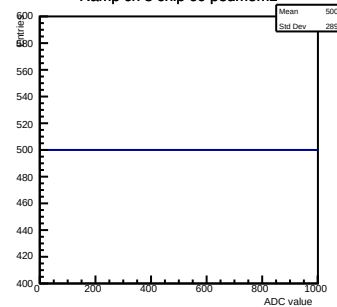
Ramp ch 1 chip 60 pedmem1



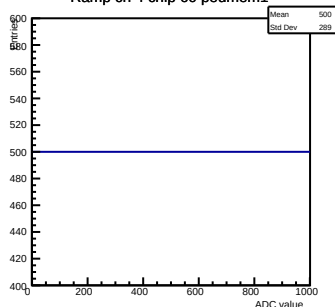
Ramp ch 2 chip 60 pedmem1



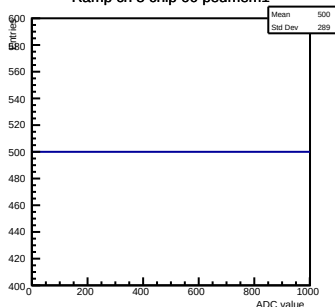
Ramp ch 3 chip 60 pedmem:



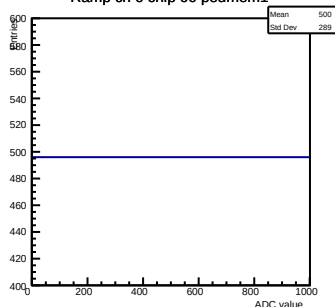
Ramp ch 4 chip 60 pedmem1



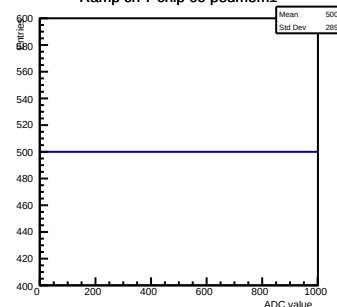
Ramp ch 5 chip 60 pedmem1



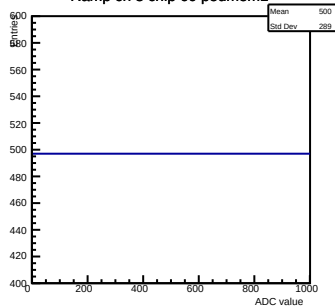
Ramp ch 6 chip 60 pedmem1



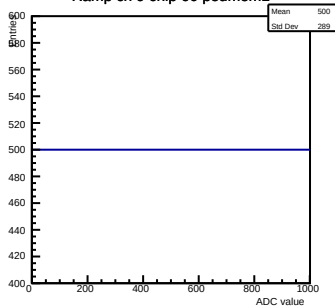
Ramp ch 7 chip 60 pedmem.



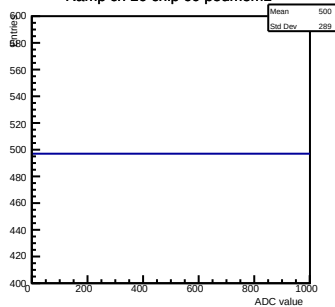
Ramp ch 8 chip 60 pedmem1



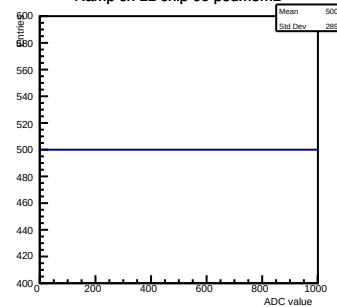
Ramp ch 9 chip 60 pedmem1



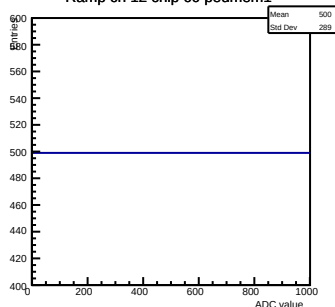
Ramp ch 10 chip 60 pedmem1



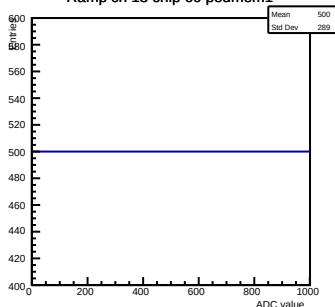
Ramp ch 11 chip 60 pedmem



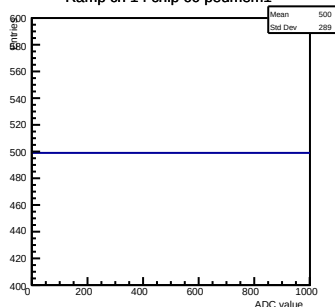
Ramp ch 12 chip 60 pedmem1



Ramp ch 13 chip 60 pedmem1



Ramp ch 14 chip 60 pedmem1



Ramp ch 15 chip 60 pedmem

