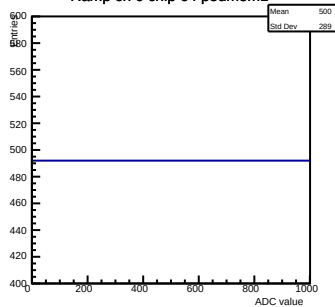
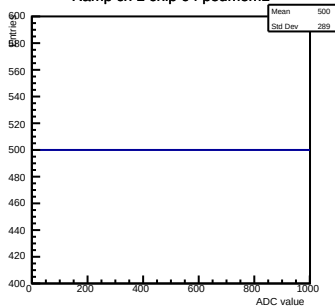


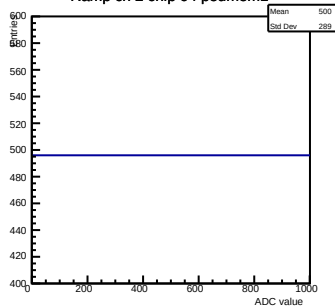
Ramp ch 0 chip 64 pedmem1



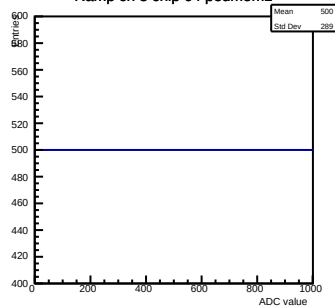
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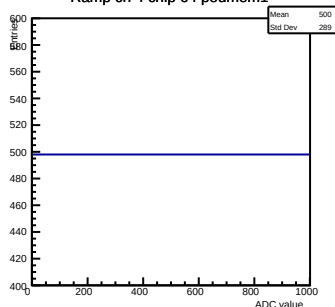
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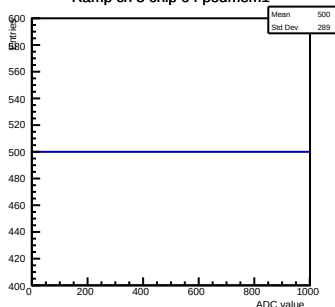
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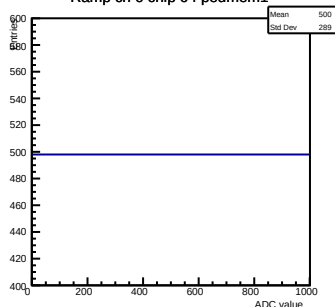
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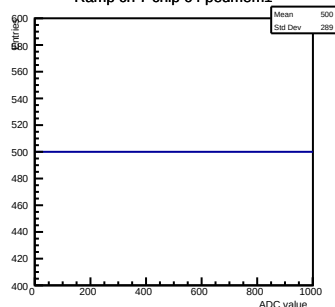
Ramp ch 5 chip 64 pedmem1



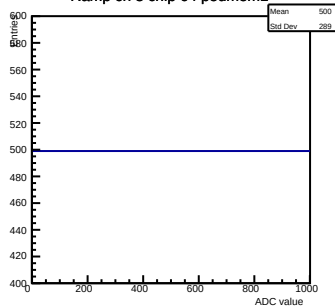
Ramp ch 6 chip 64 pedmem1



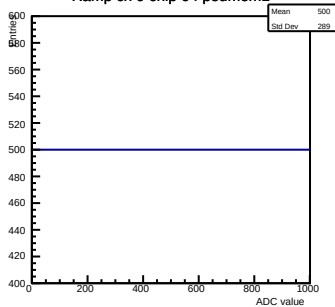
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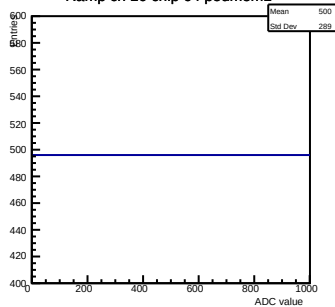
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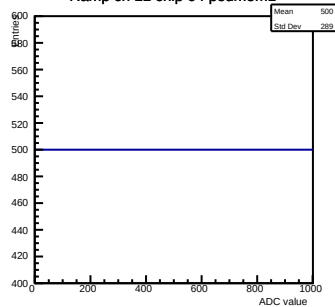
Ramp ch 9 chip 64 pedmem1



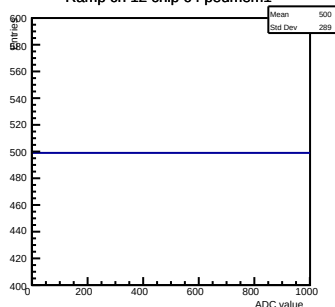
Ramp ch 10 chip 64 pedmem1



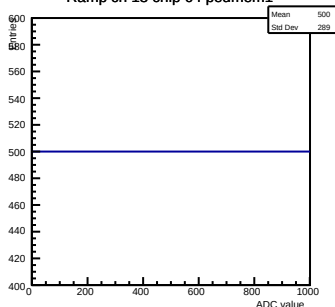
Ramp ch 11 chip 64 pedmem.



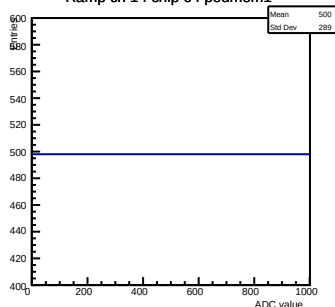
Ramp ch 12 chip 64 pedmem1



Ramp ch 13 chip 64 pedmem1



Ramp ch 14 chip 64 pedmem1



Ramp ch 15 chip 64 pedmem.

