

Histograms of frequency of each ADC value for chip 65.

Several runs at different times.

Runs used for pulserdata:

readout-20957_0.root
readout-20958_0.root
readout-20985_0.root
readout-20986_0.root
readout-20992_0.root
readout-20993_0.root
readout-20999_0.root
readout-21008_0.root

Run used for pedestals:

readout-20309_0.root

Pulse is from not synchronized pulse generator:

Square wave pulse, ca 20.5 kHz, 50% duty.

Some runs have only two channels, 0 and 8, and then with 980 samples.

Some runs have all channels, 0-15, and then with 135 samples.

Two different pulses with different amplitudes, i.e. two peaks at high ADC values.

No zero suppression, i.e. negative going edge of incoming pulse will give values below baseline down to 0.

Plots: first is pedestals for all channels

Then plots for each channel:

ADC values 0-1023

ADC values 0-123

ADC values 124-623

ADC values 624-1023

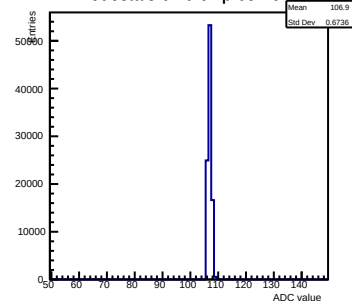
Comments: (with excuse for uncontrolled input pulse)

There is some change of behavior below the pedestal.

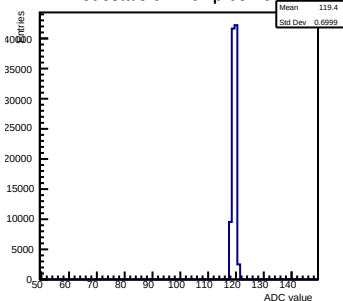
There are valleys and heights in some channels.

All channels have a specific pattern, 4 clock cycles pattern.

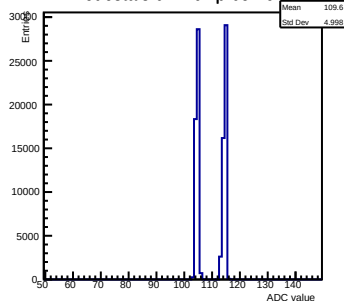
Pedestals ch 0 chip 65 nom



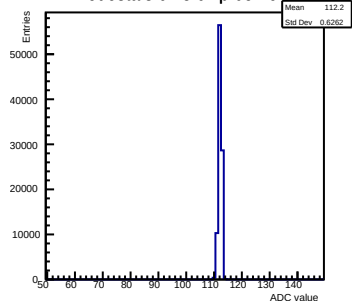
Pedestals ch 1 chip 65 nom



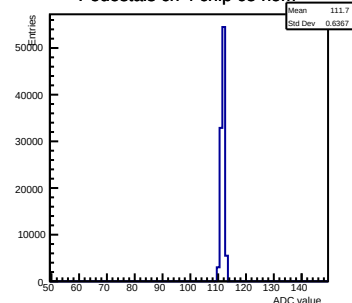
Pedestals ch 2 chip 65 nom



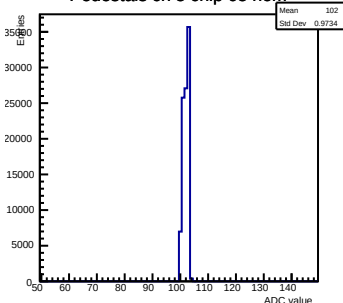
Pedestals ch 3 chip 65 nom



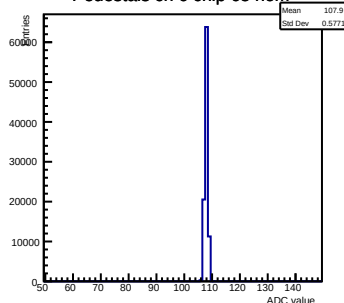
Pedestals ch 4 chip 65 nom



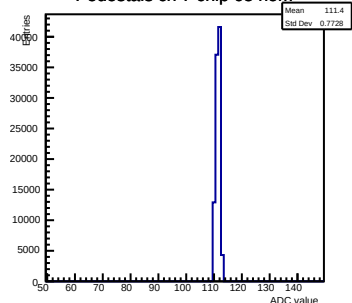
Pedestals ch 5 chip 65 nom



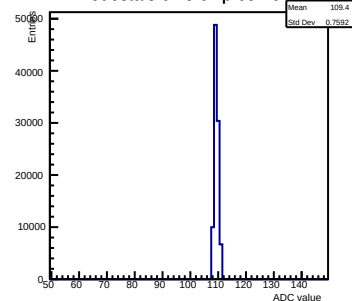
Pedestals ch 6 chip 65 nom



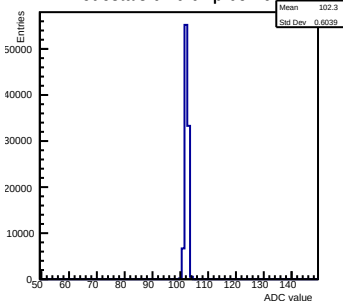
Pedestals ch 7 chip 65 nom



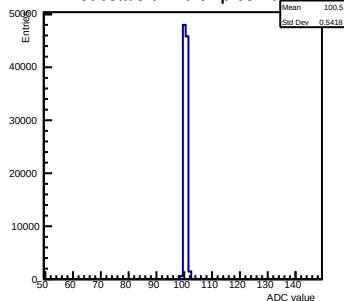
Pedestals ch 8 chip 65 nom



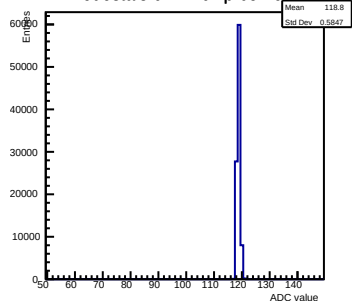
Pedestals ch 9 chip 65 nom



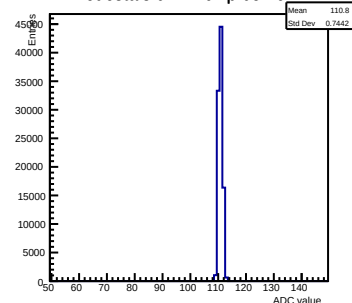
Pedestals ch 10 chip 65 nom



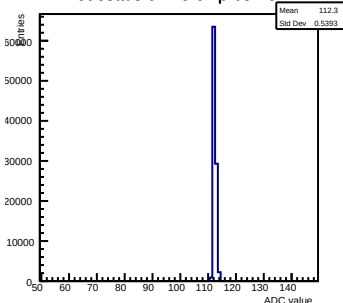
Pedestals ch 11 chip 65 nom



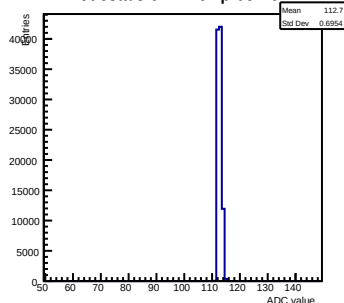
Pedestals ch 12 chip 65 nom



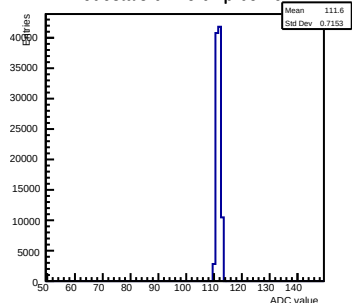
Pedestals ch 13 chip 65 nom



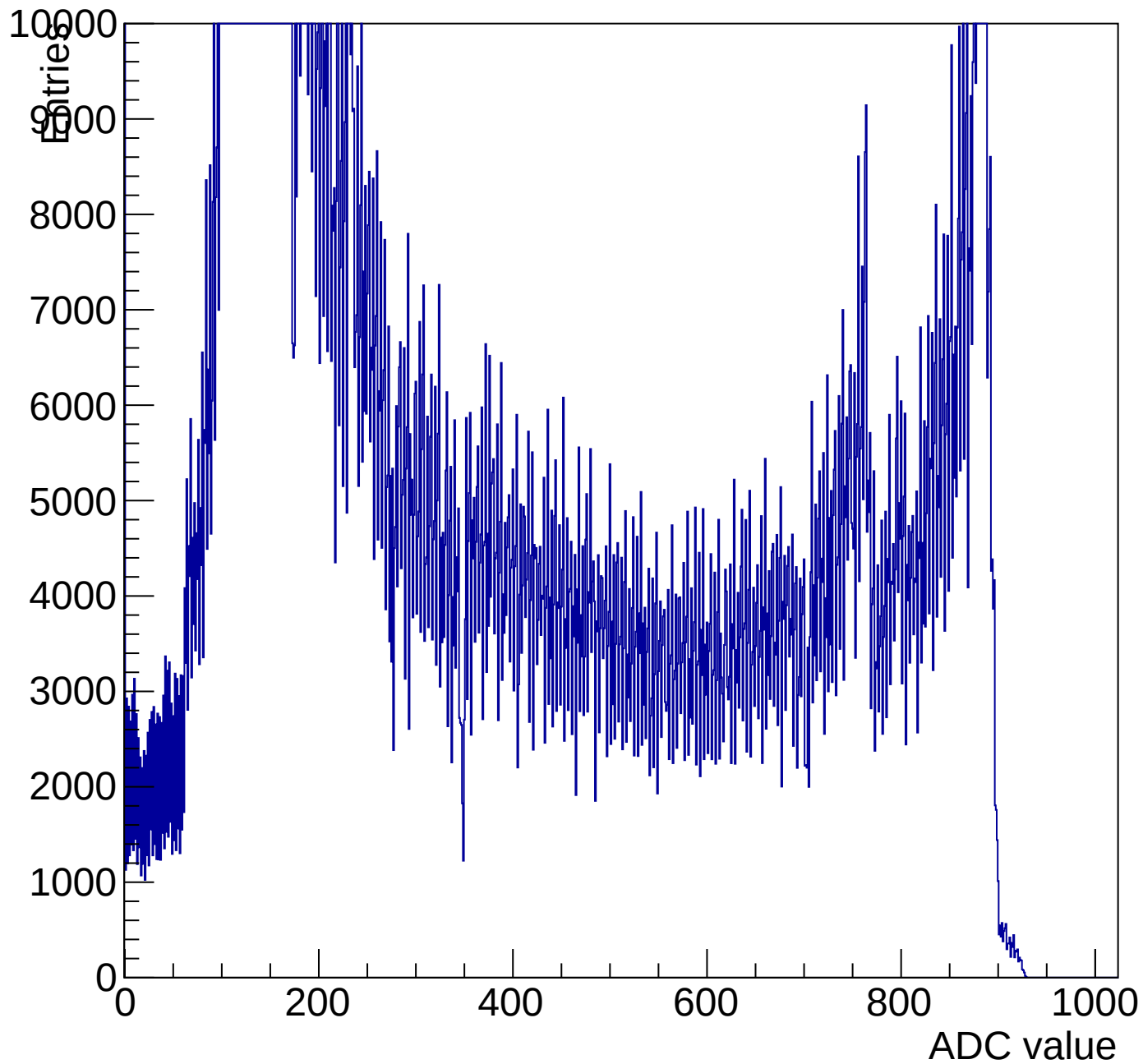
Pedestals ch 14 chip 65 nom



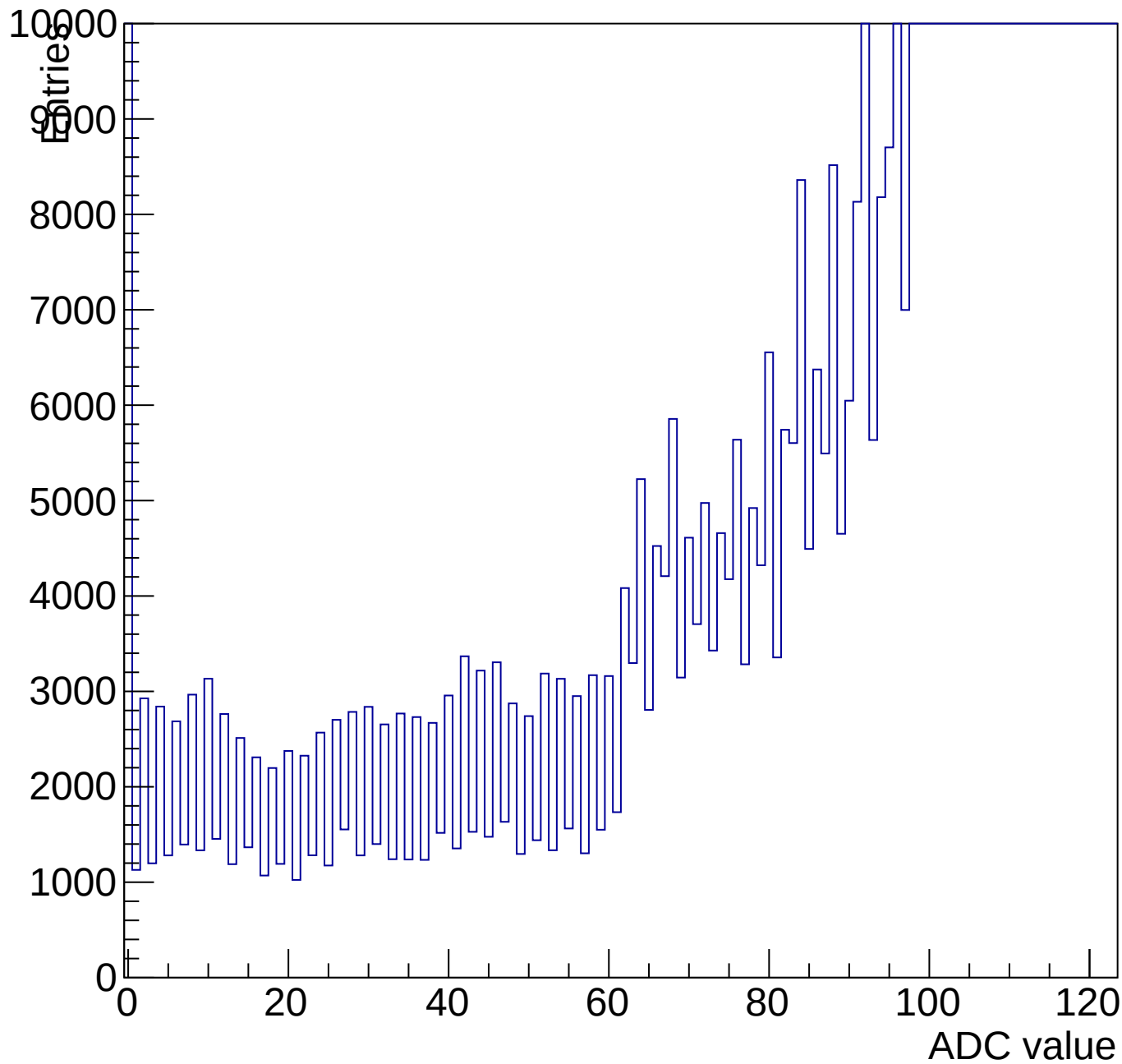
Pedestals ch 15 chip 65 nom



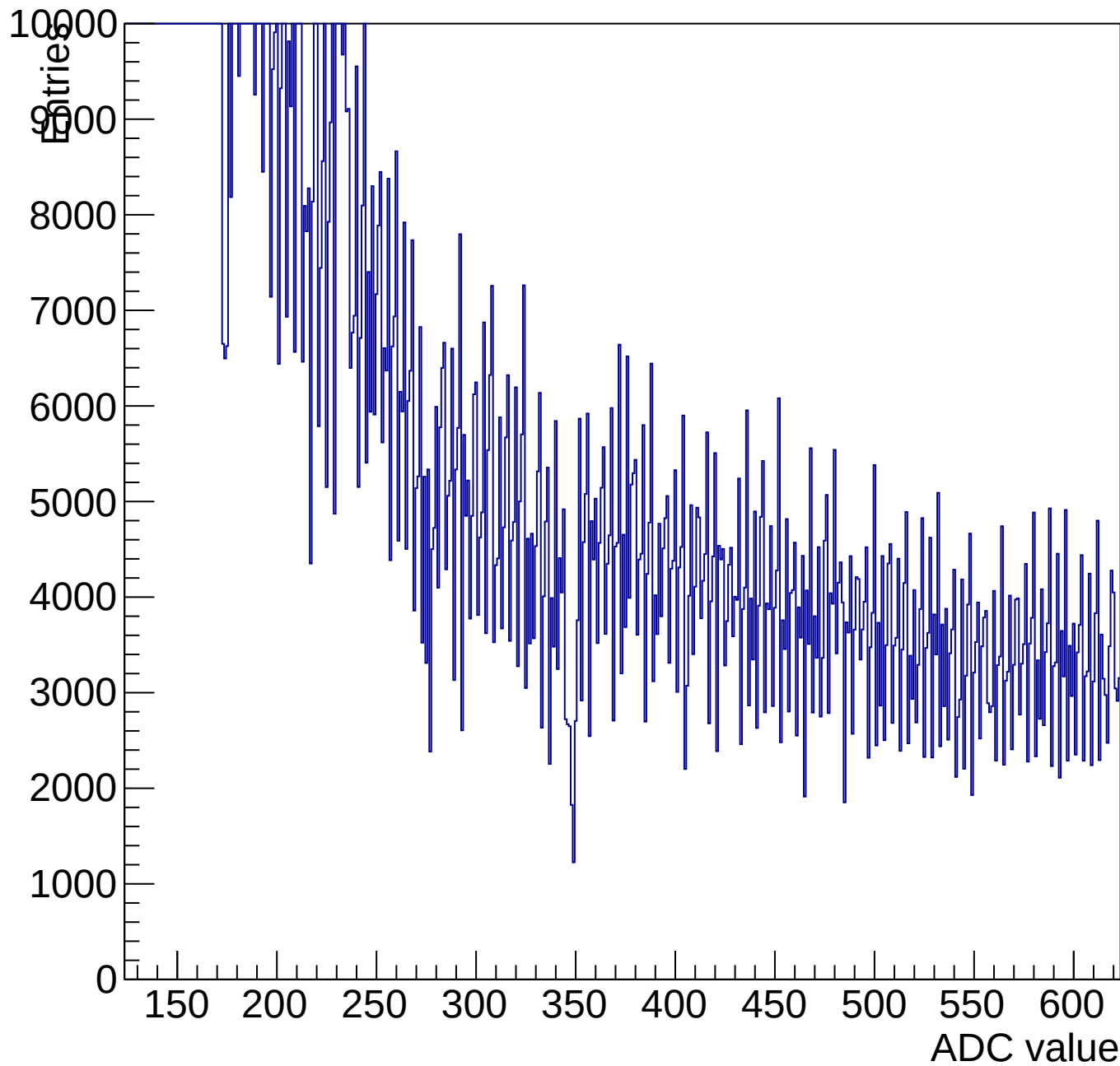
Pulse height chip 65 ch 0



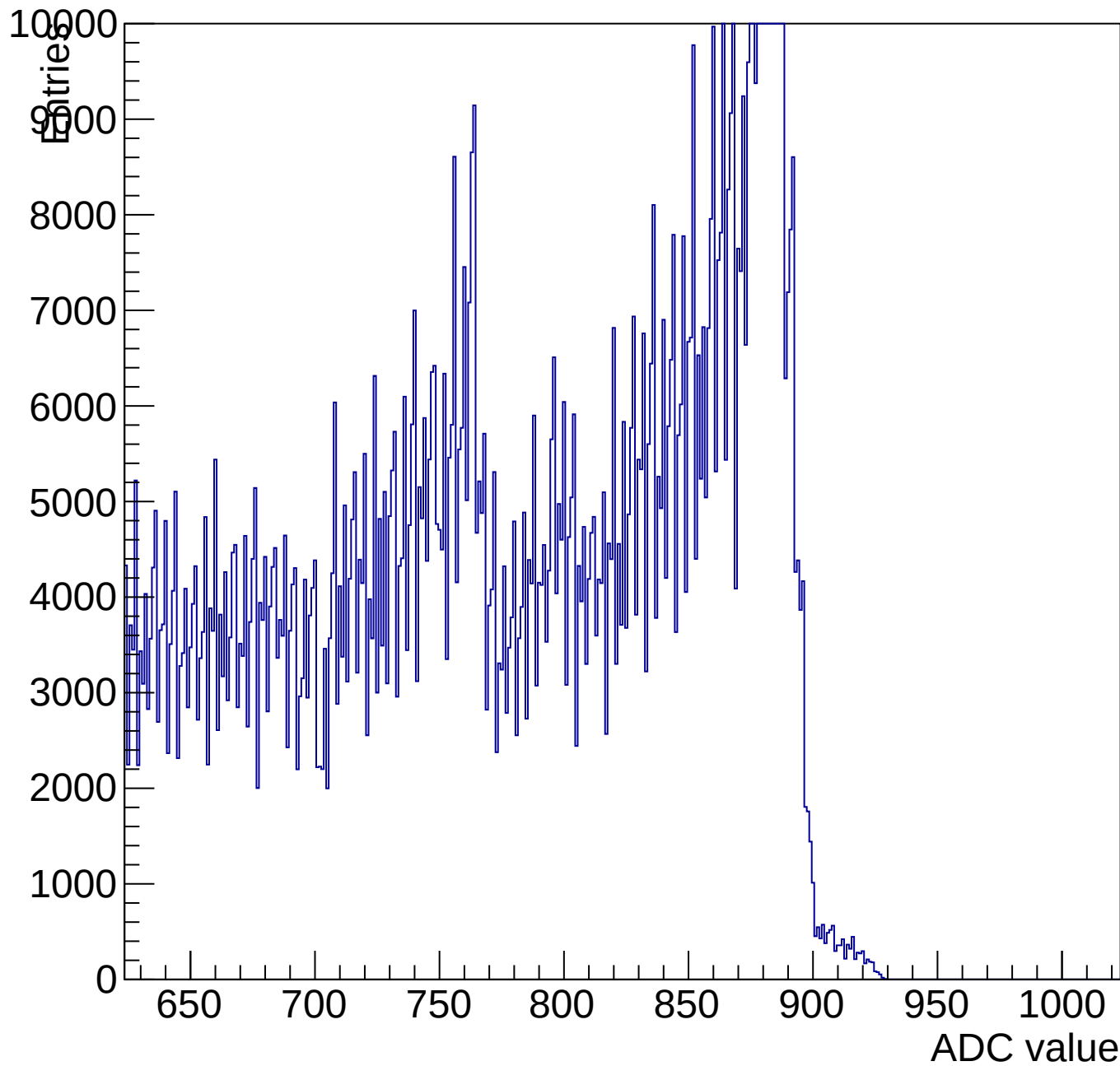
Pulse height chip 65 ch 0



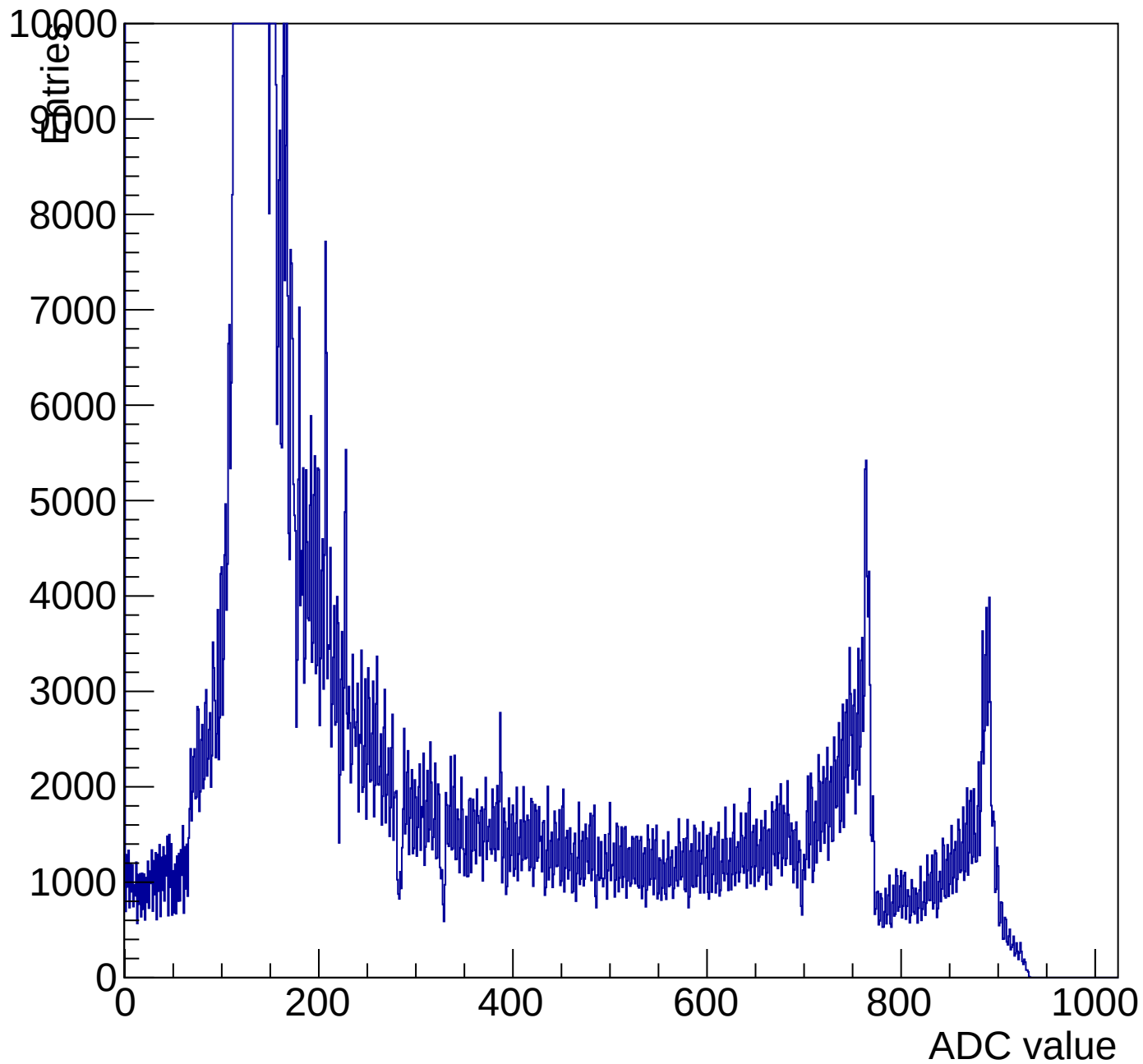
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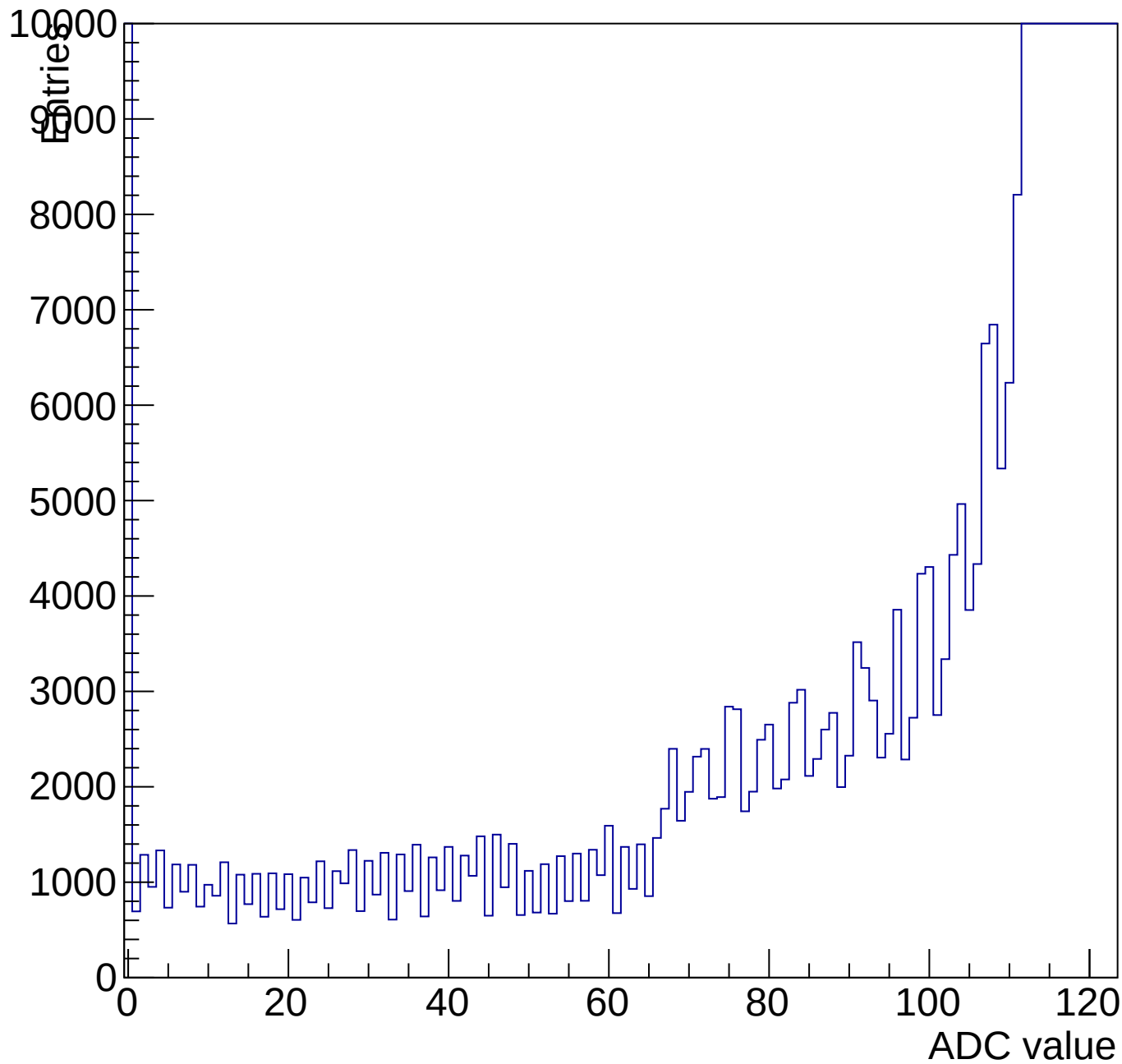
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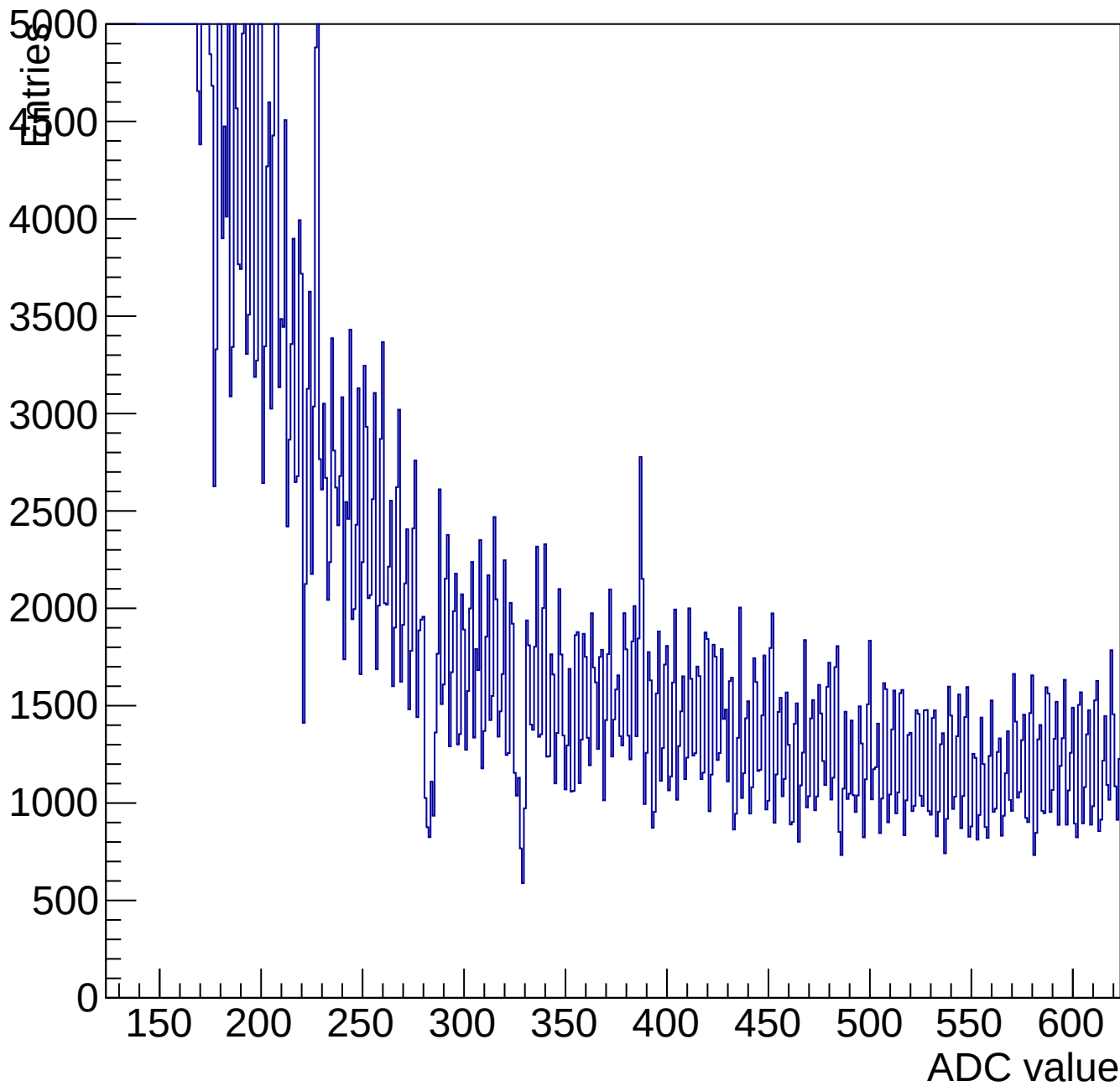
Pulse height chip 65 ch 1



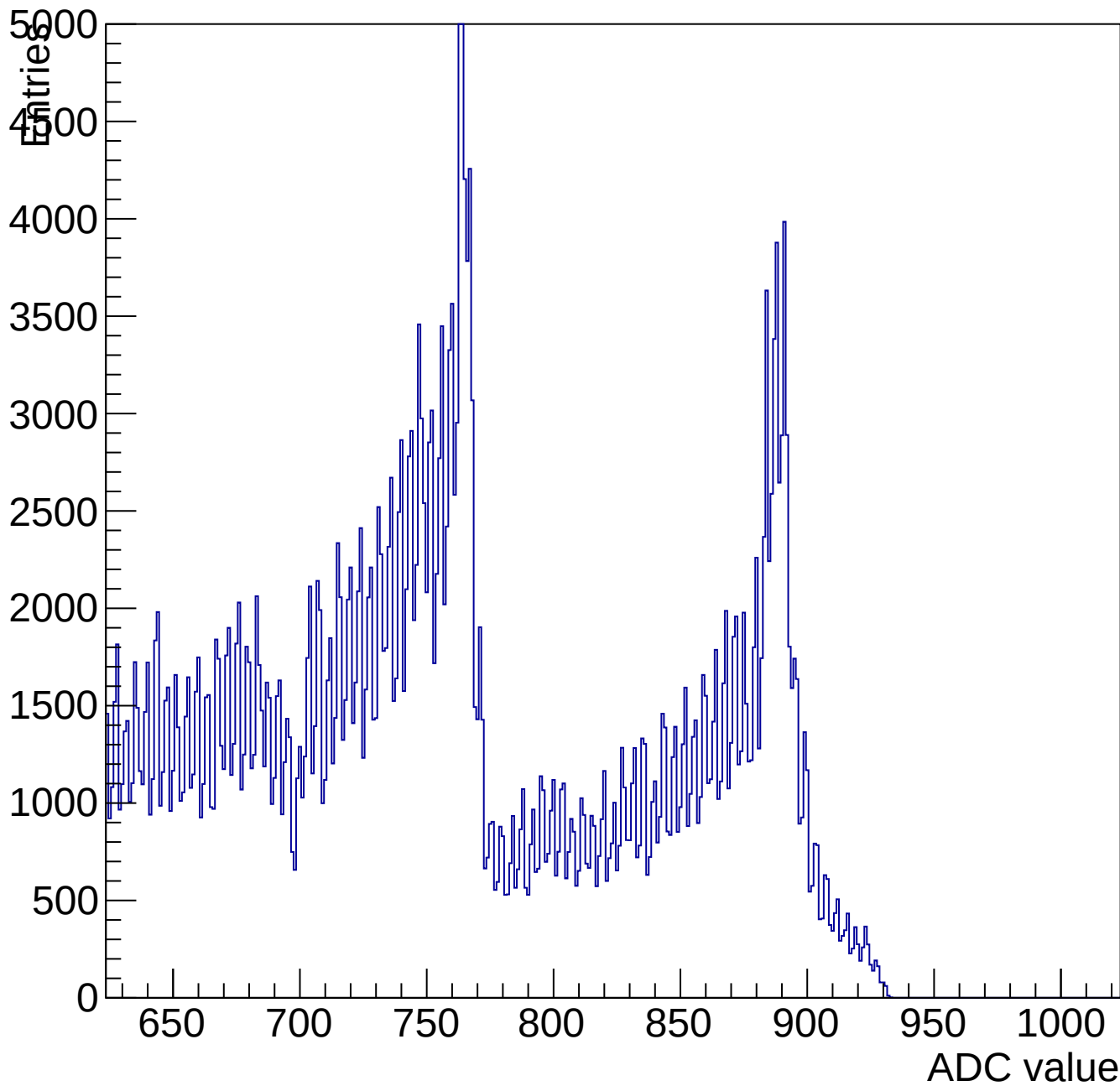
Pulse height chip 65 ch 1



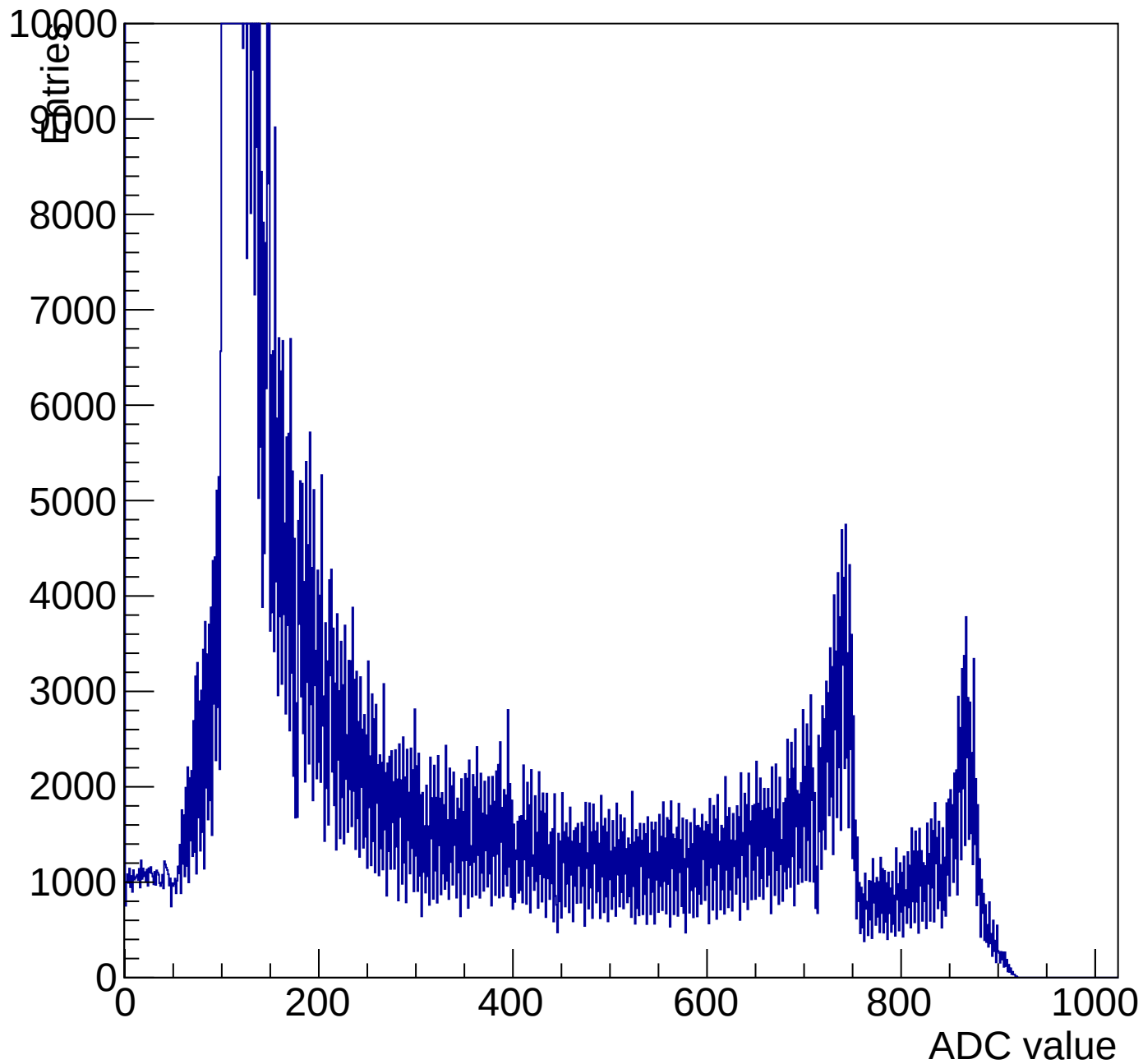
Pulse height chip 65 ch 1



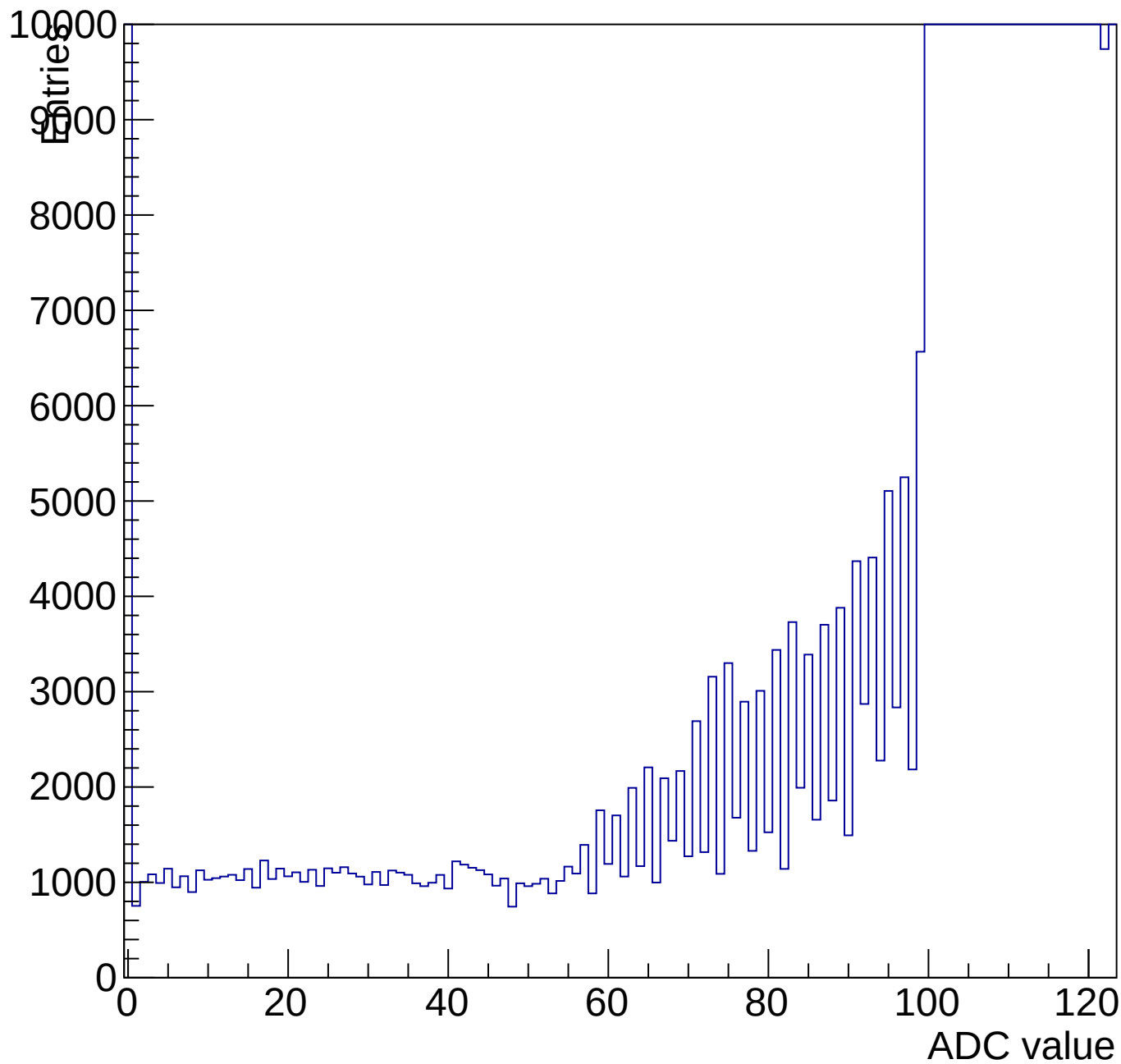
Pulse heightchip 65 ch 1



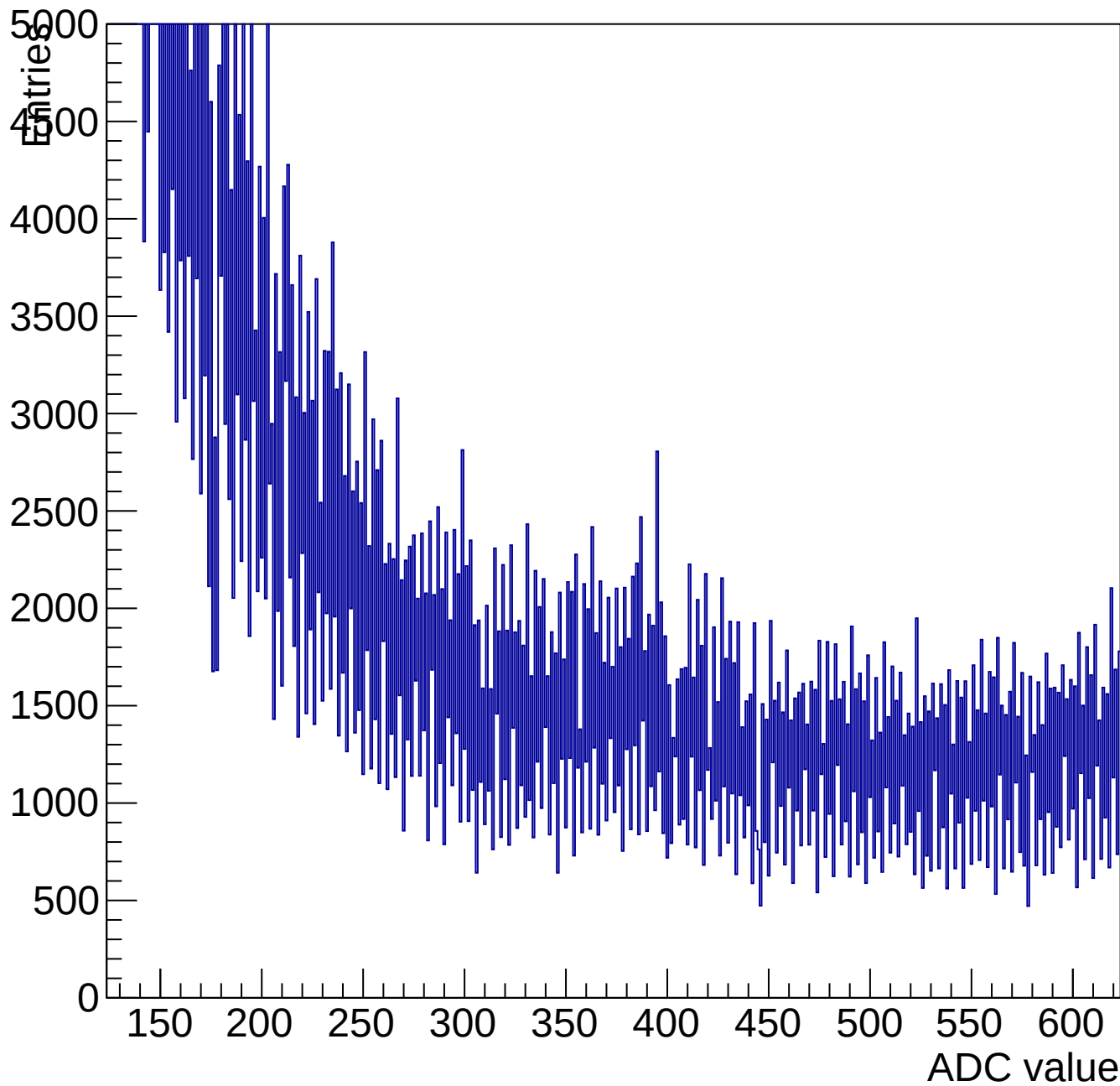
Pulse height chip 65 ch 2



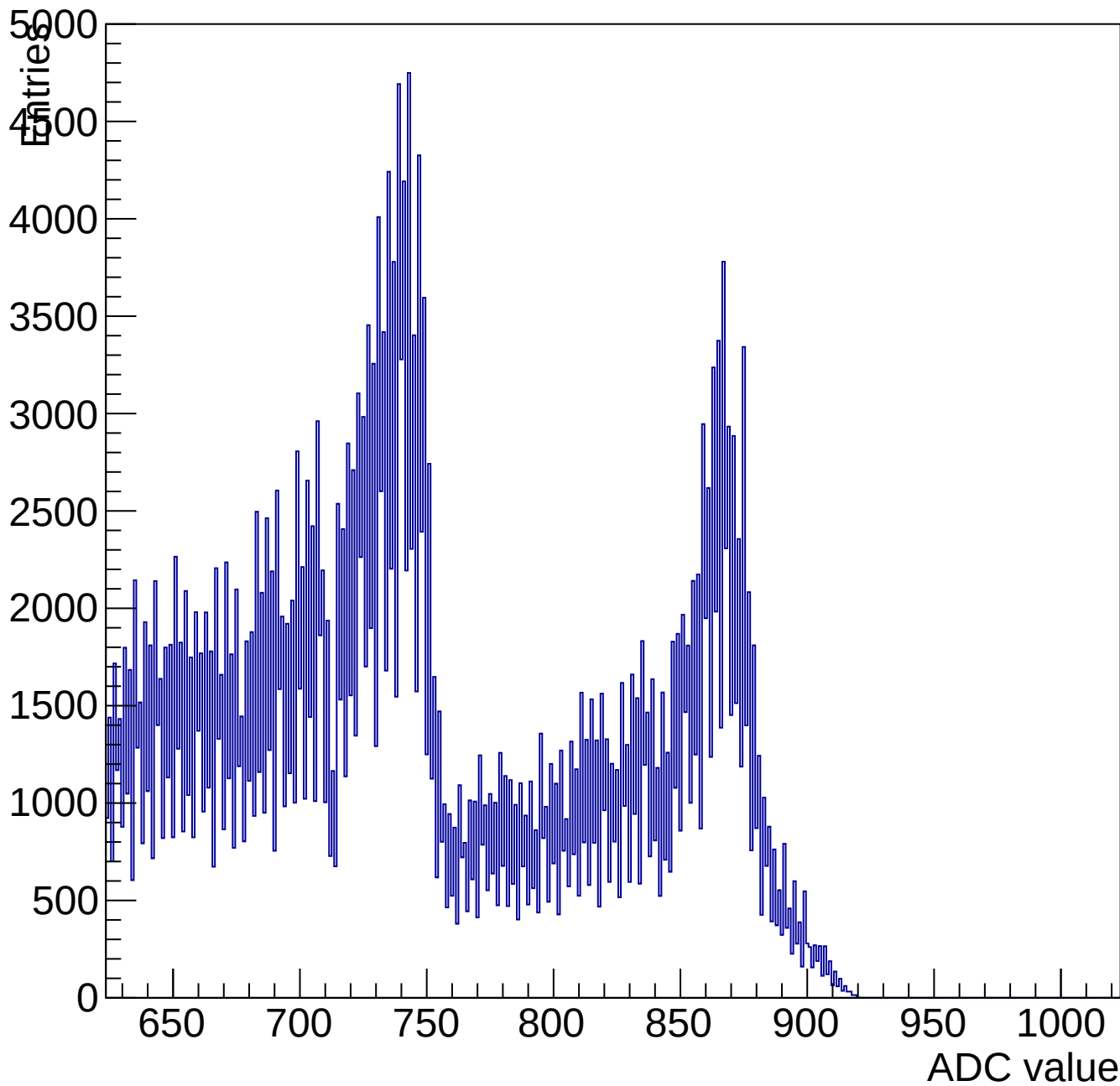
Pulse height chip 65 ch 2



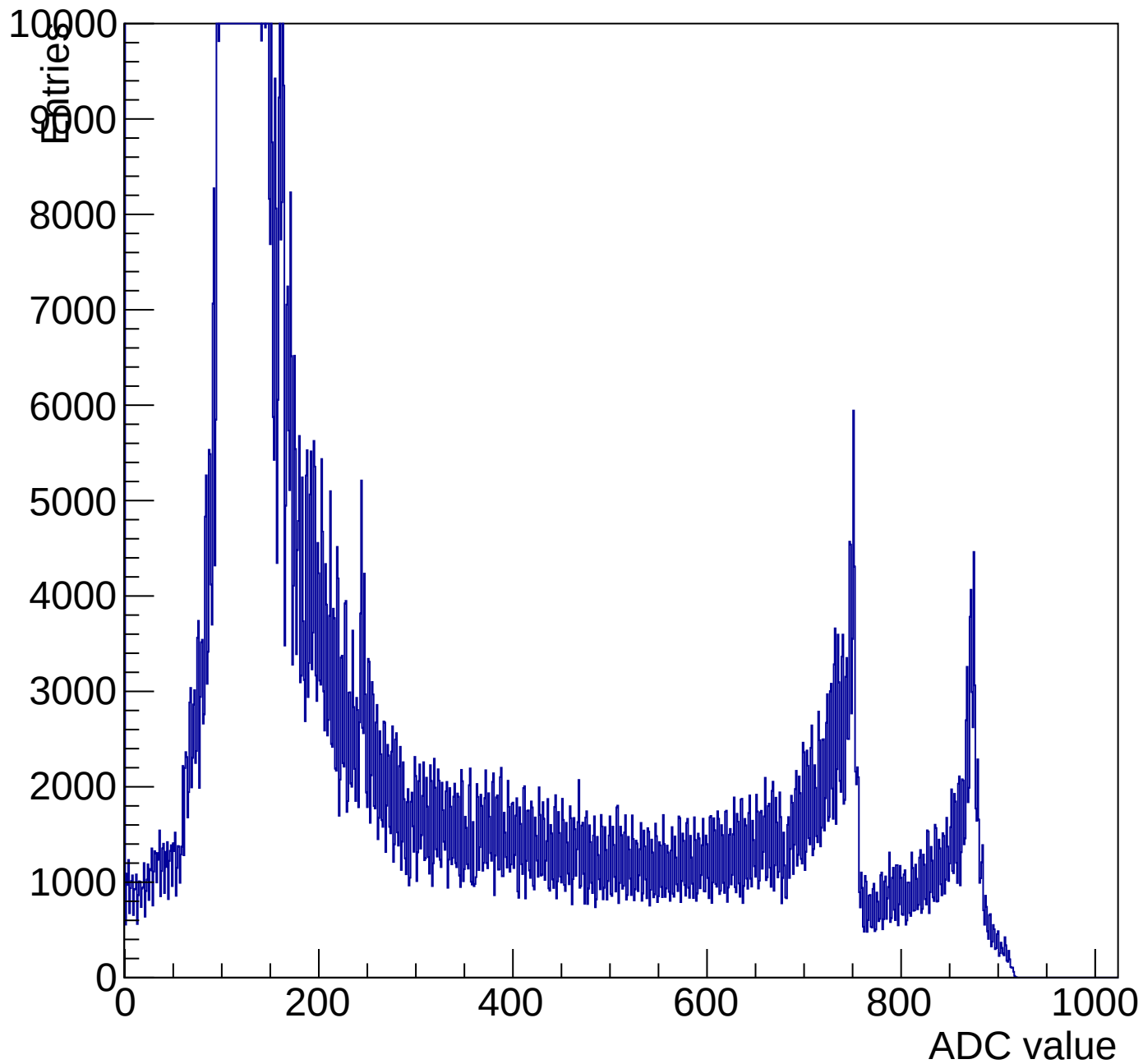
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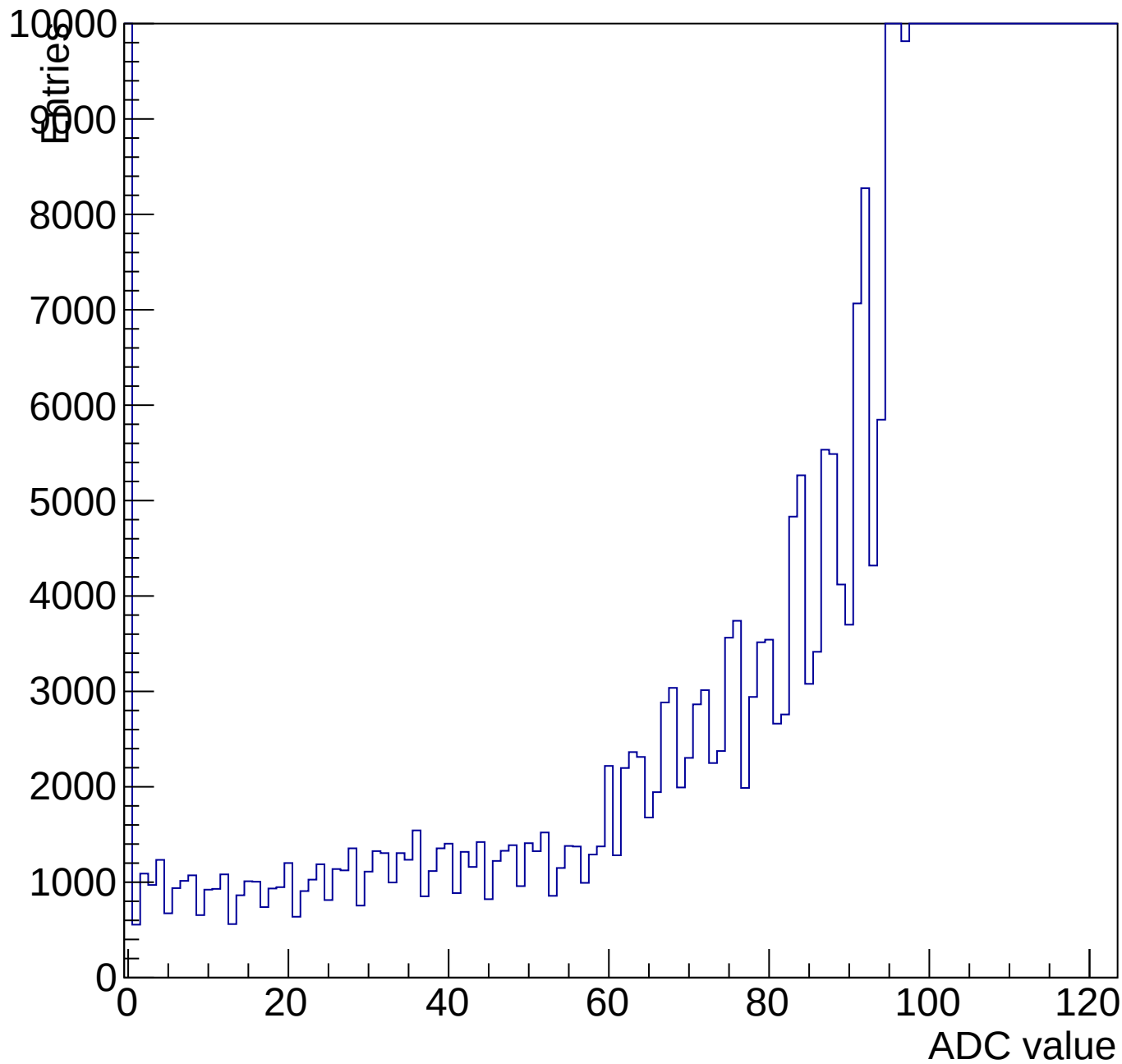
Pulse heightchip 65 ch 2



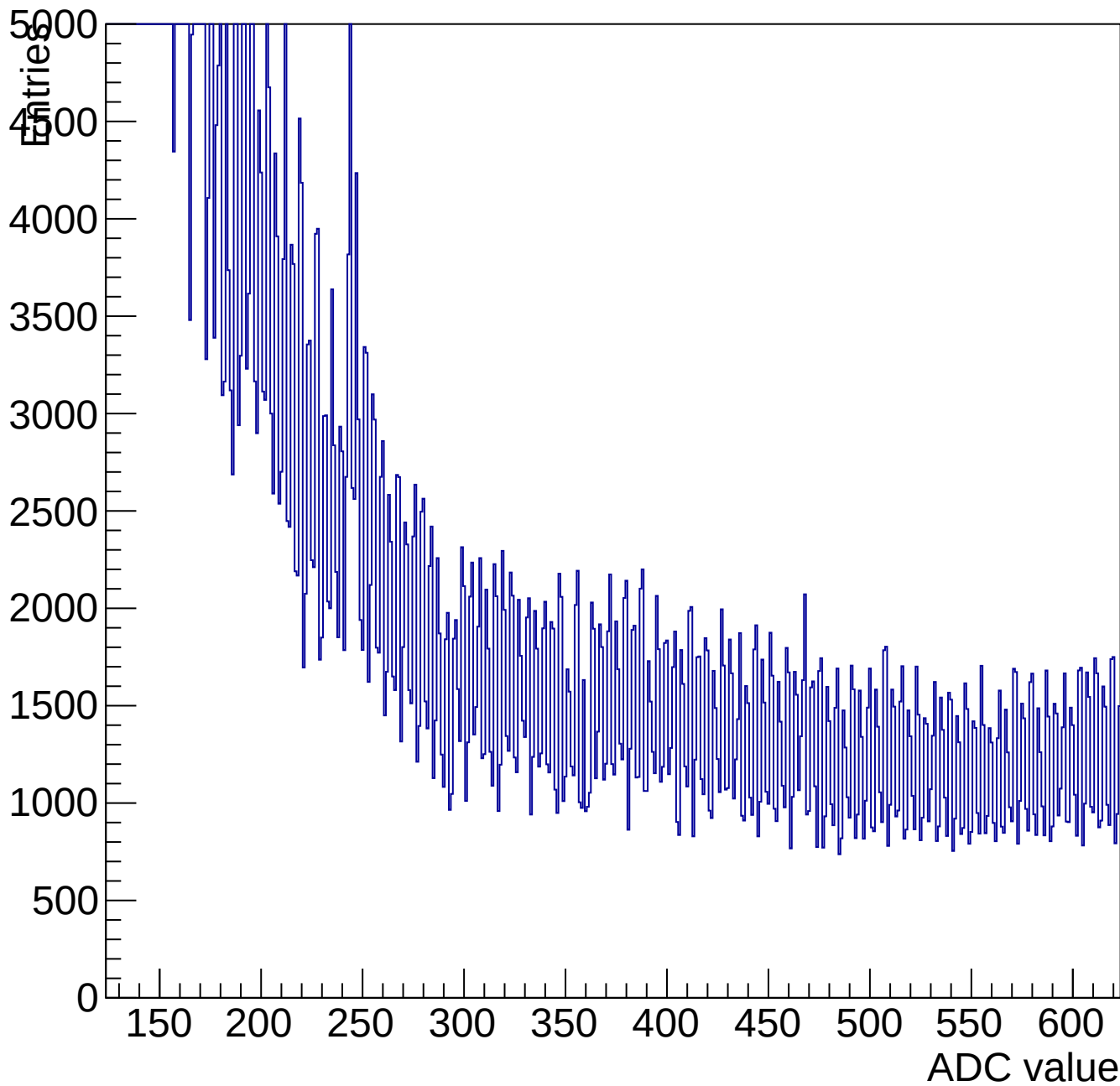
Pulse height chip 65 ch 3



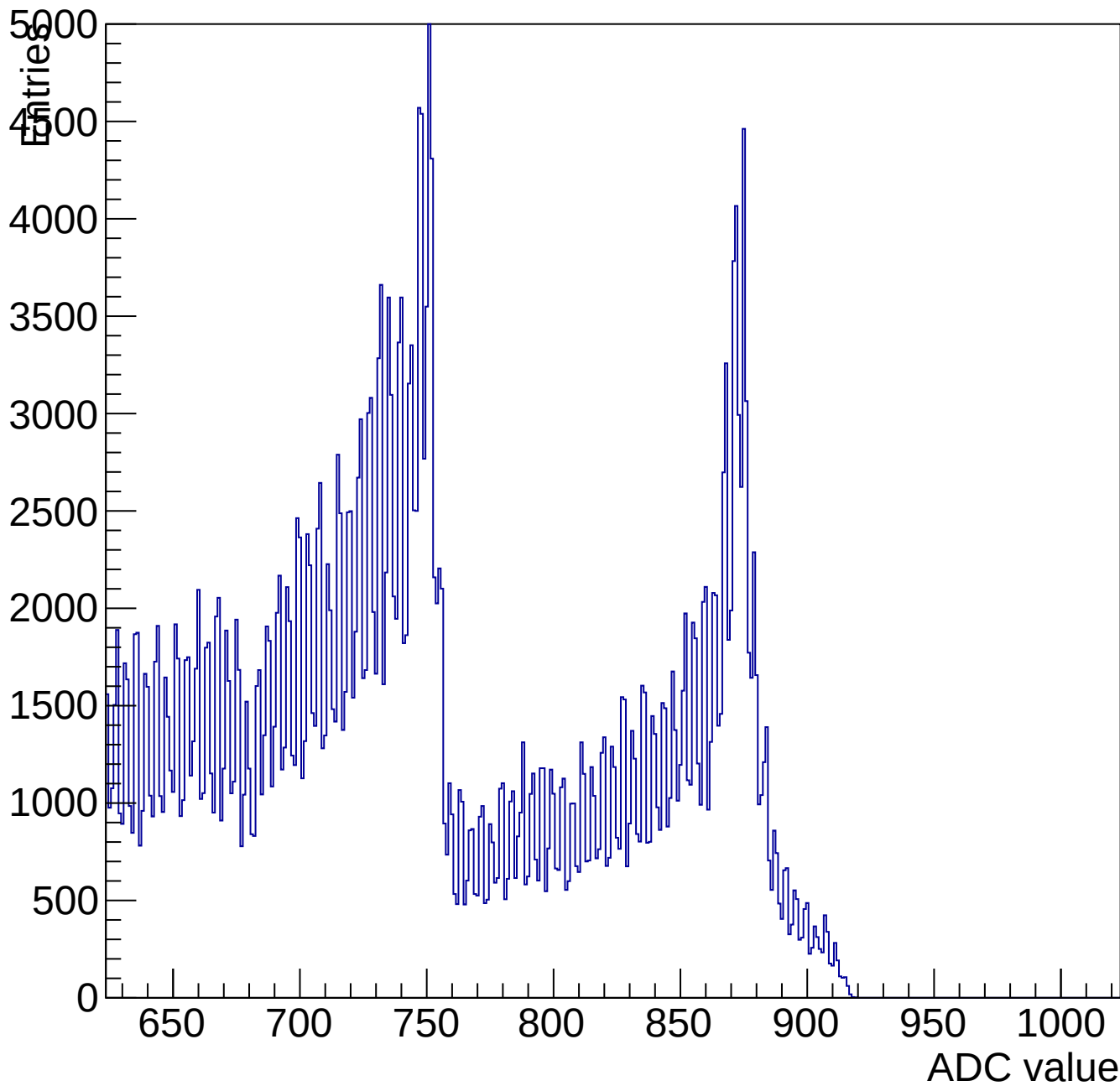
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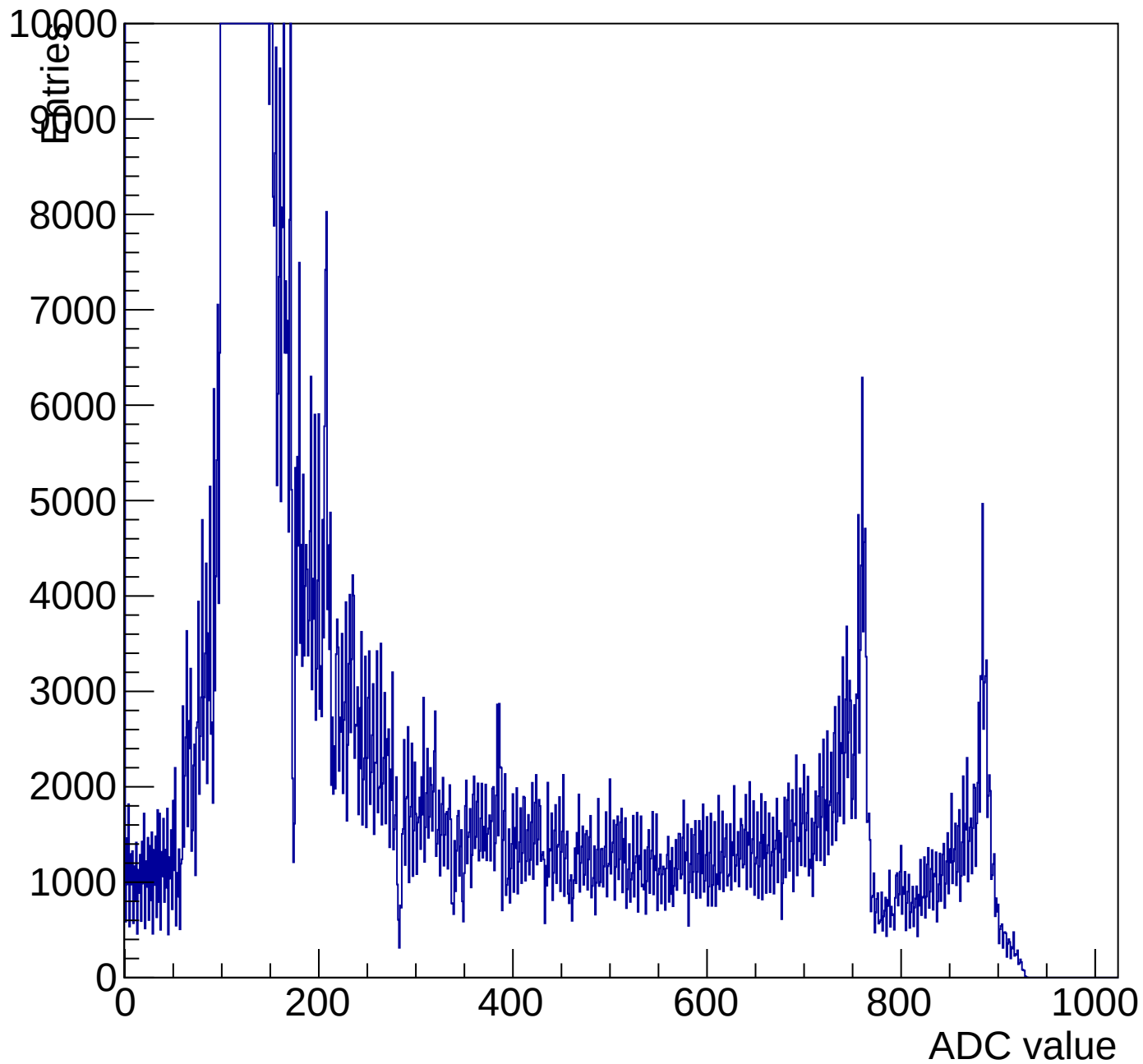
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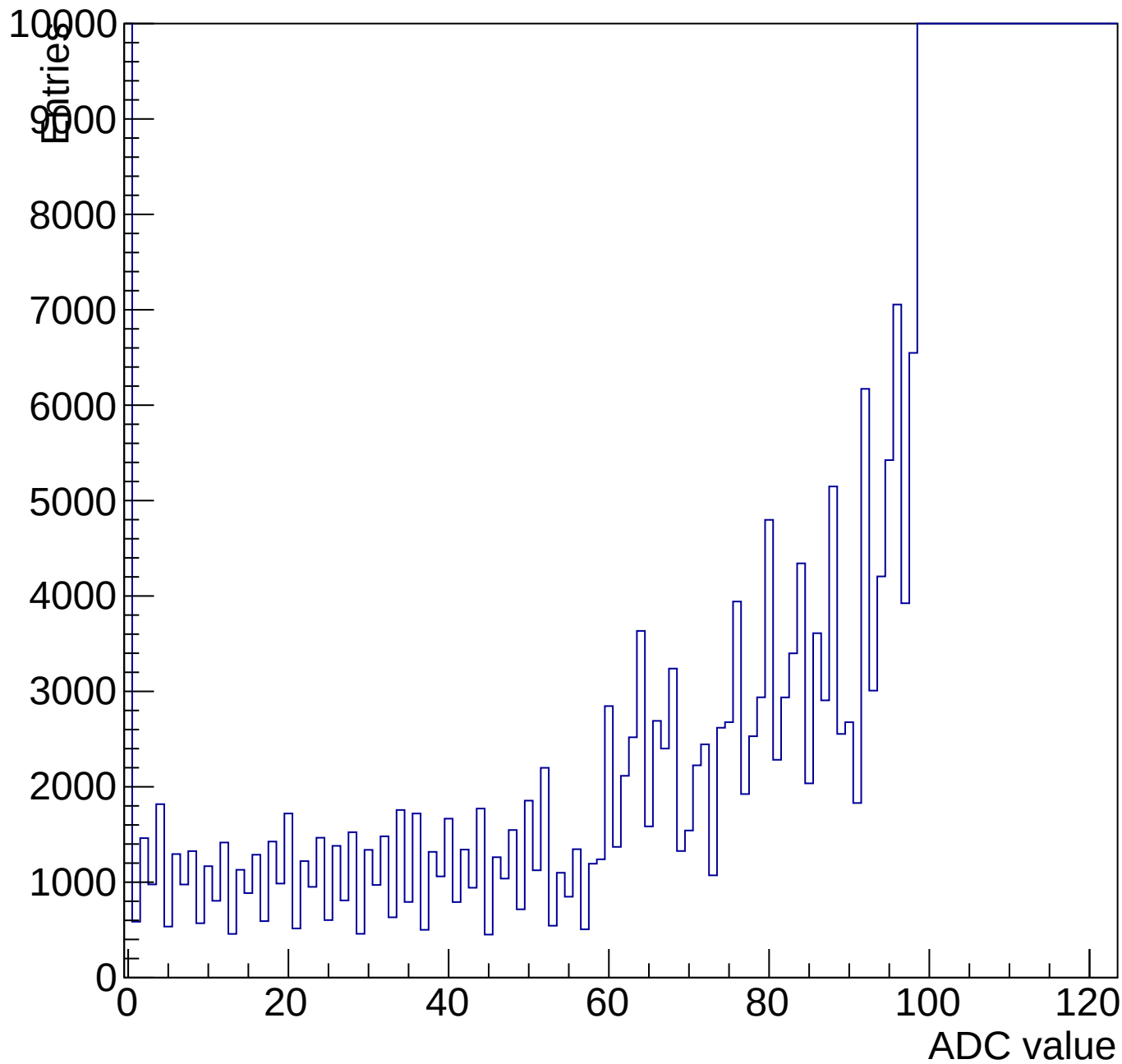
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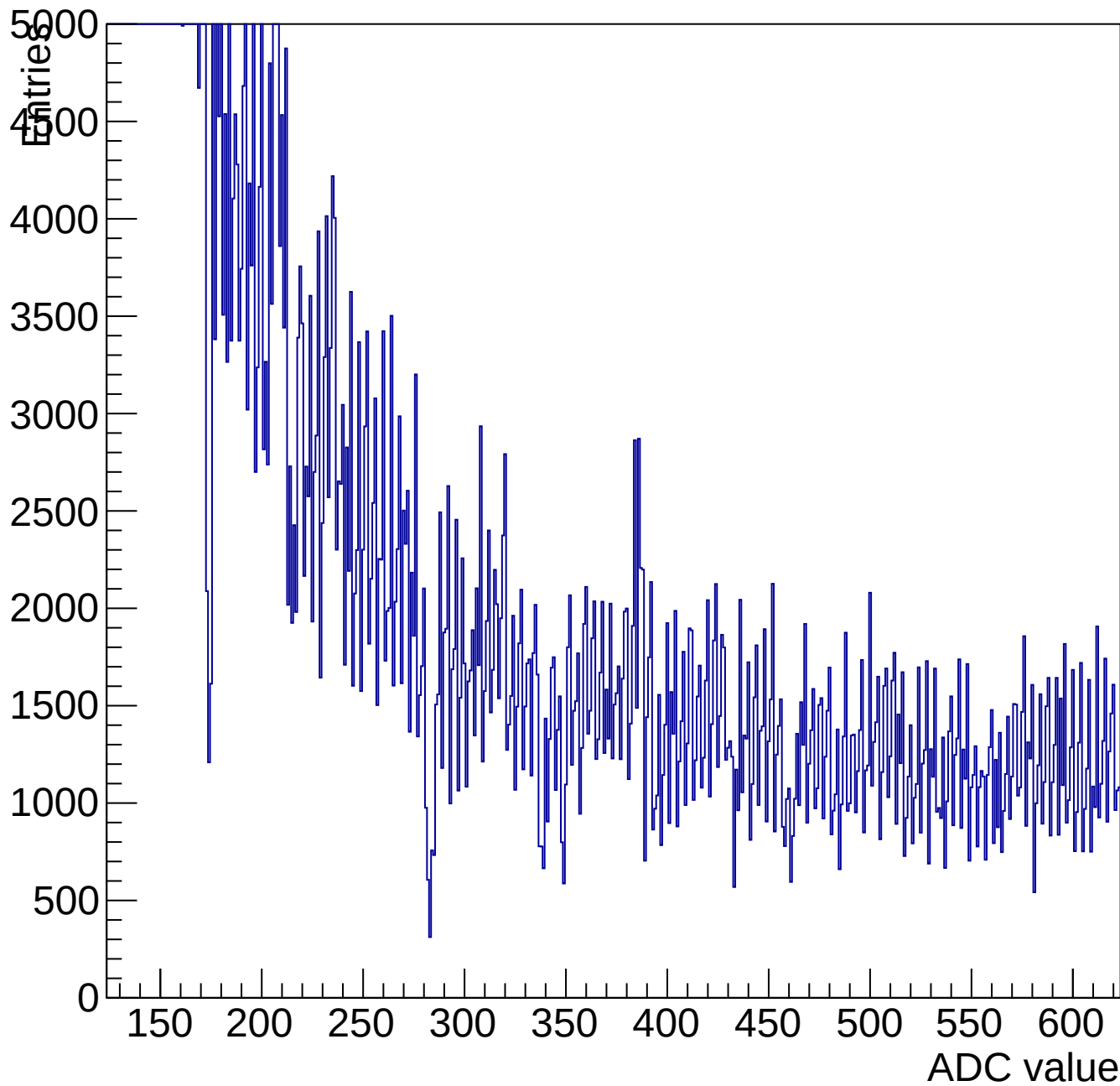
Pulse height chip 65 ch 4



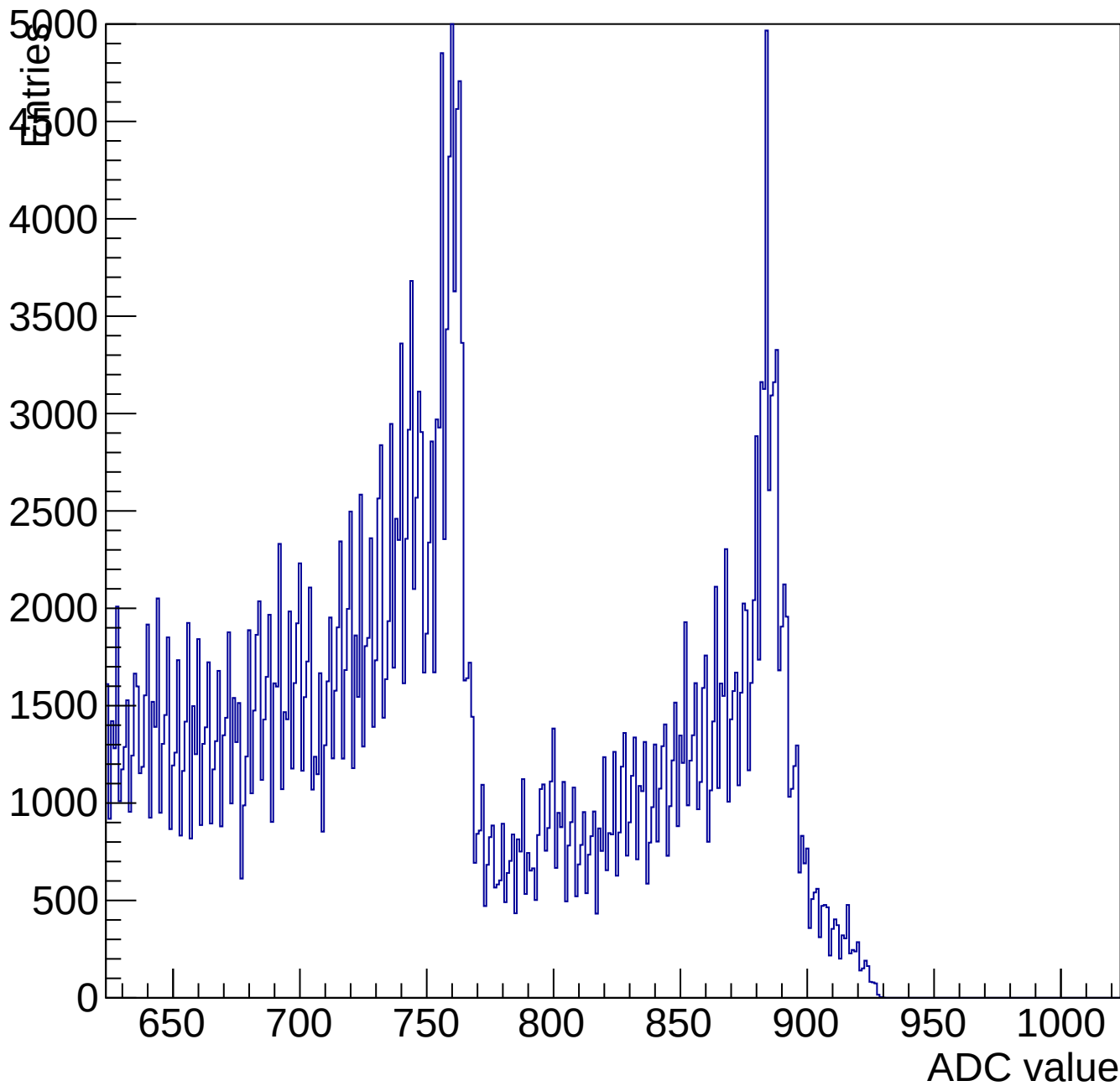
Pulse height chip 65 ch 4



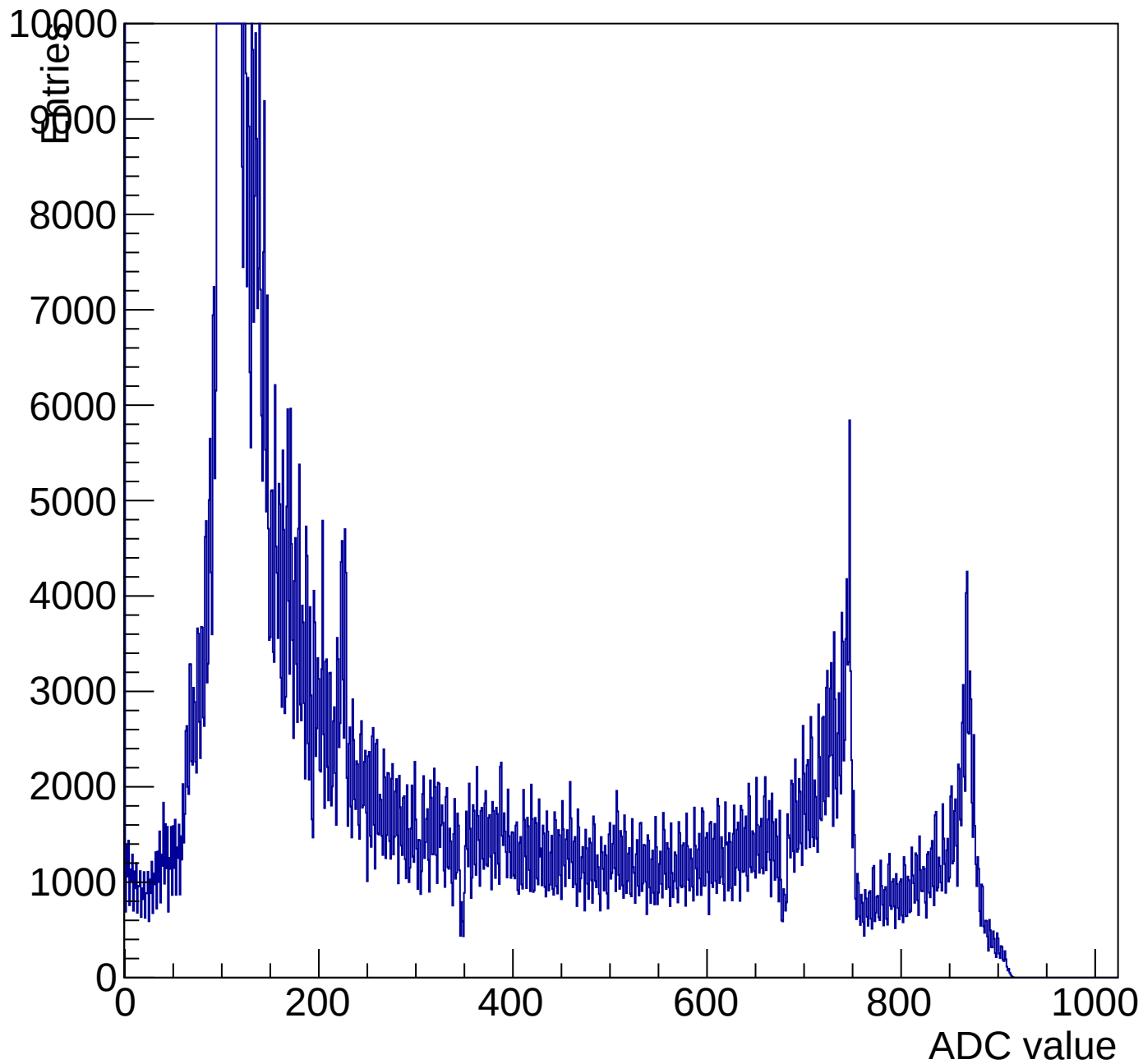
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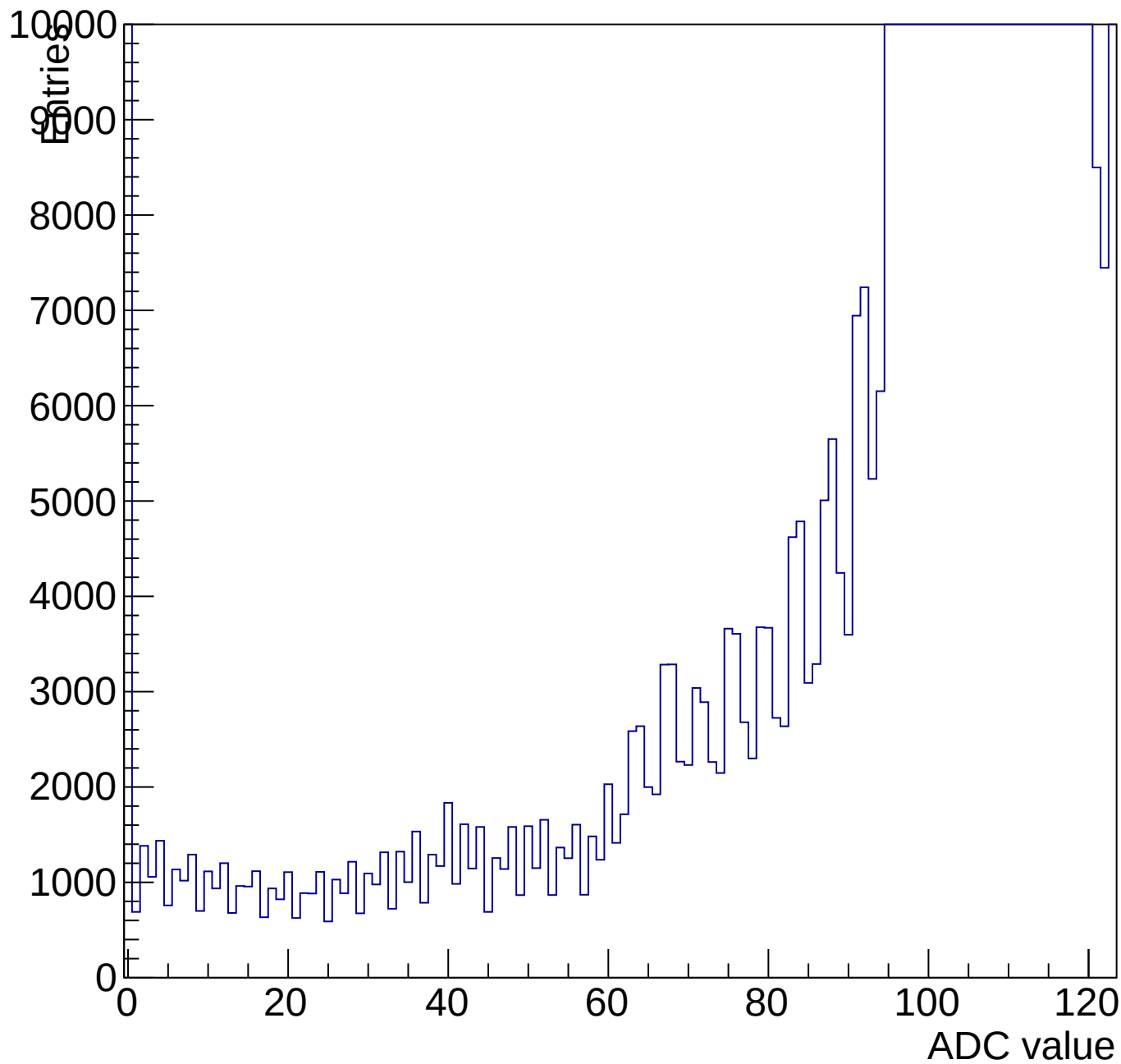
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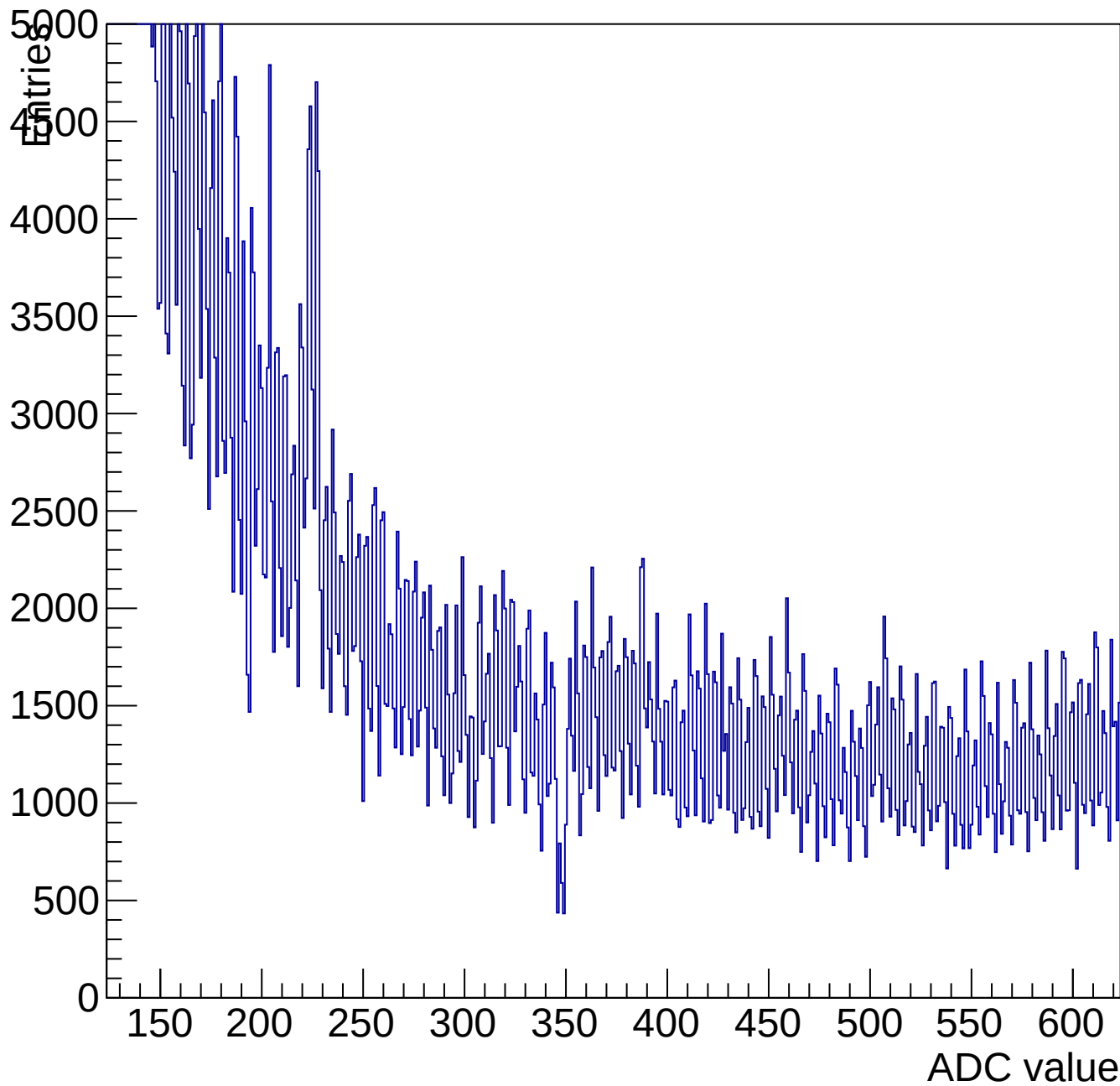
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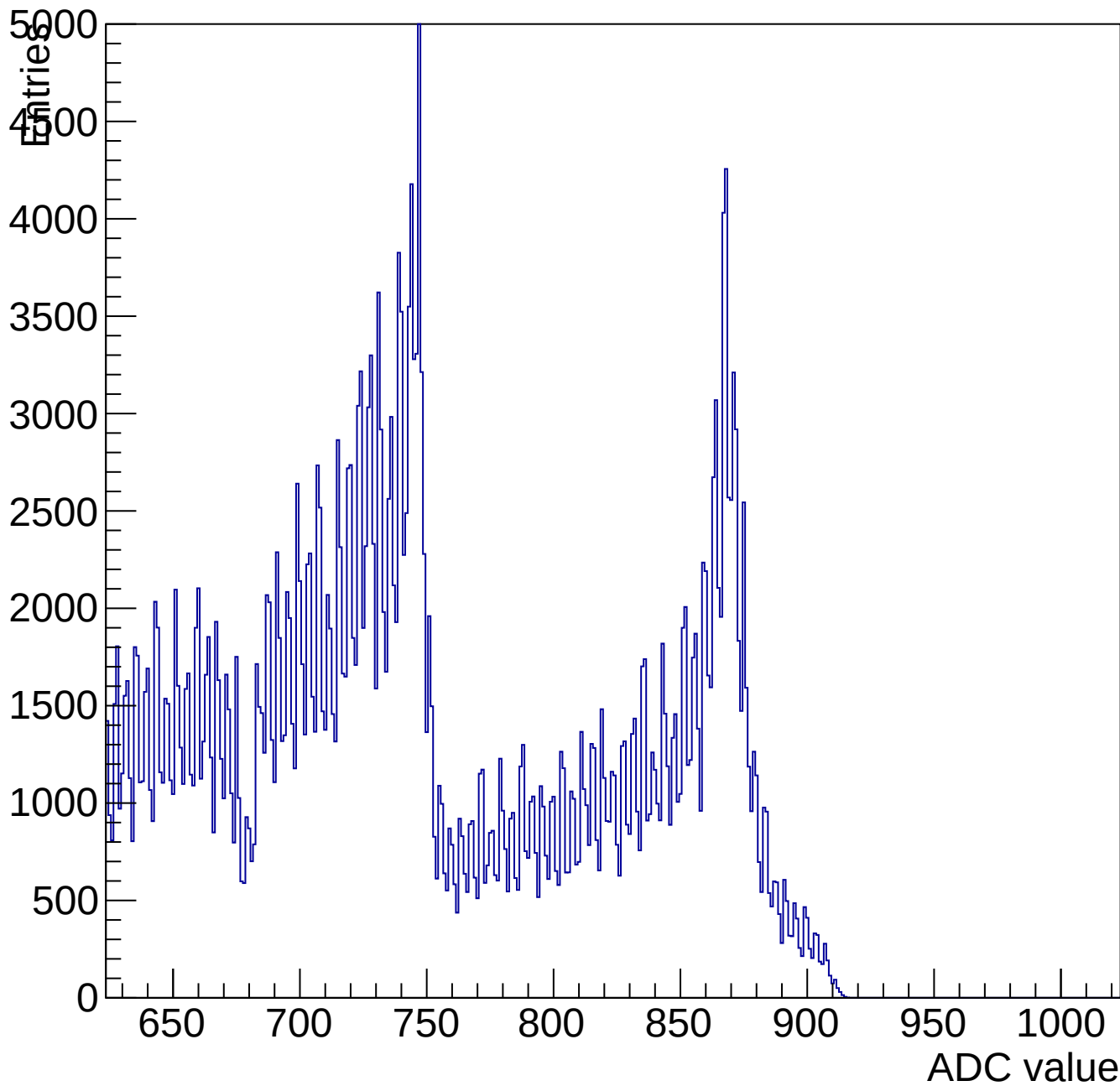
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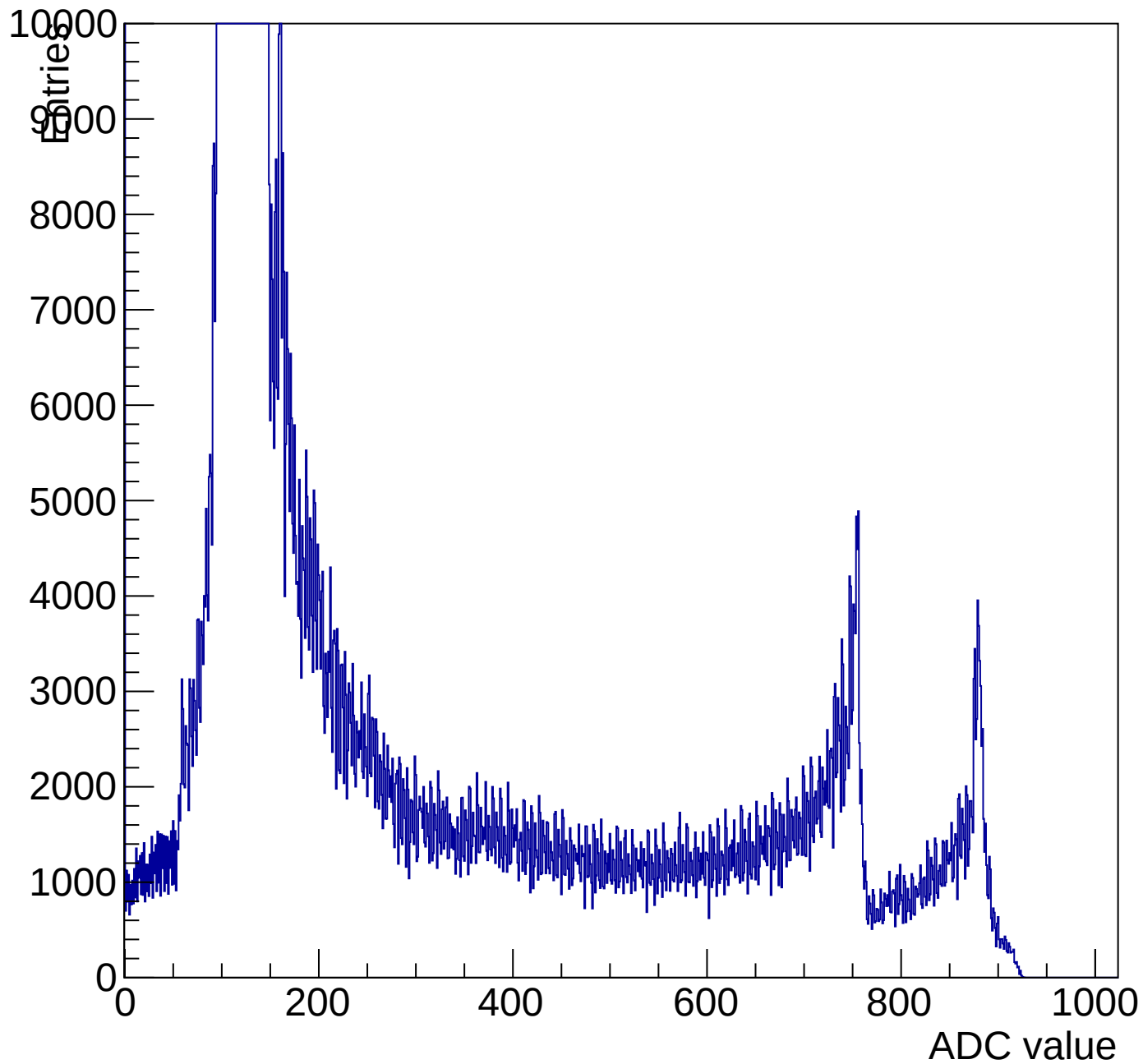
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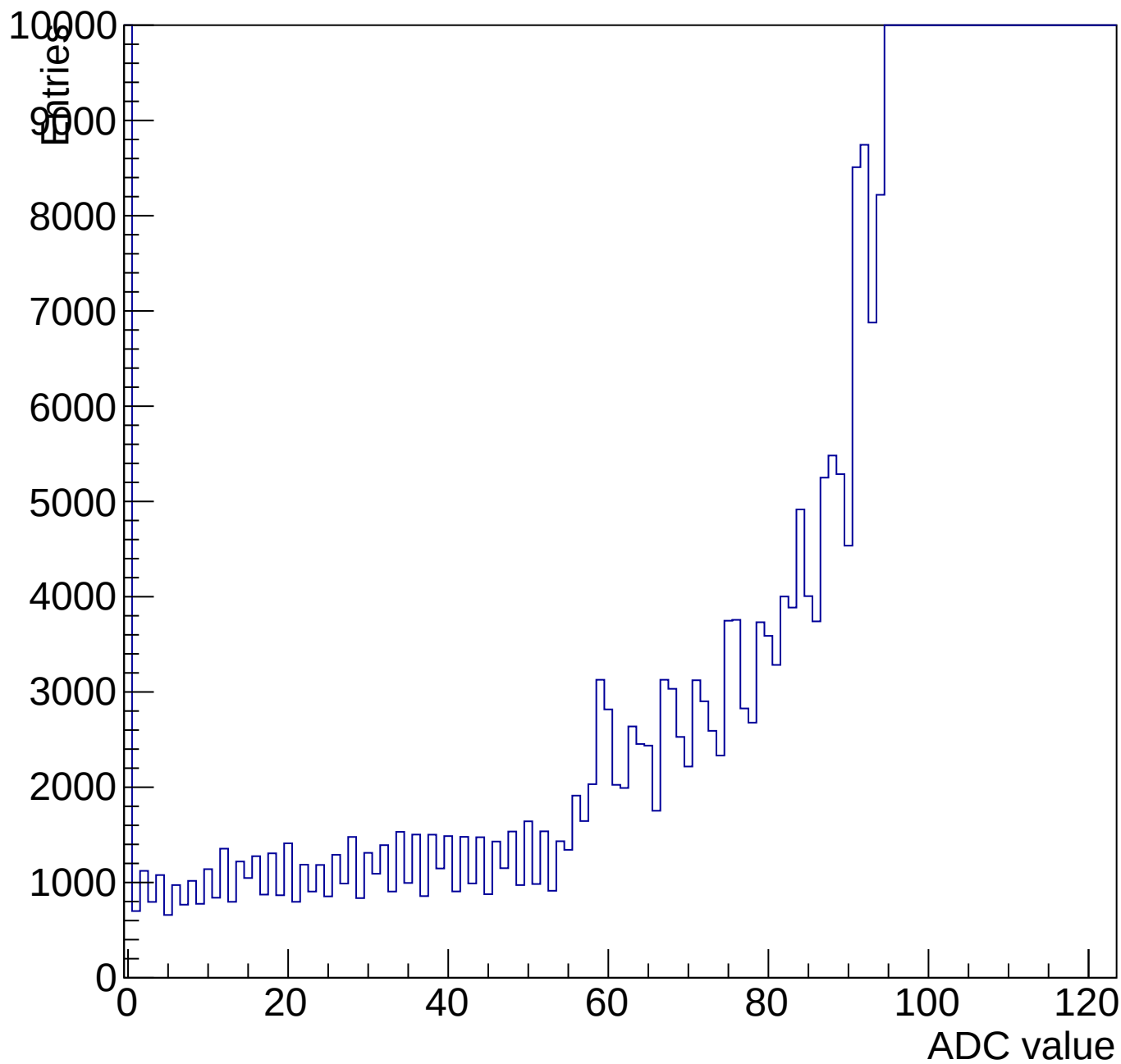
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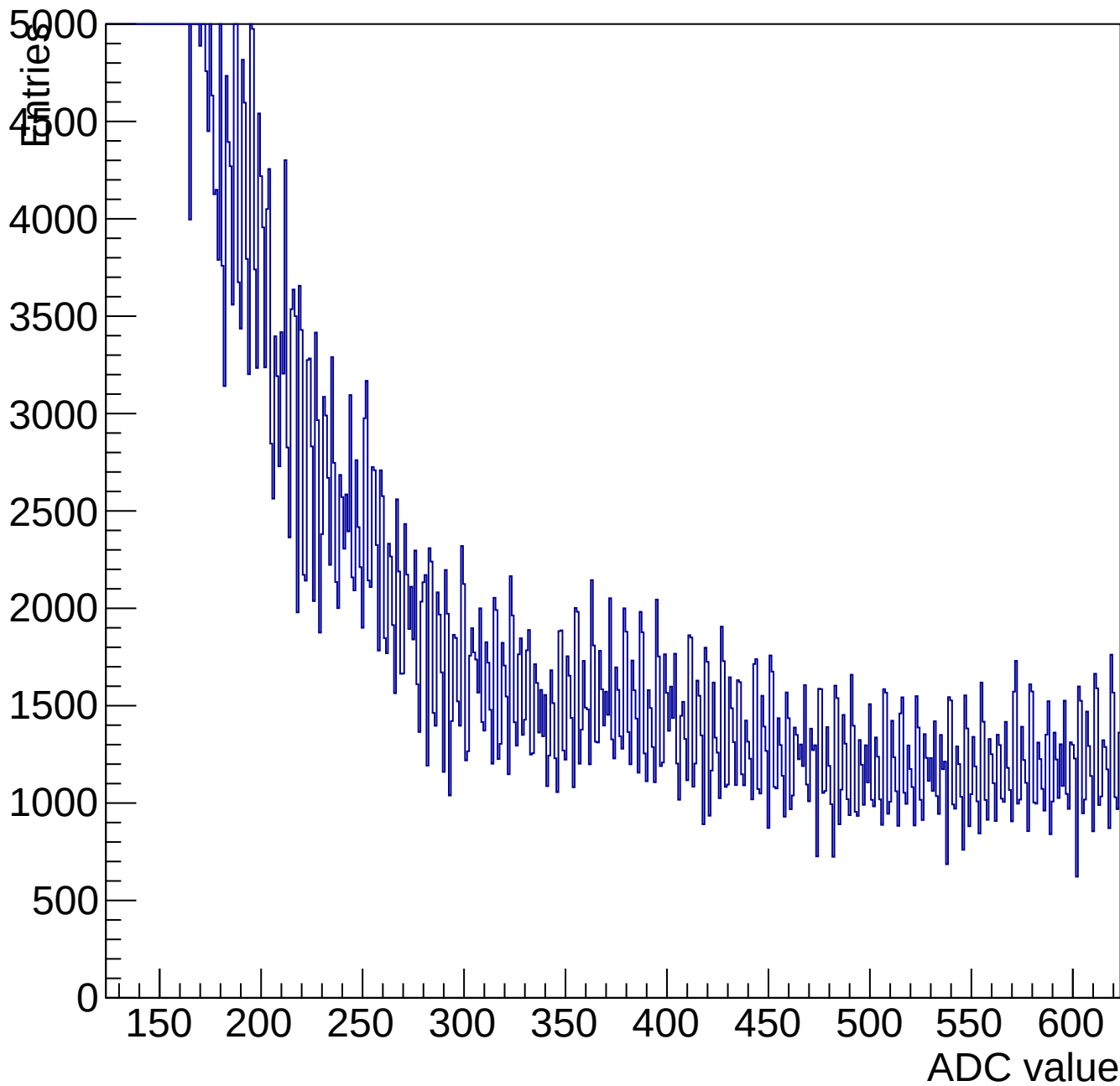
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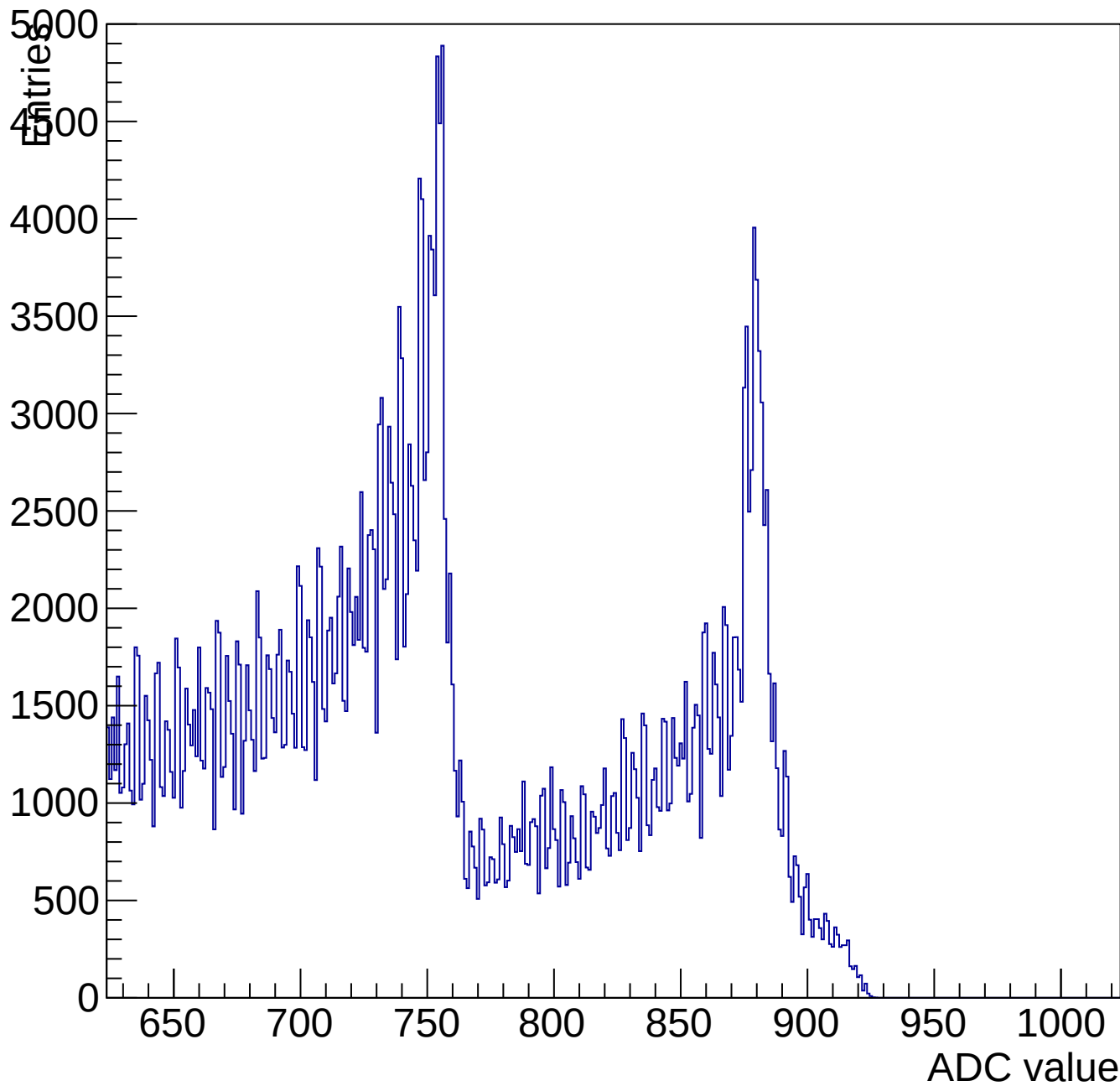
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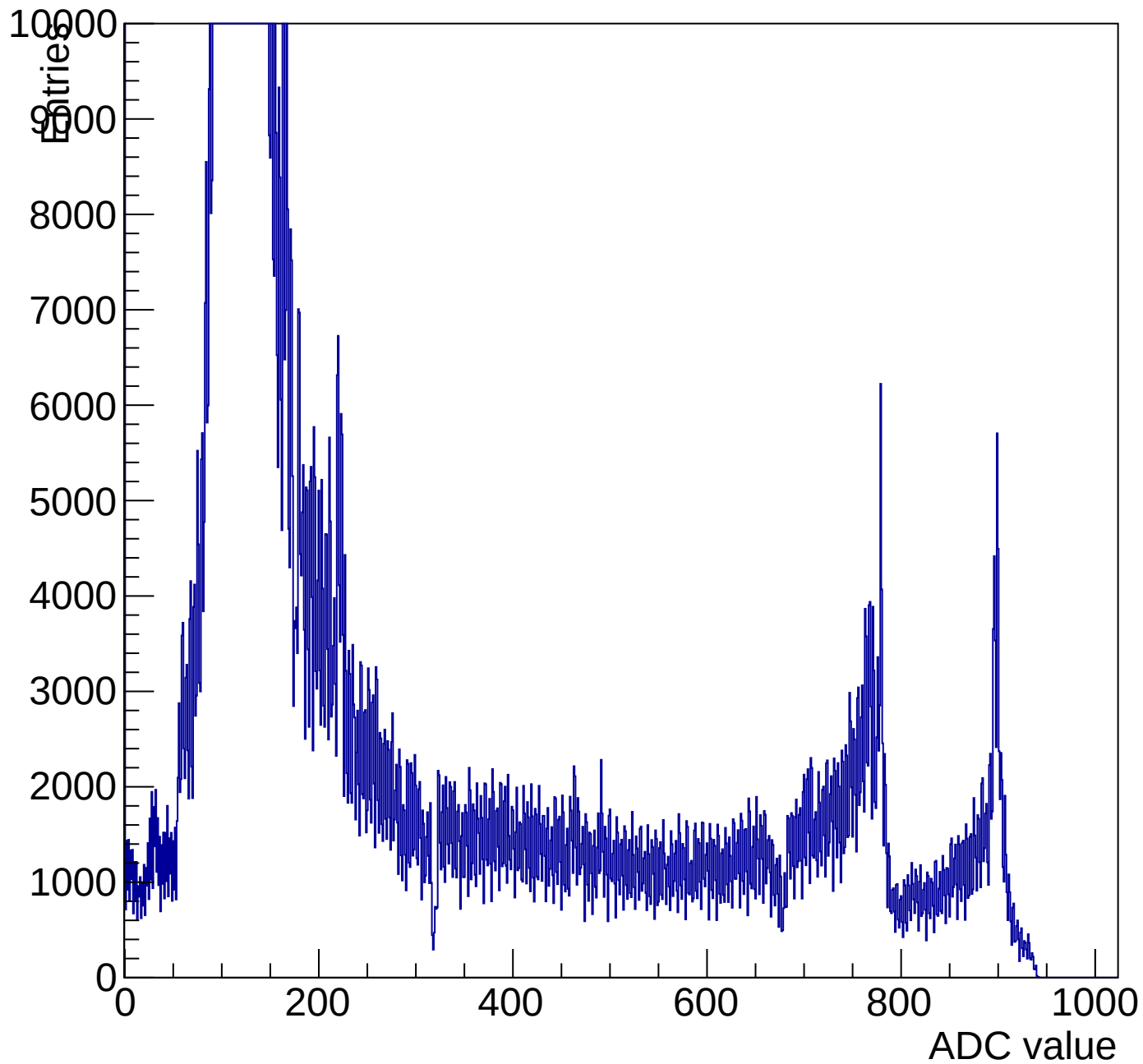
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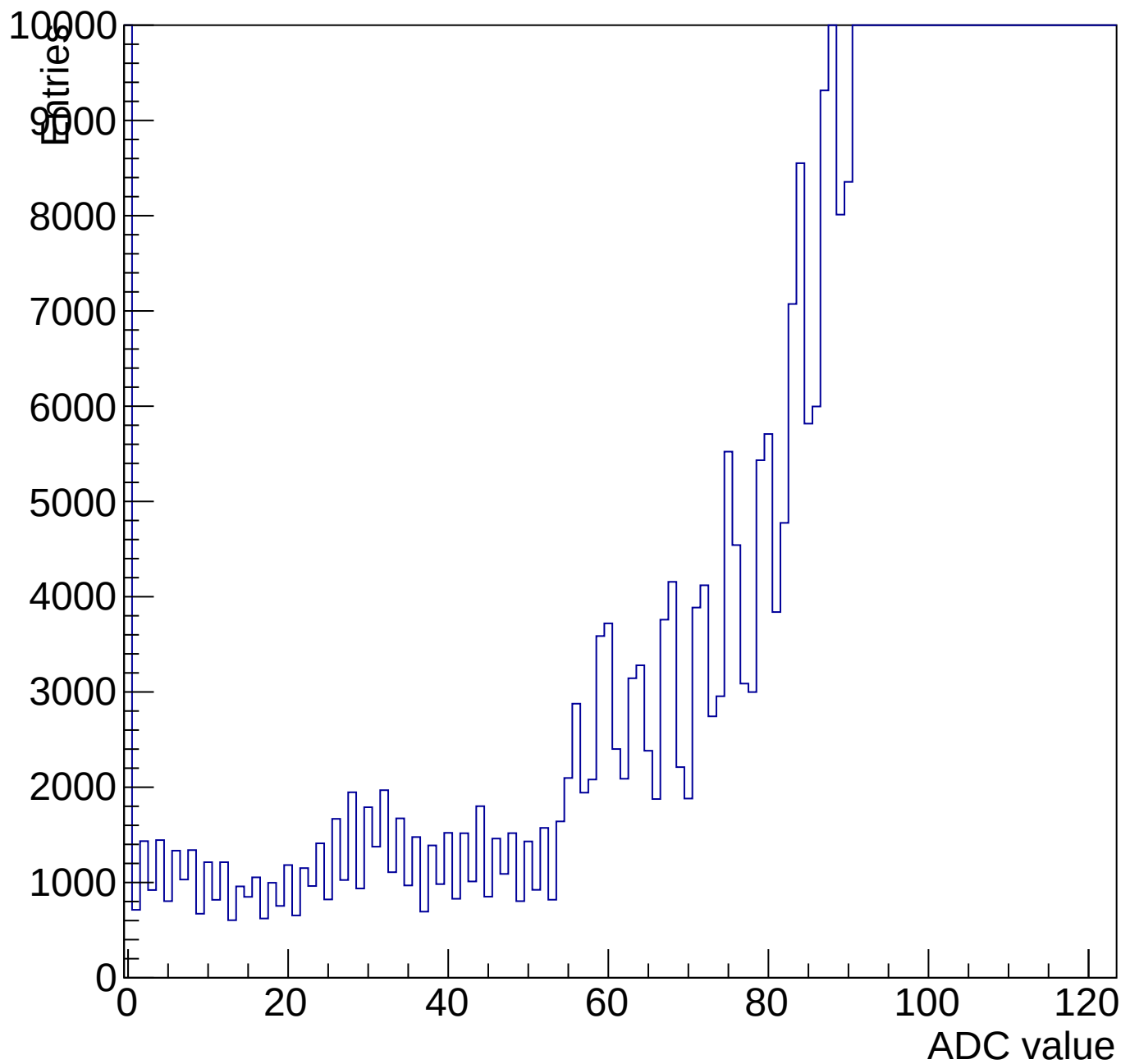
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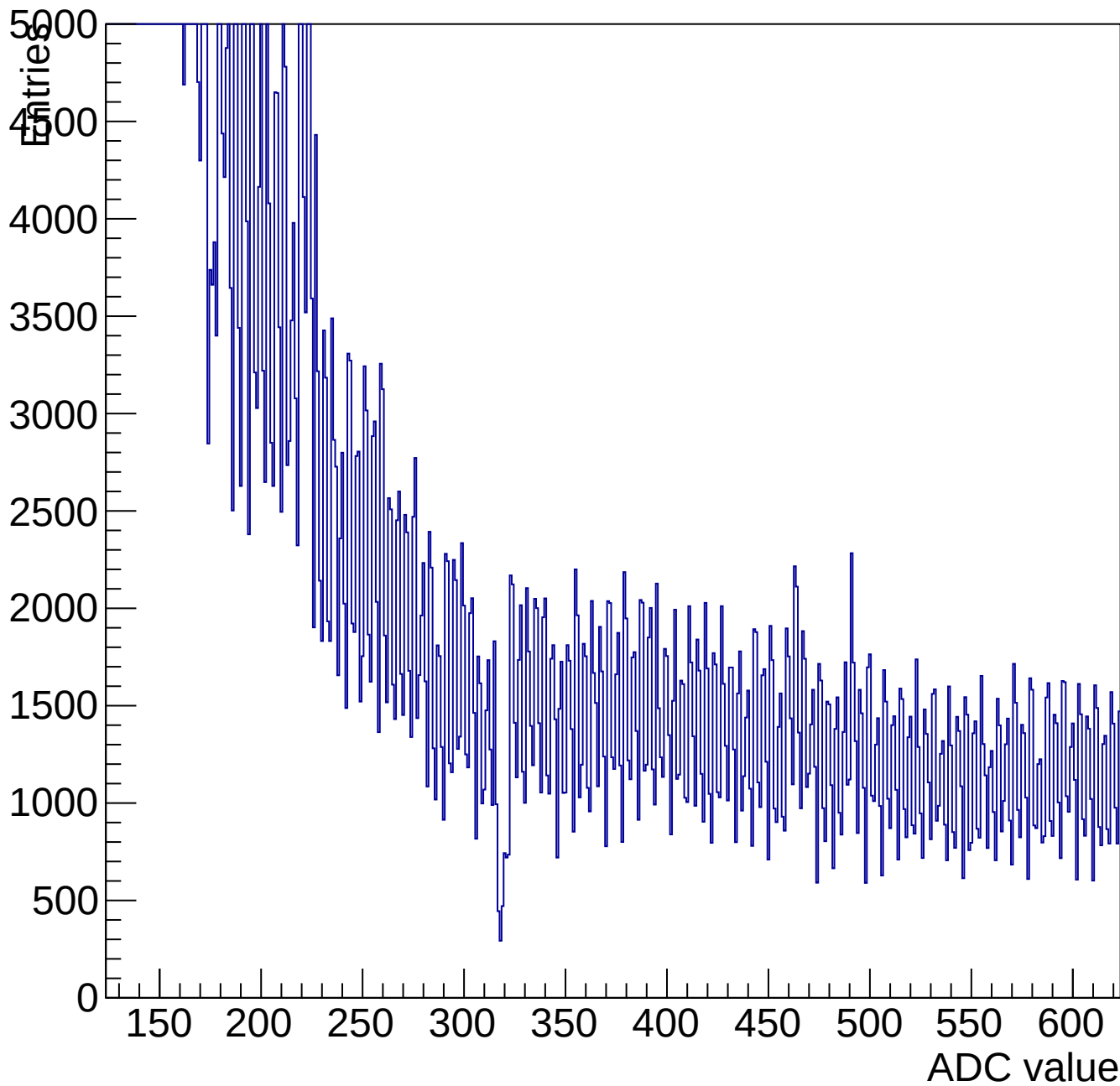
Pulse height chip 65 ch 7



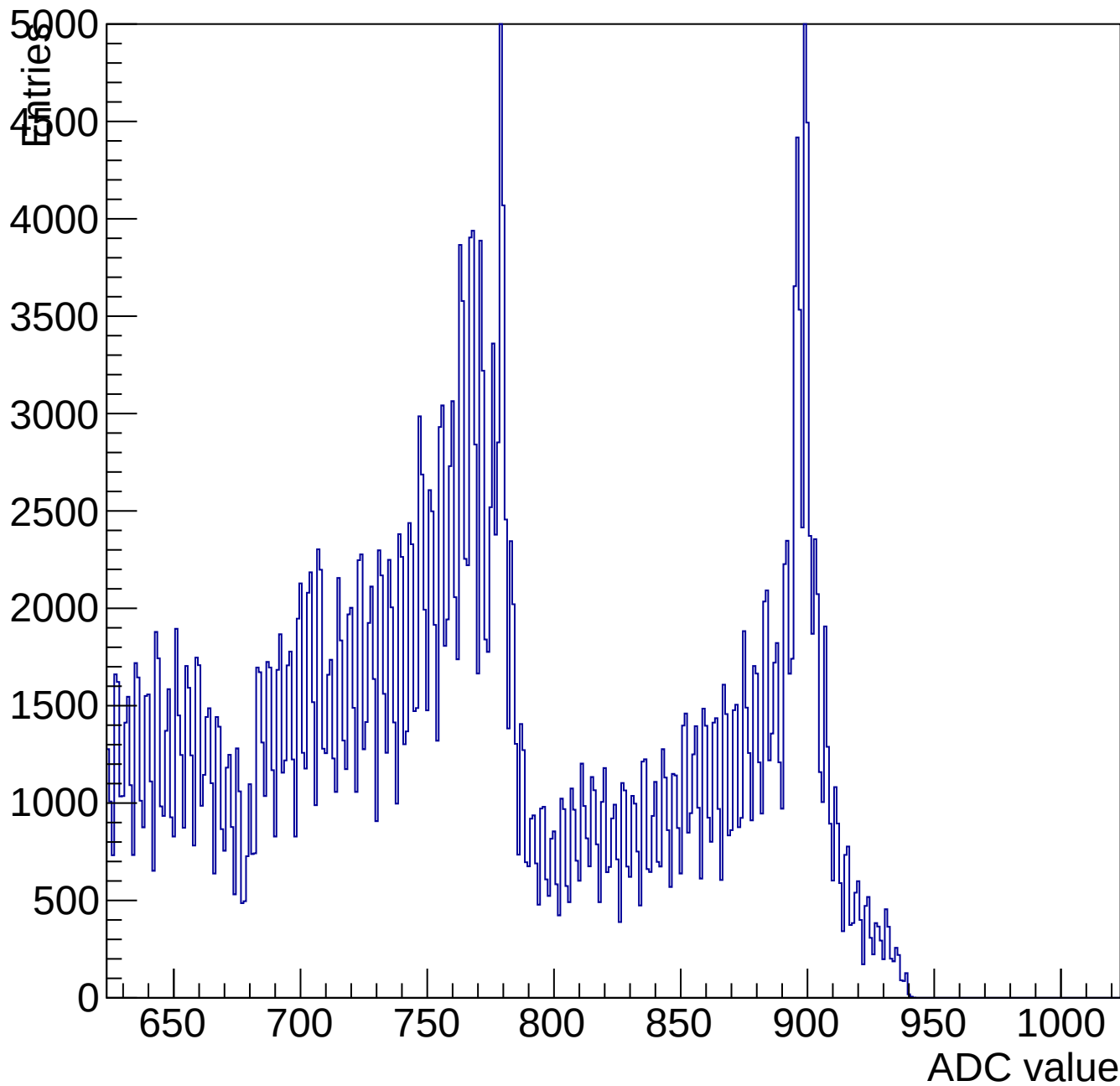
Pulse height chip 65 ch 7



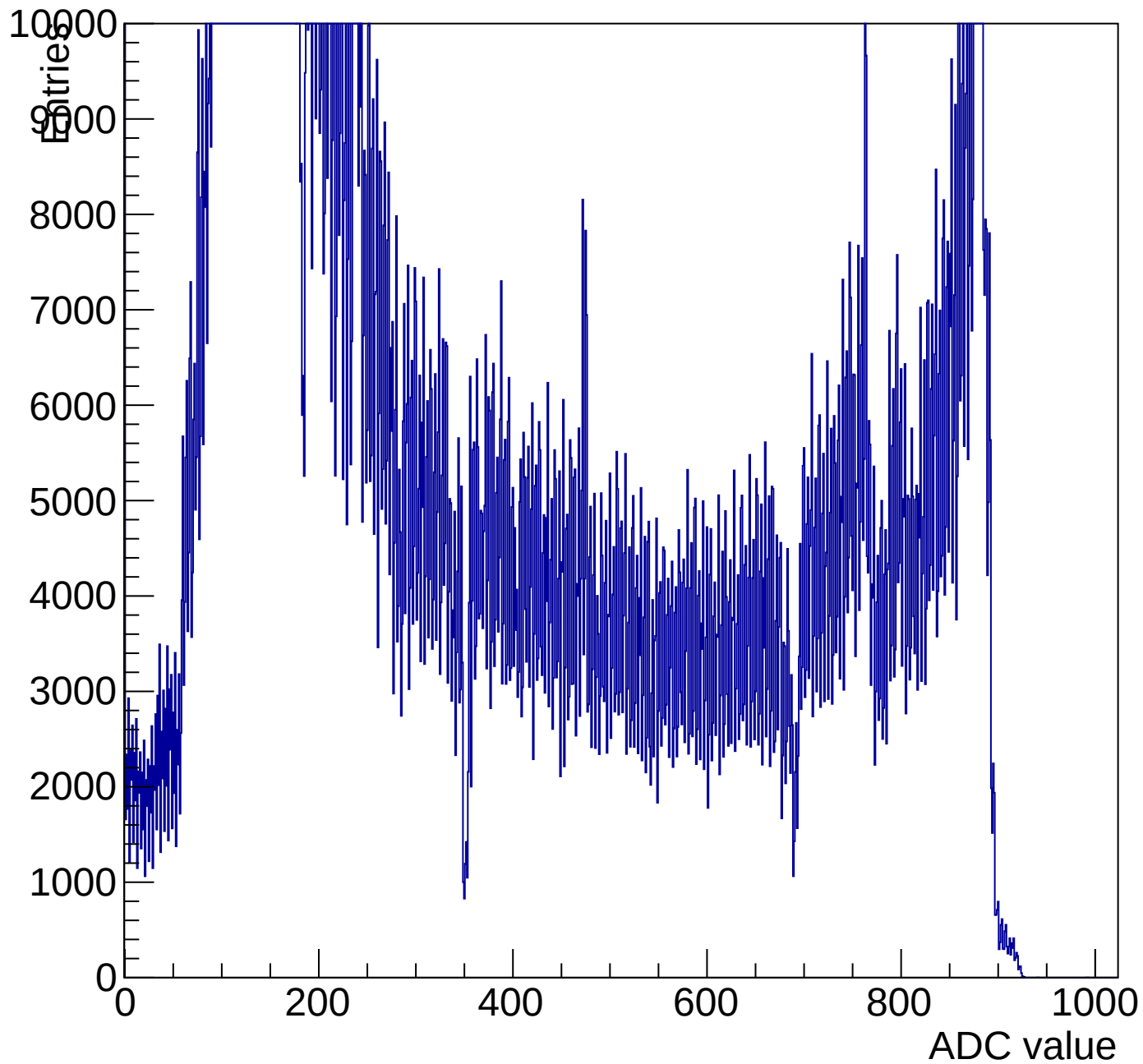
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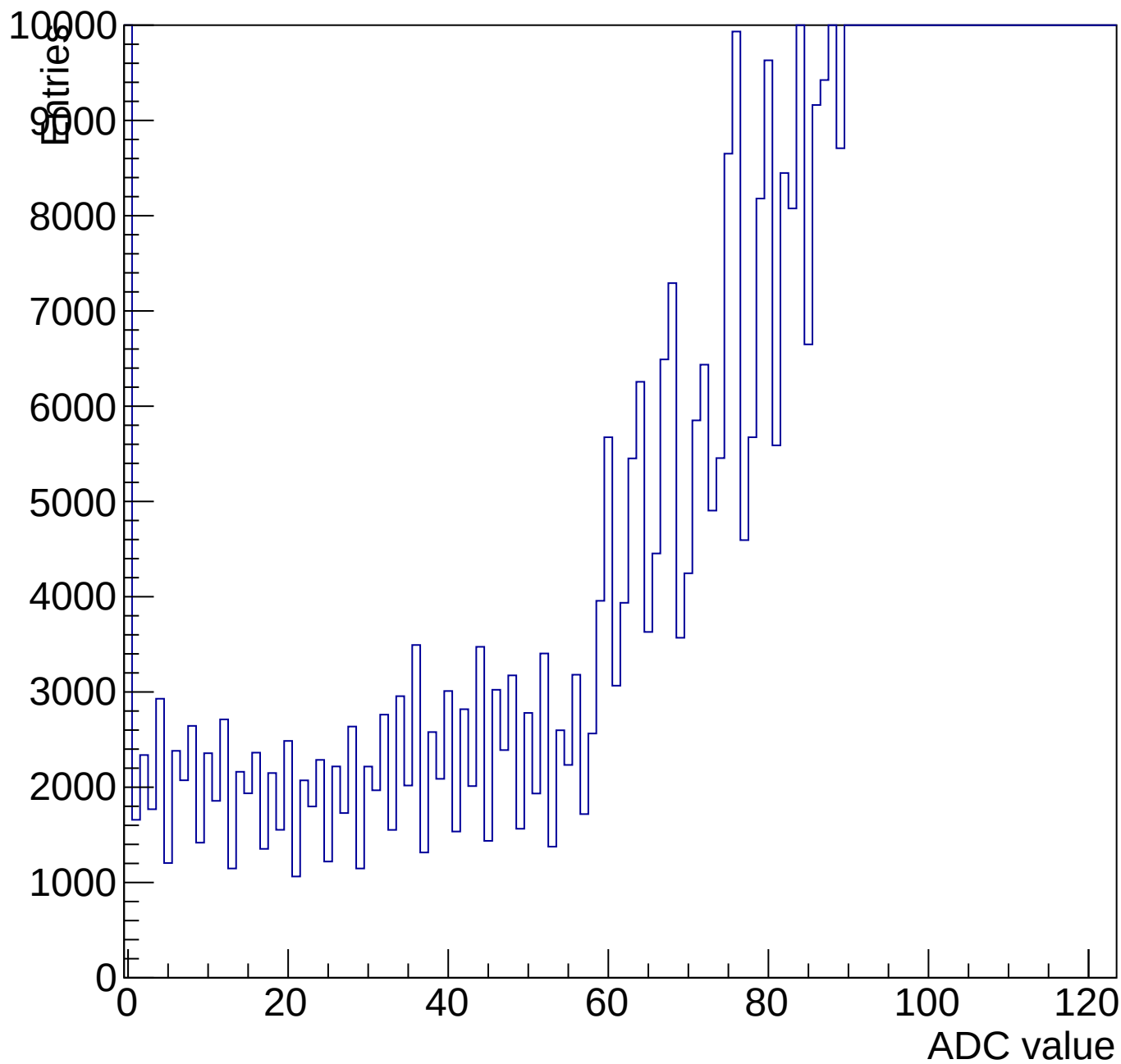
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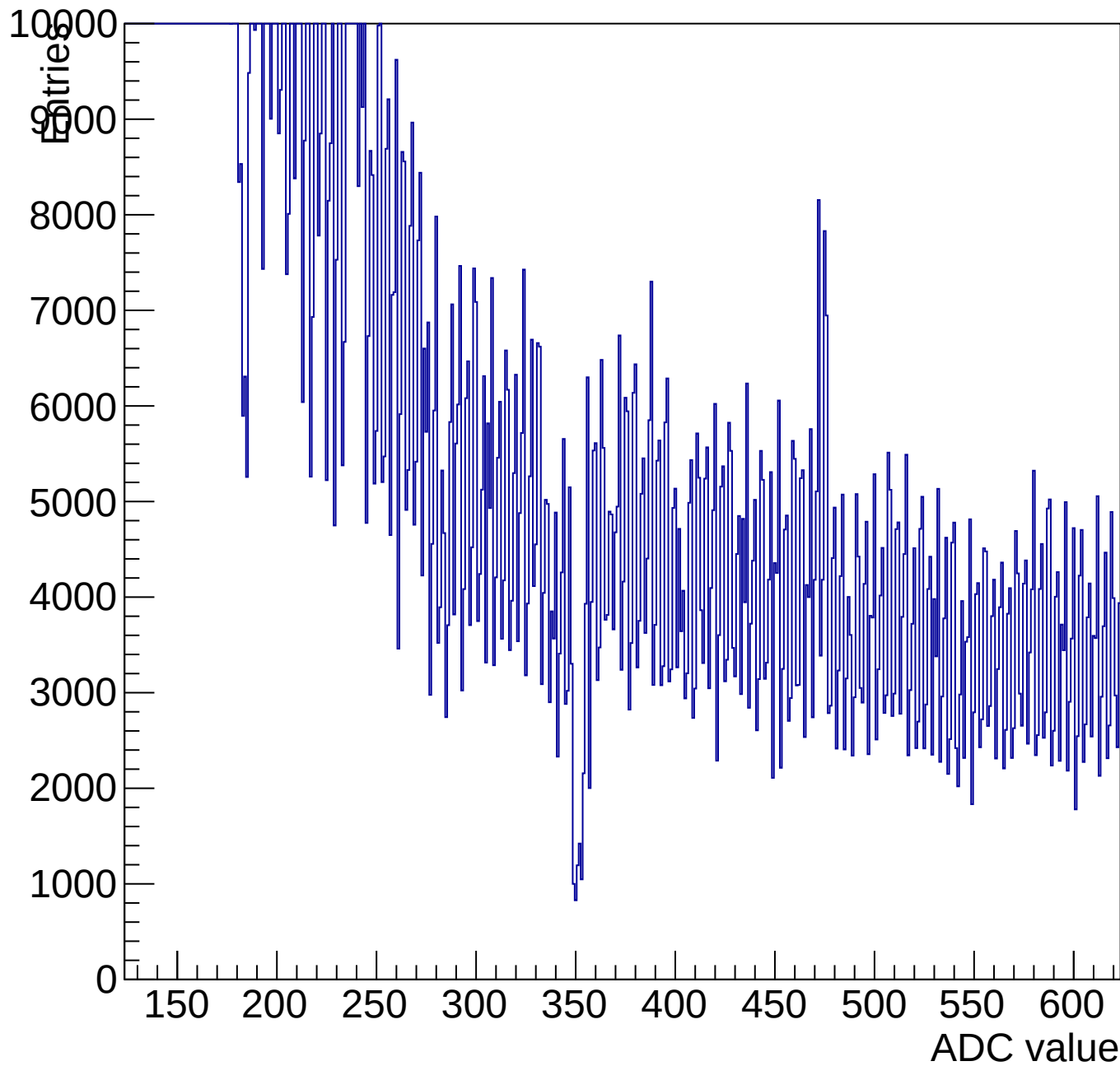
Pulse height chip 65 ch 8



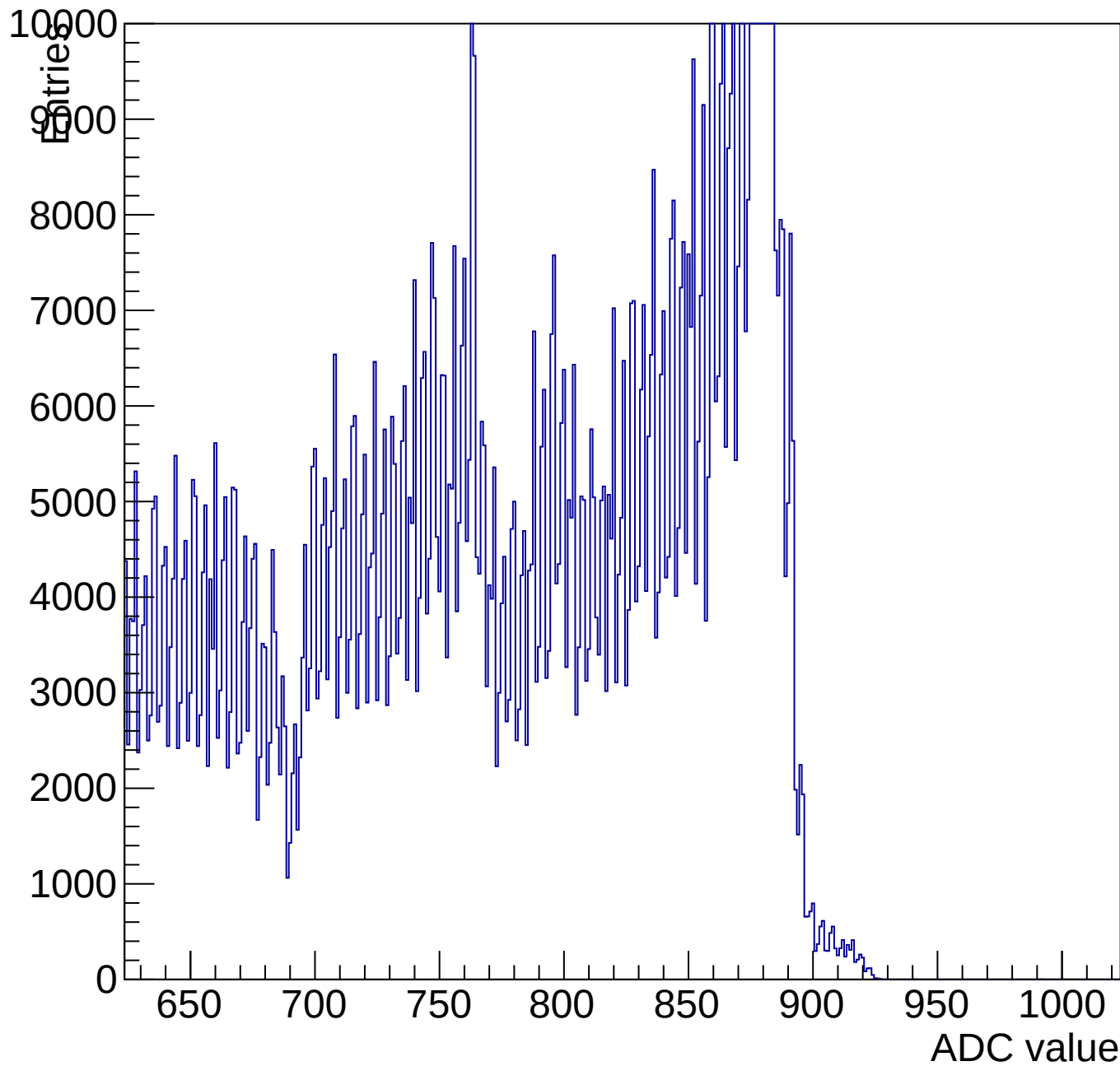
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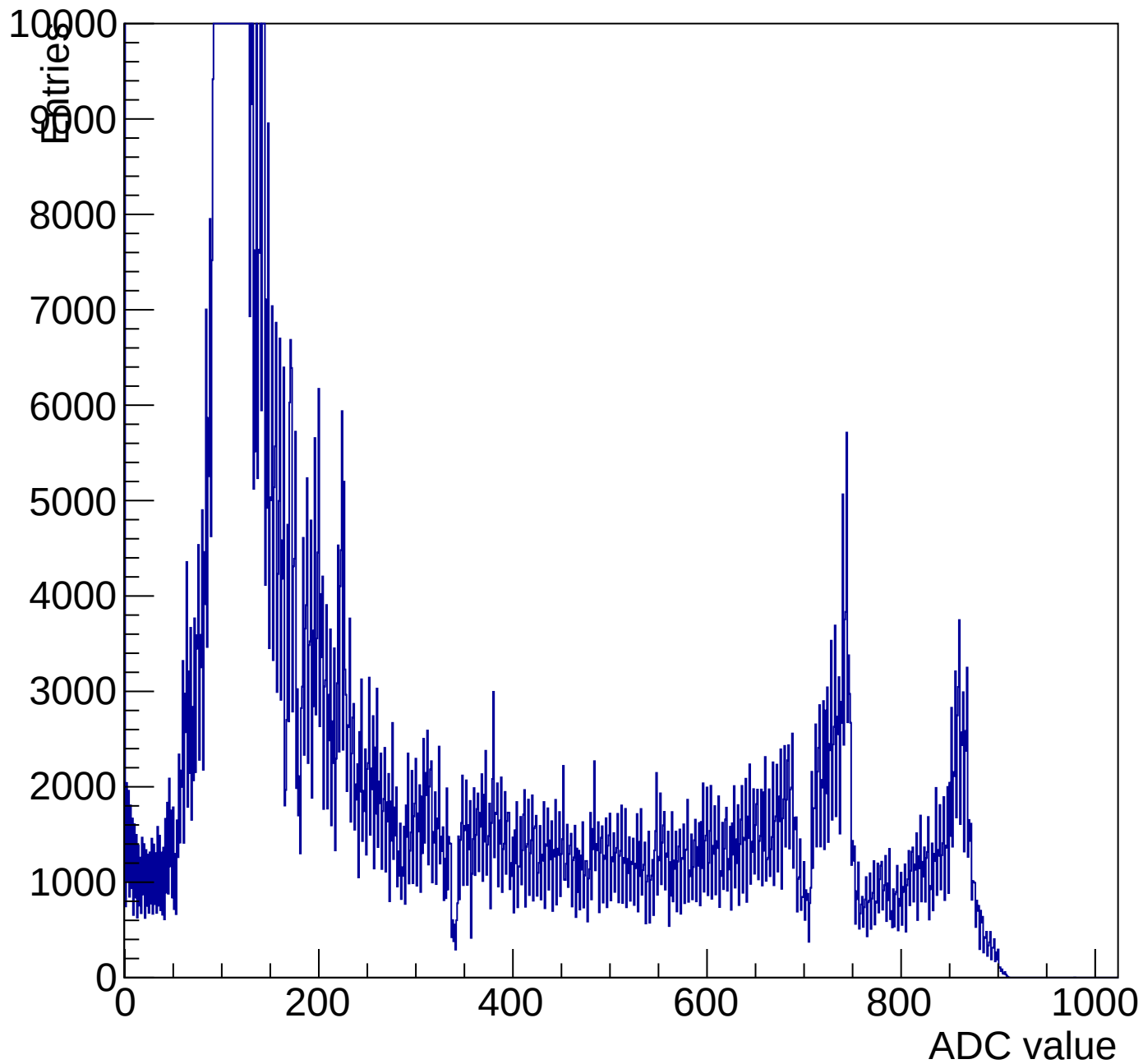
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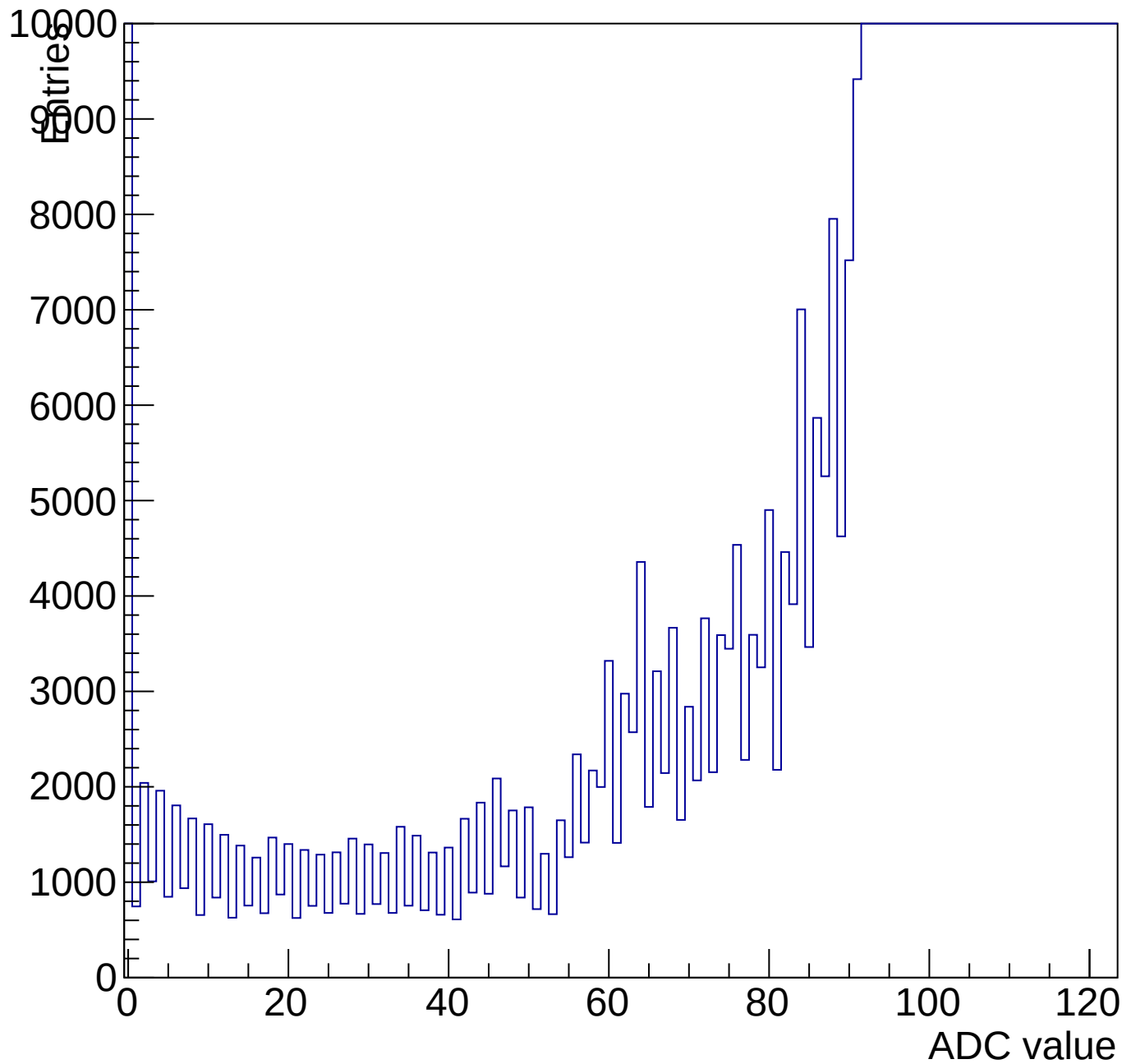
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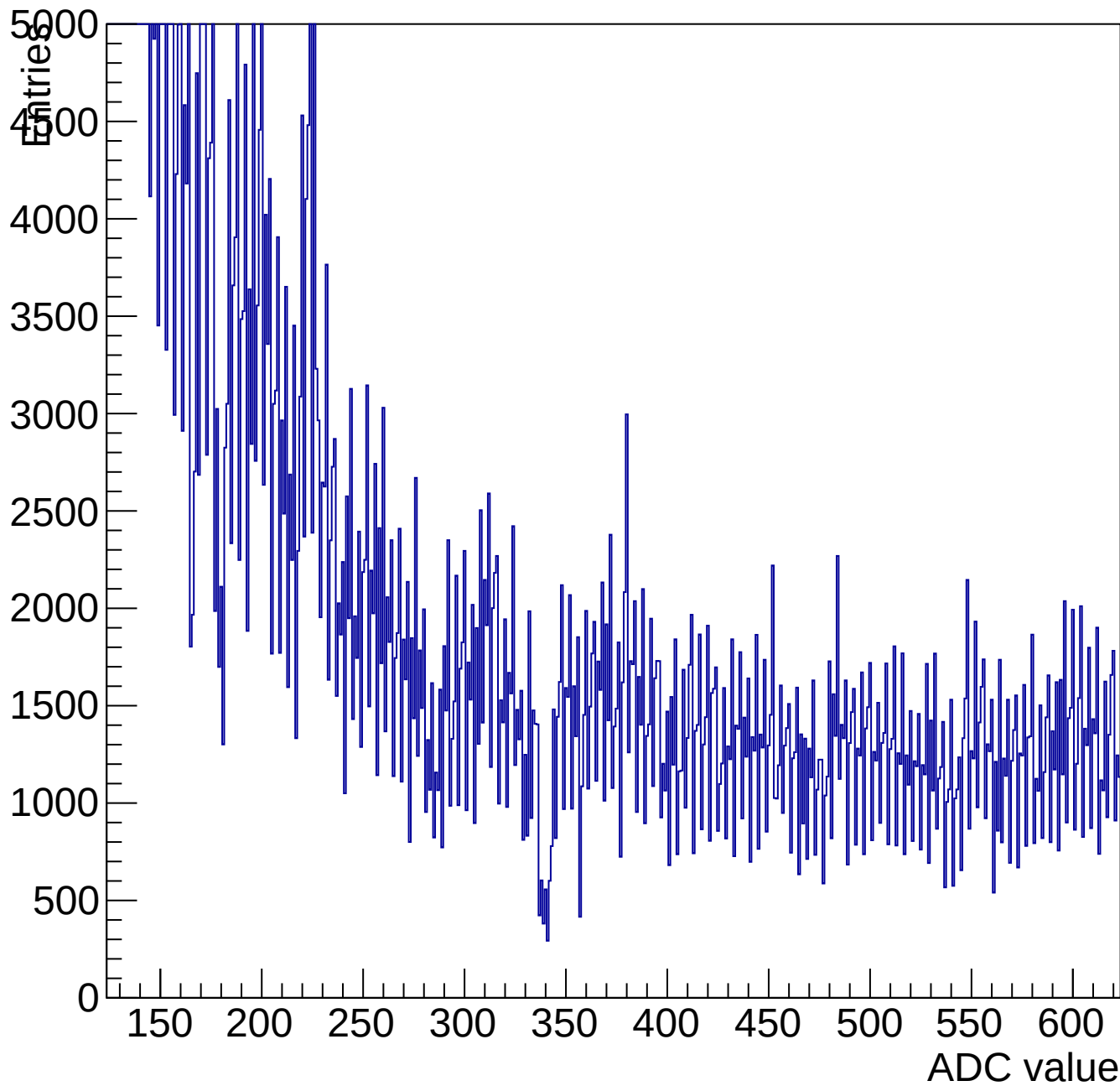
Pulse height chip 65 ch 9



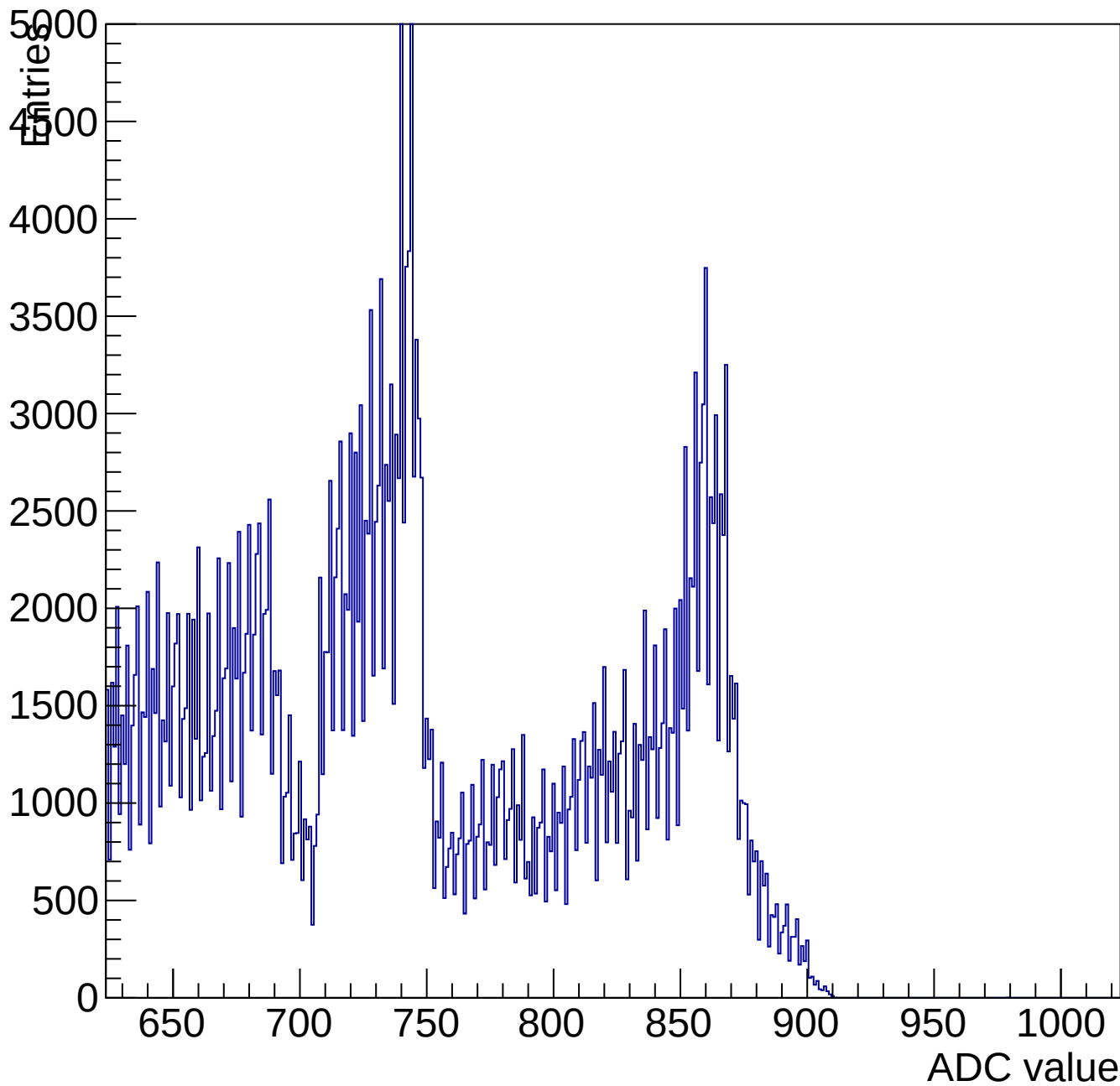
Pulse height chip 65 ch 9



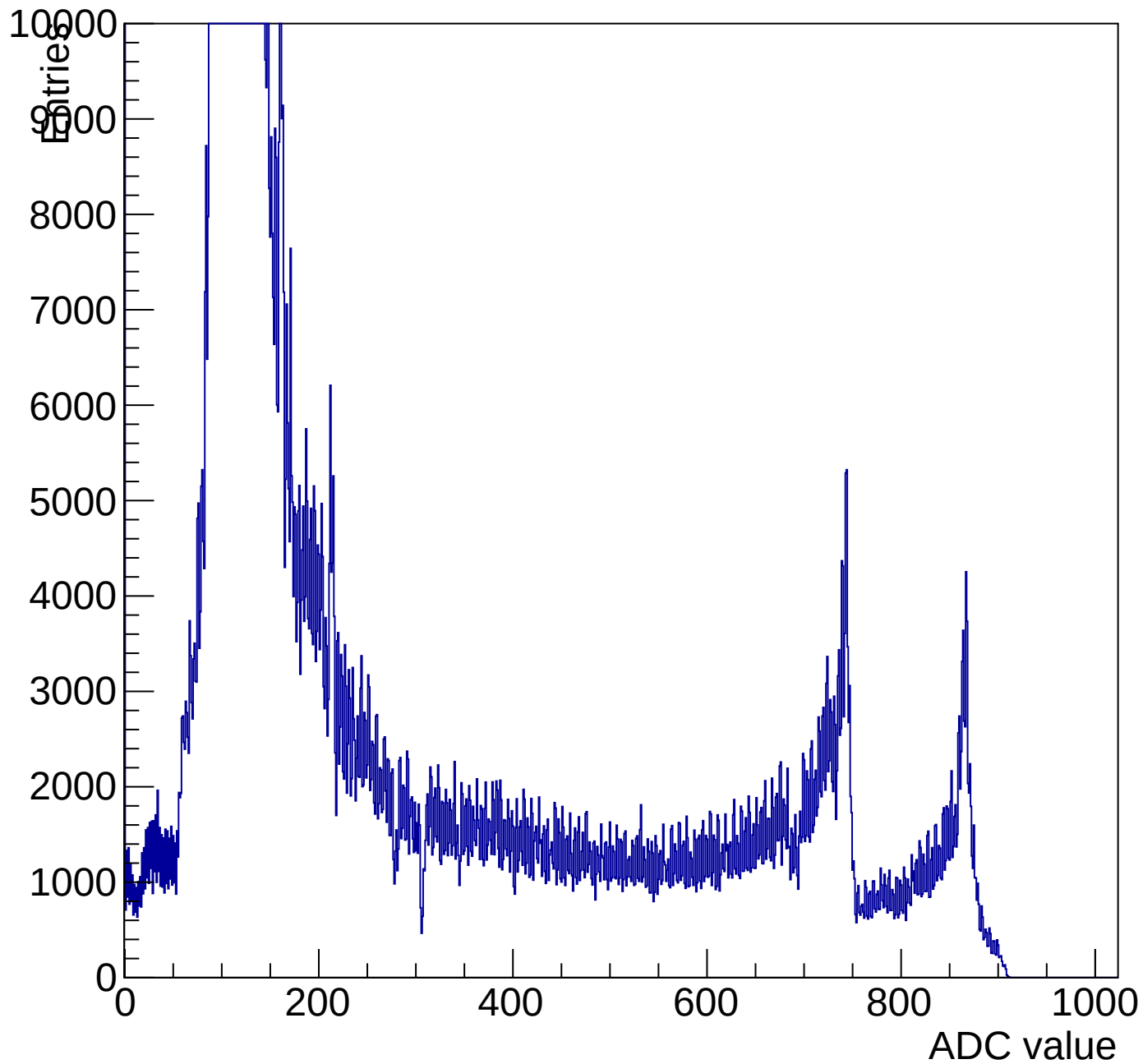
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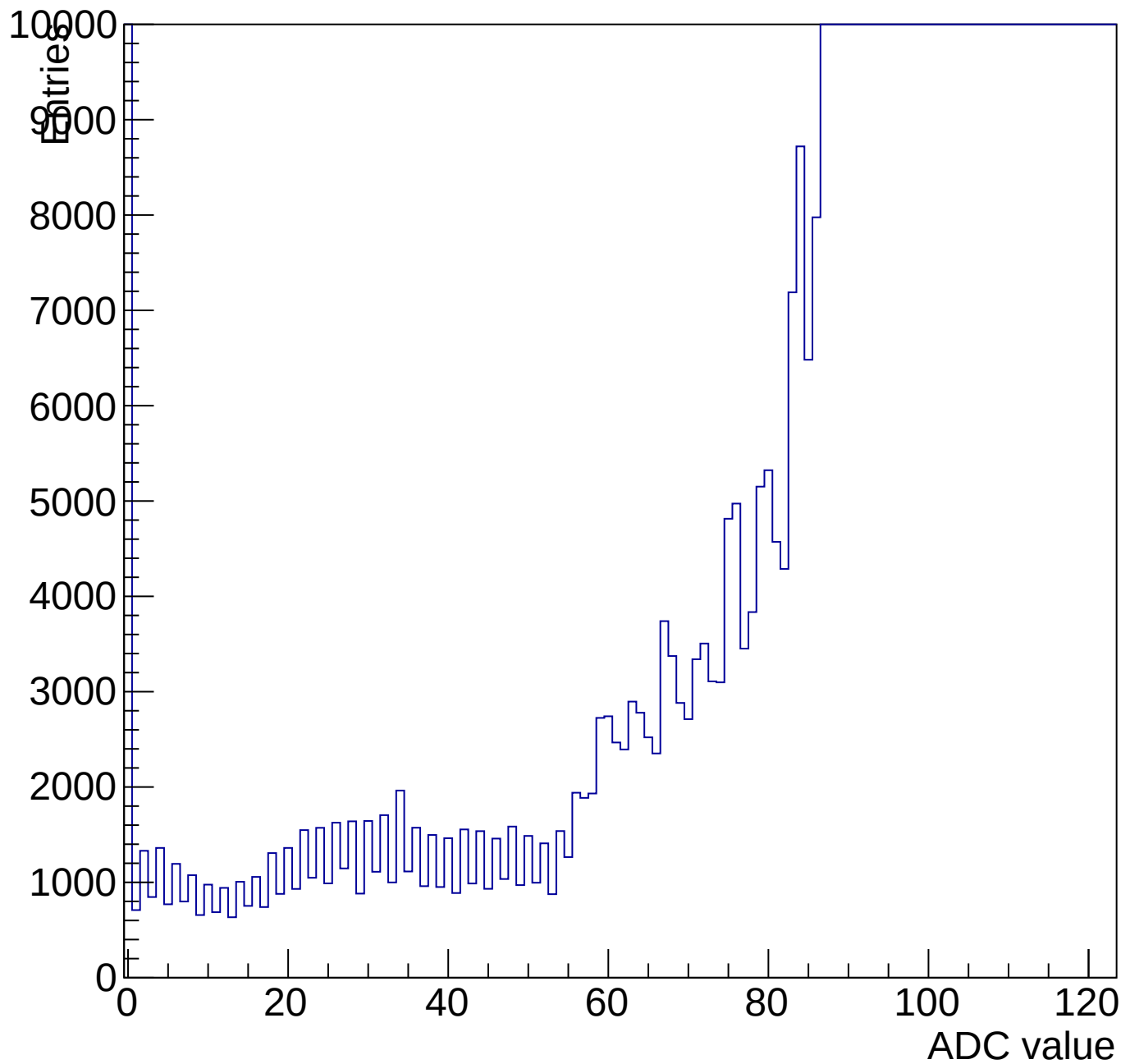
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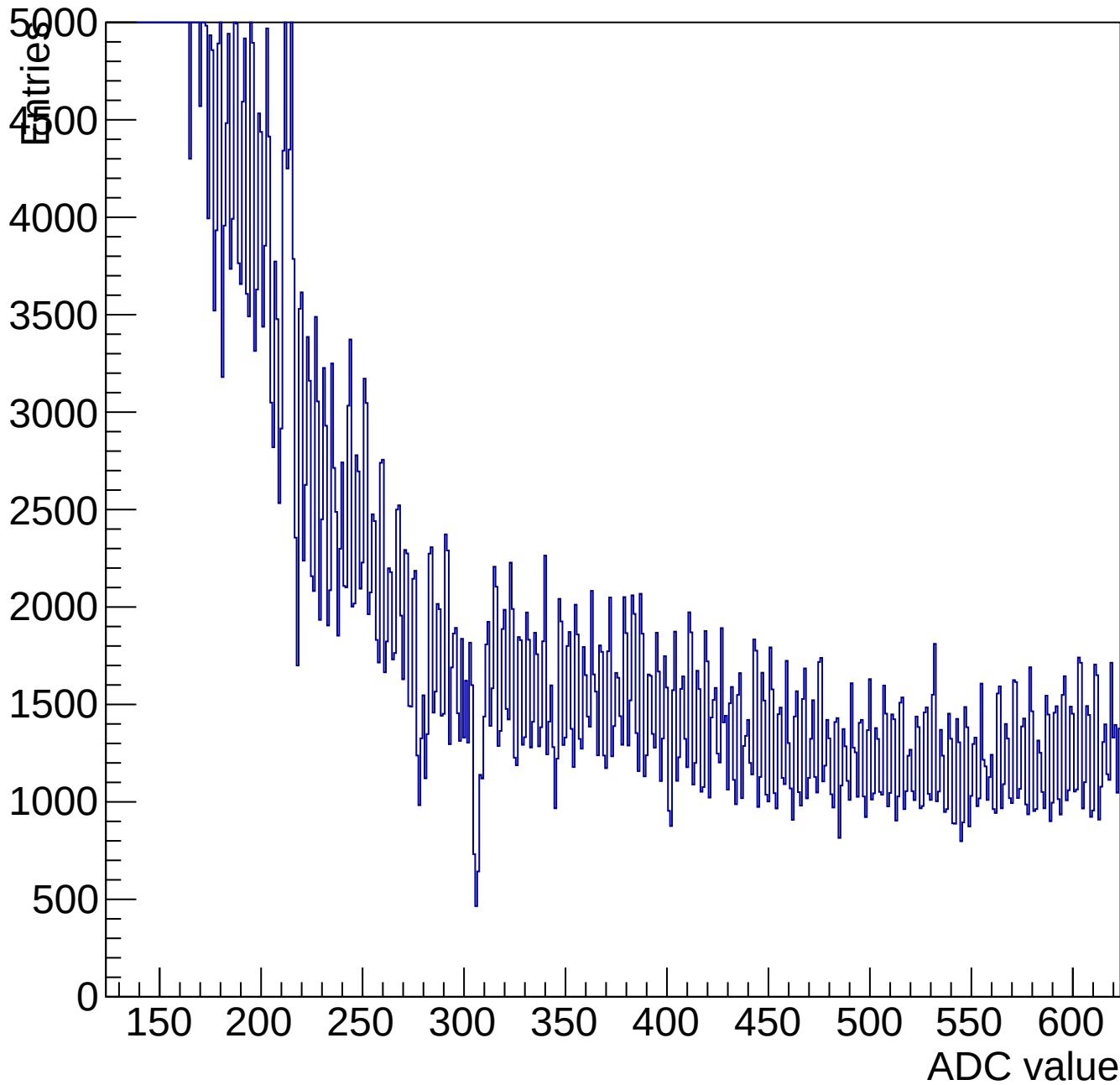
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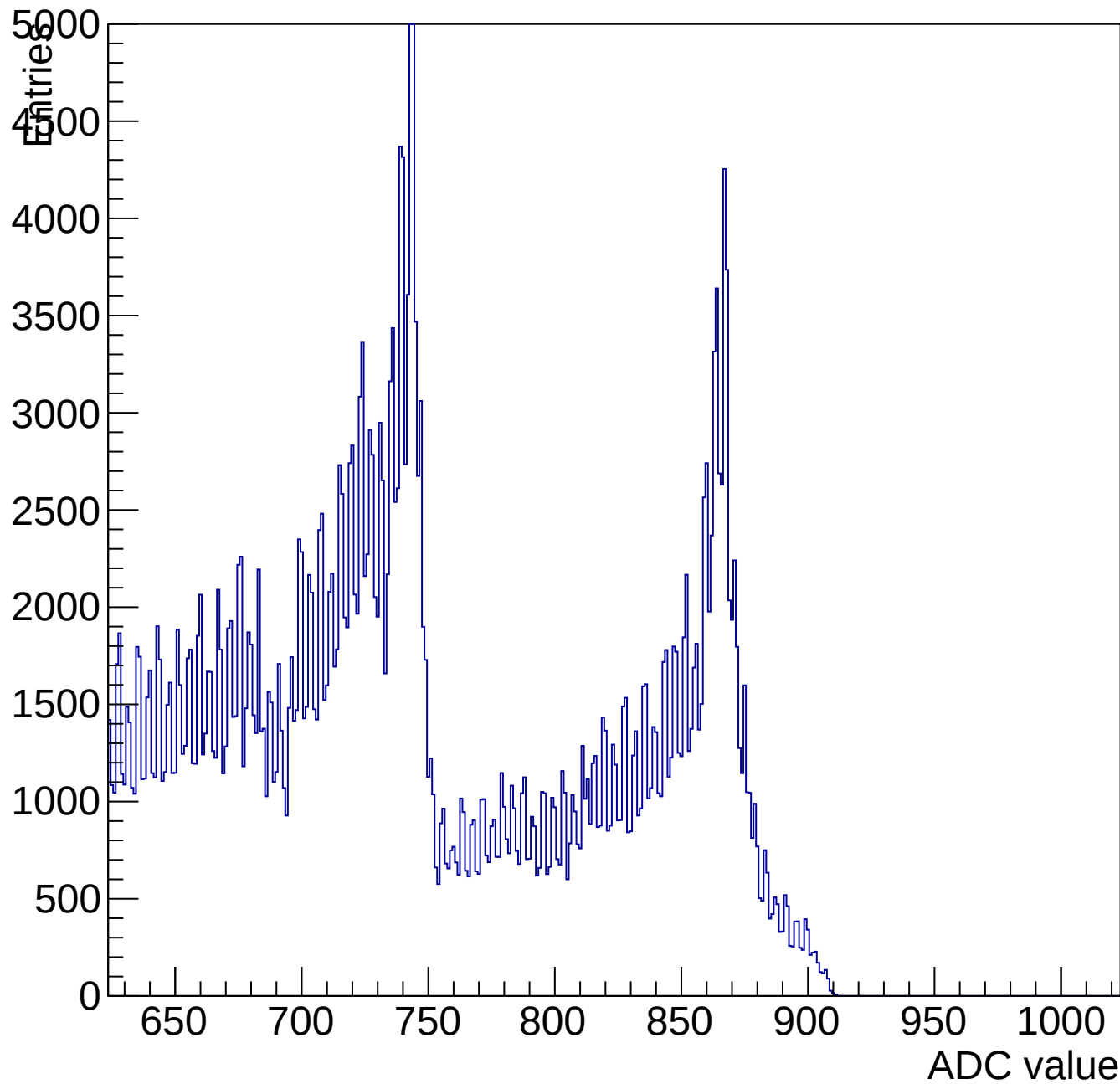
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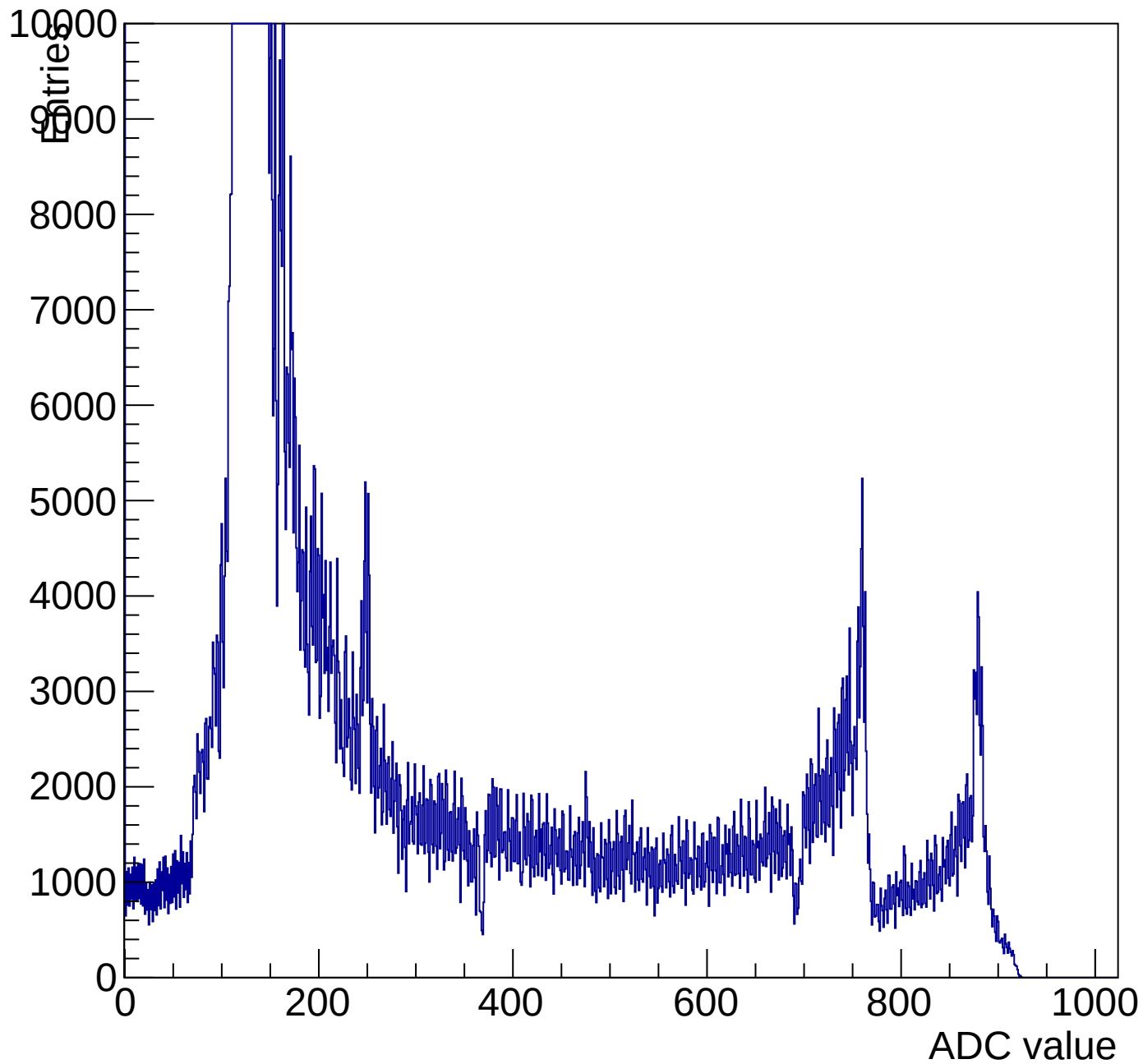
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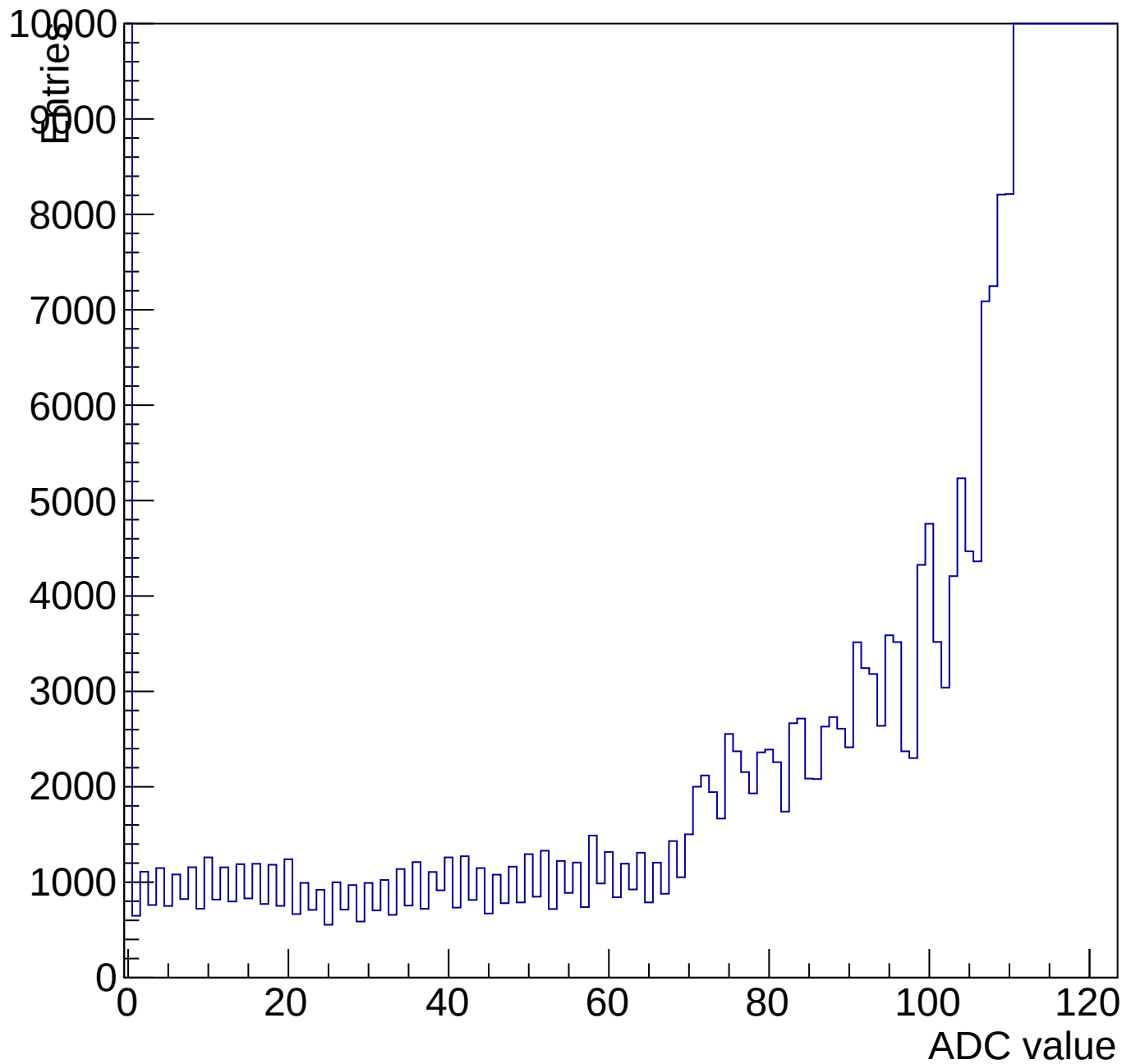
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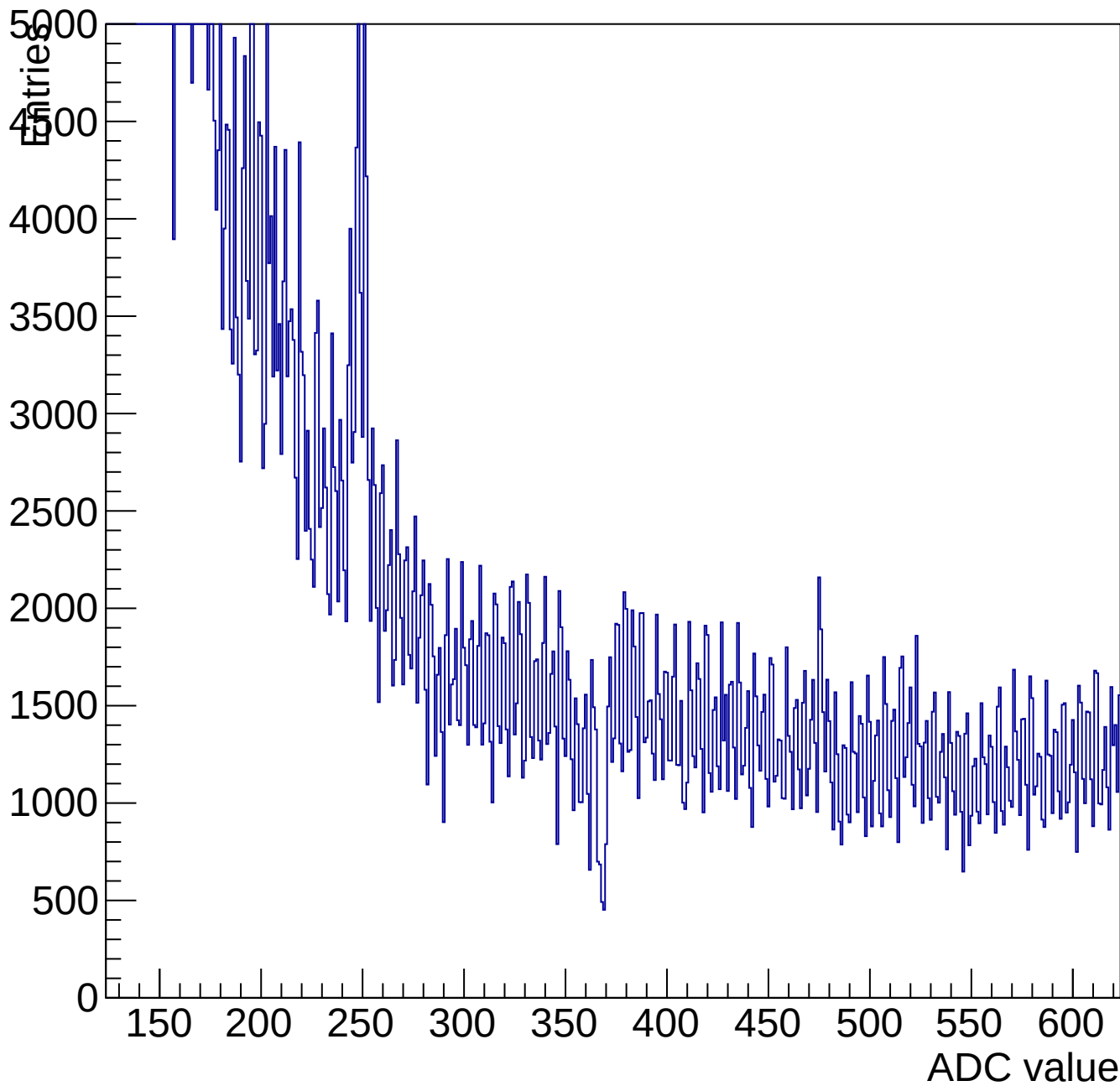
Pulse height chip 65 ch 11



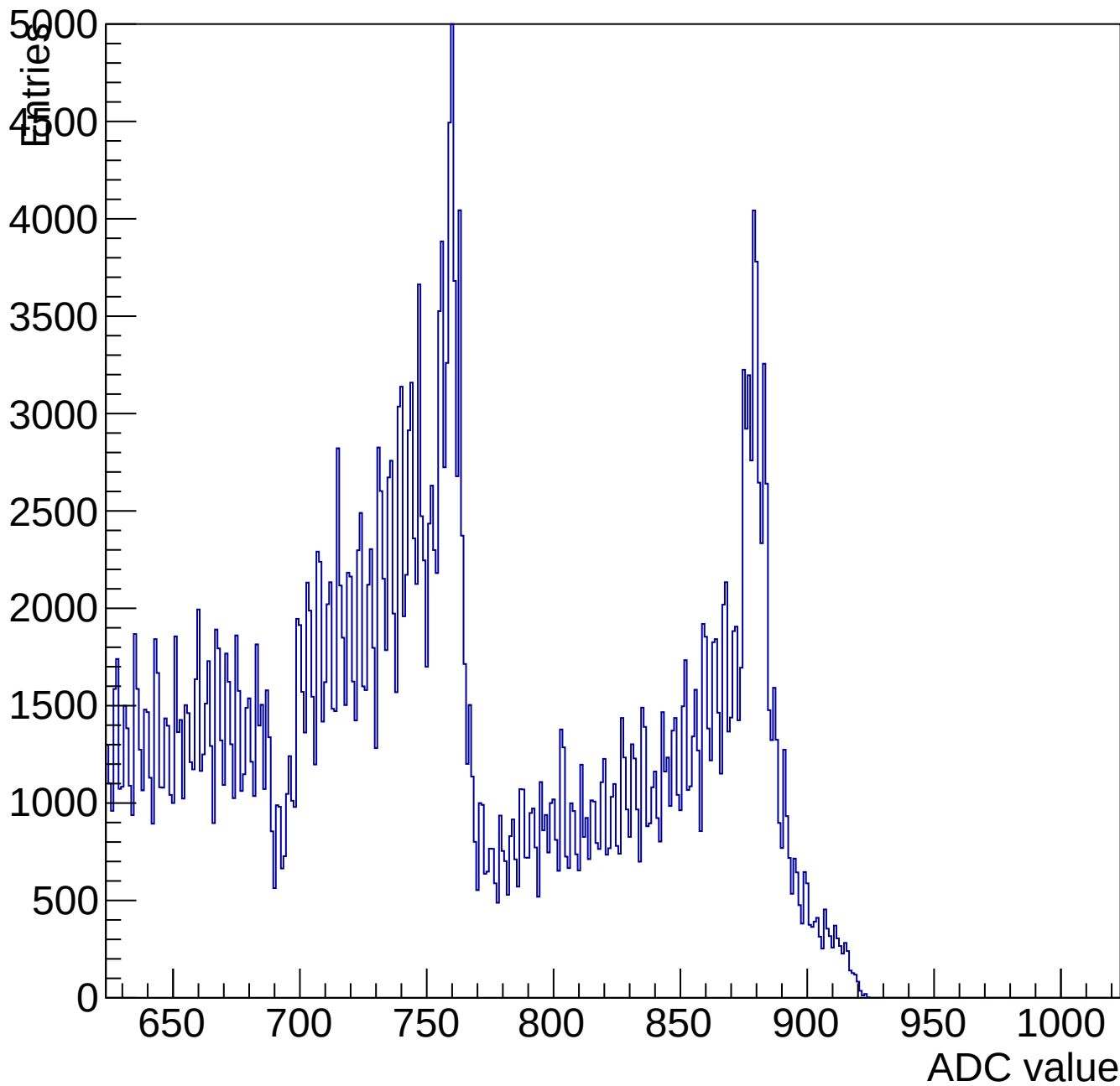
Pulse height chip 65 ch 11



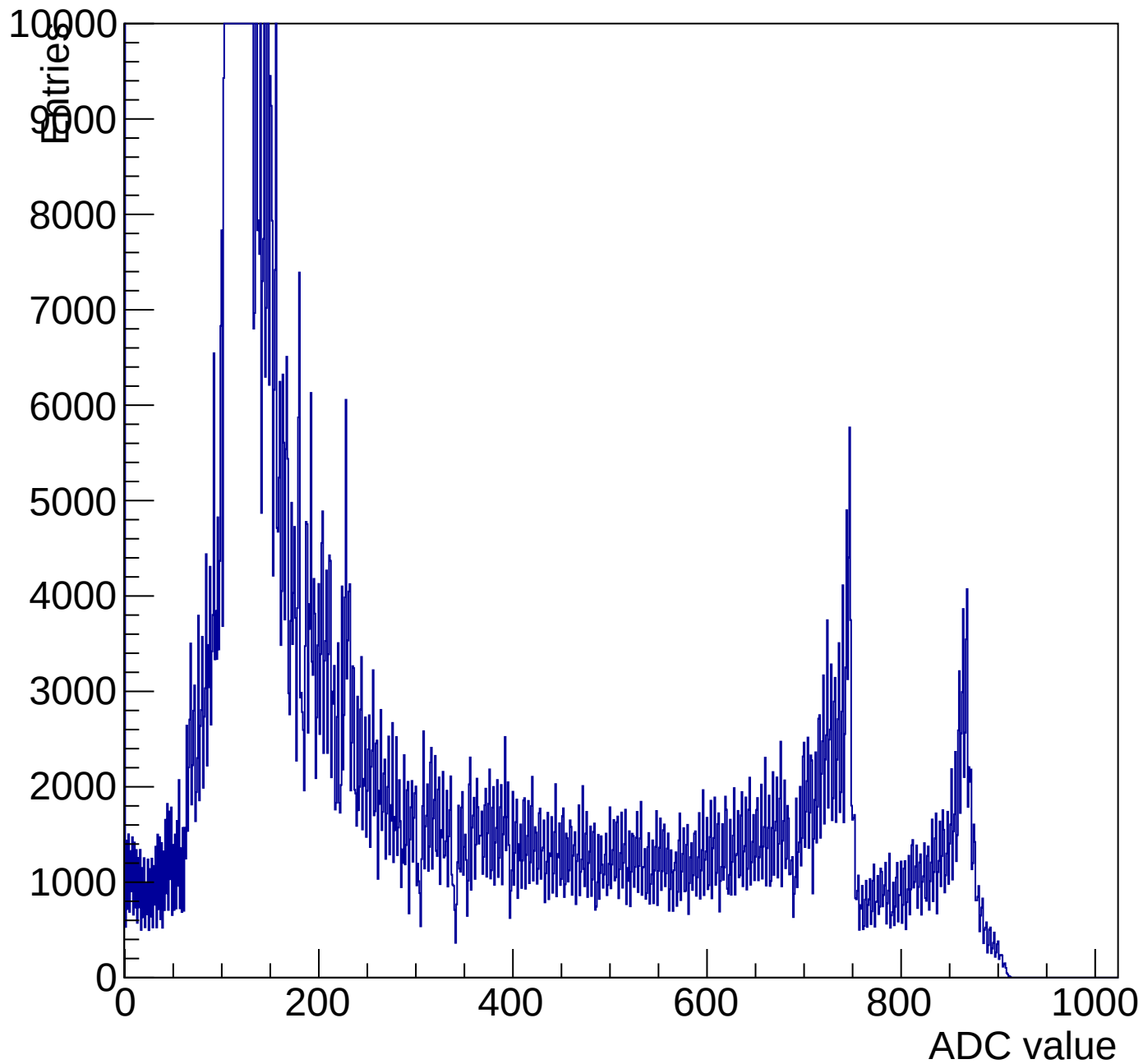
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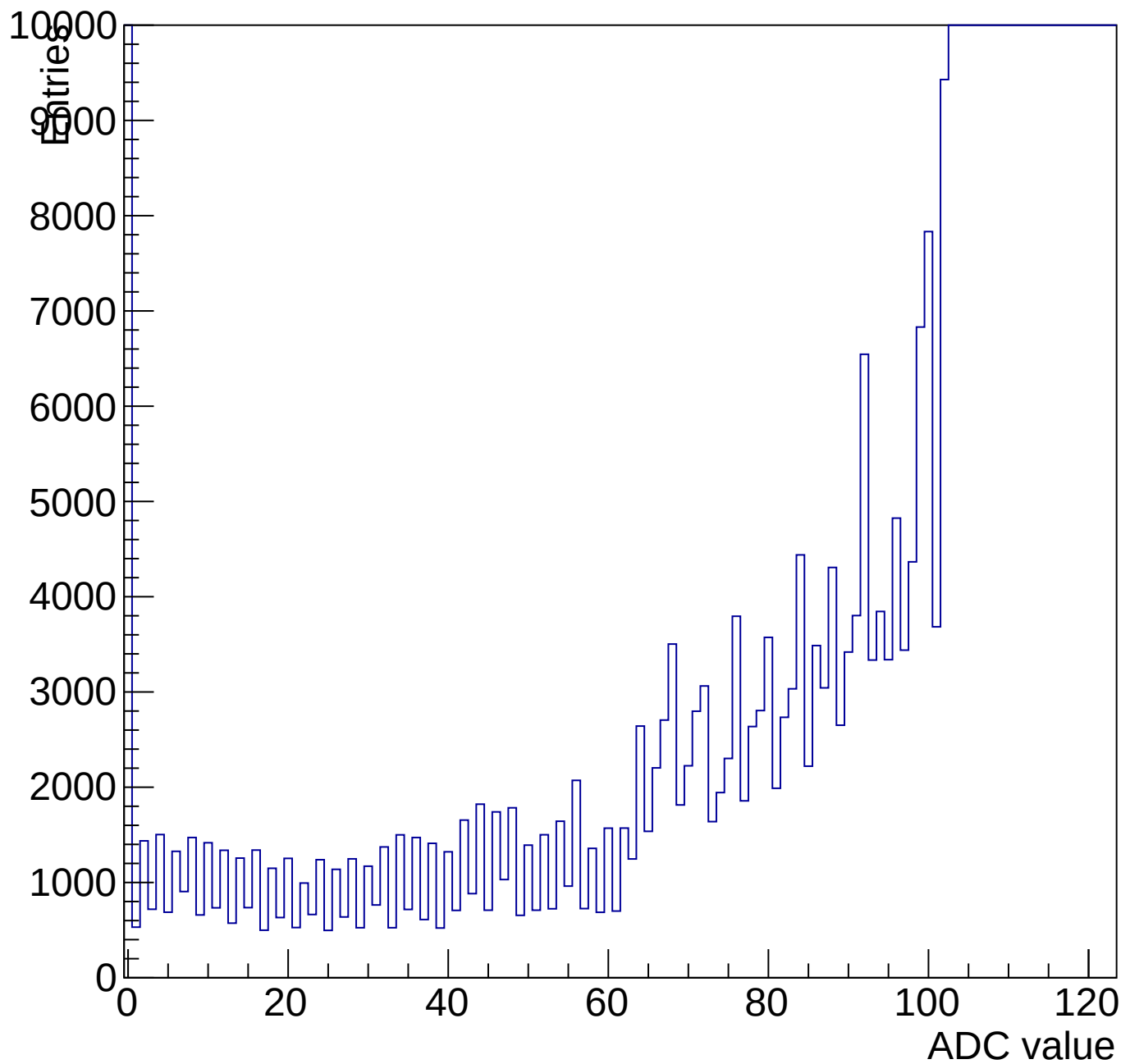
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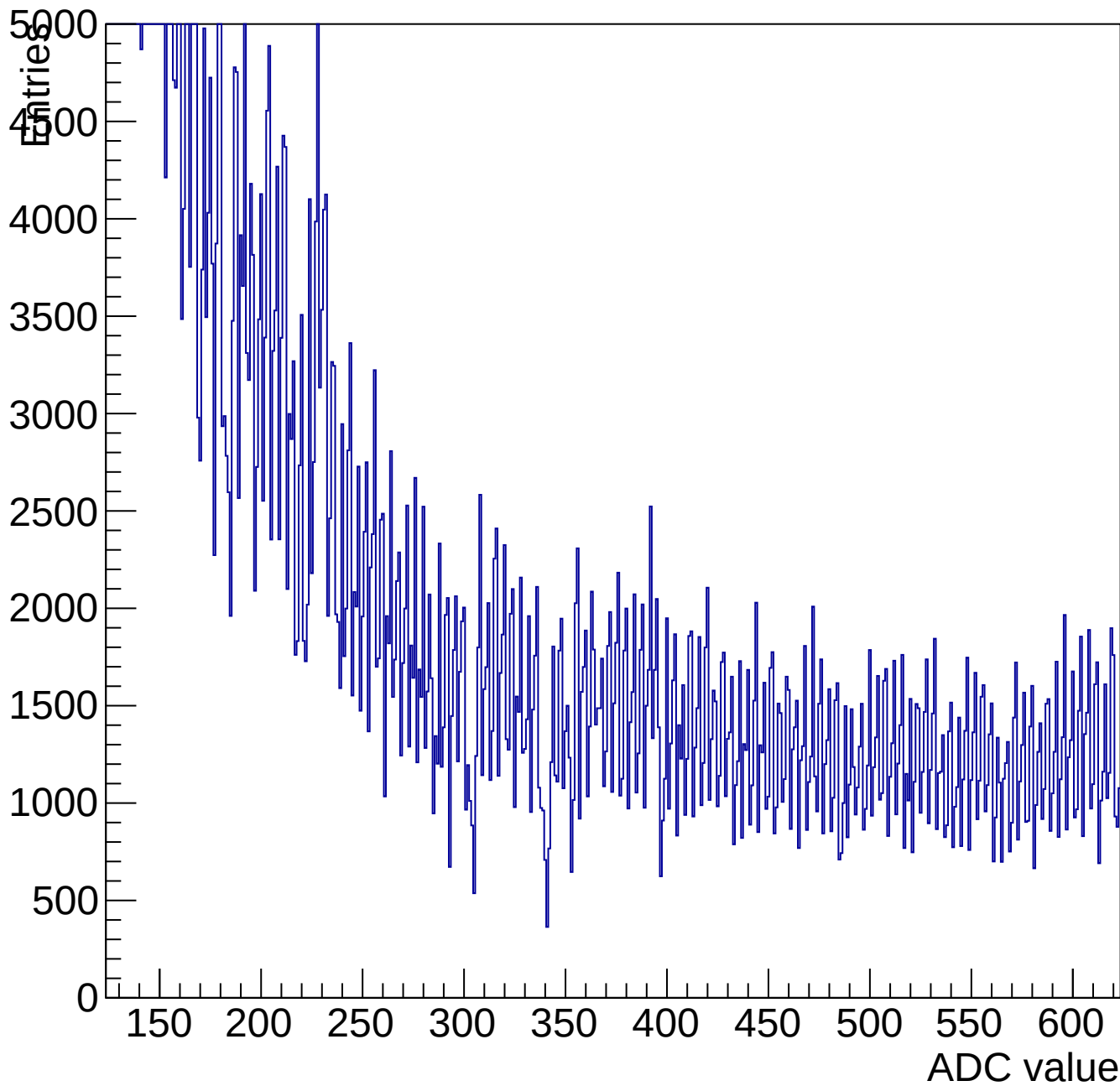
Pulse height chip 65 ch 12



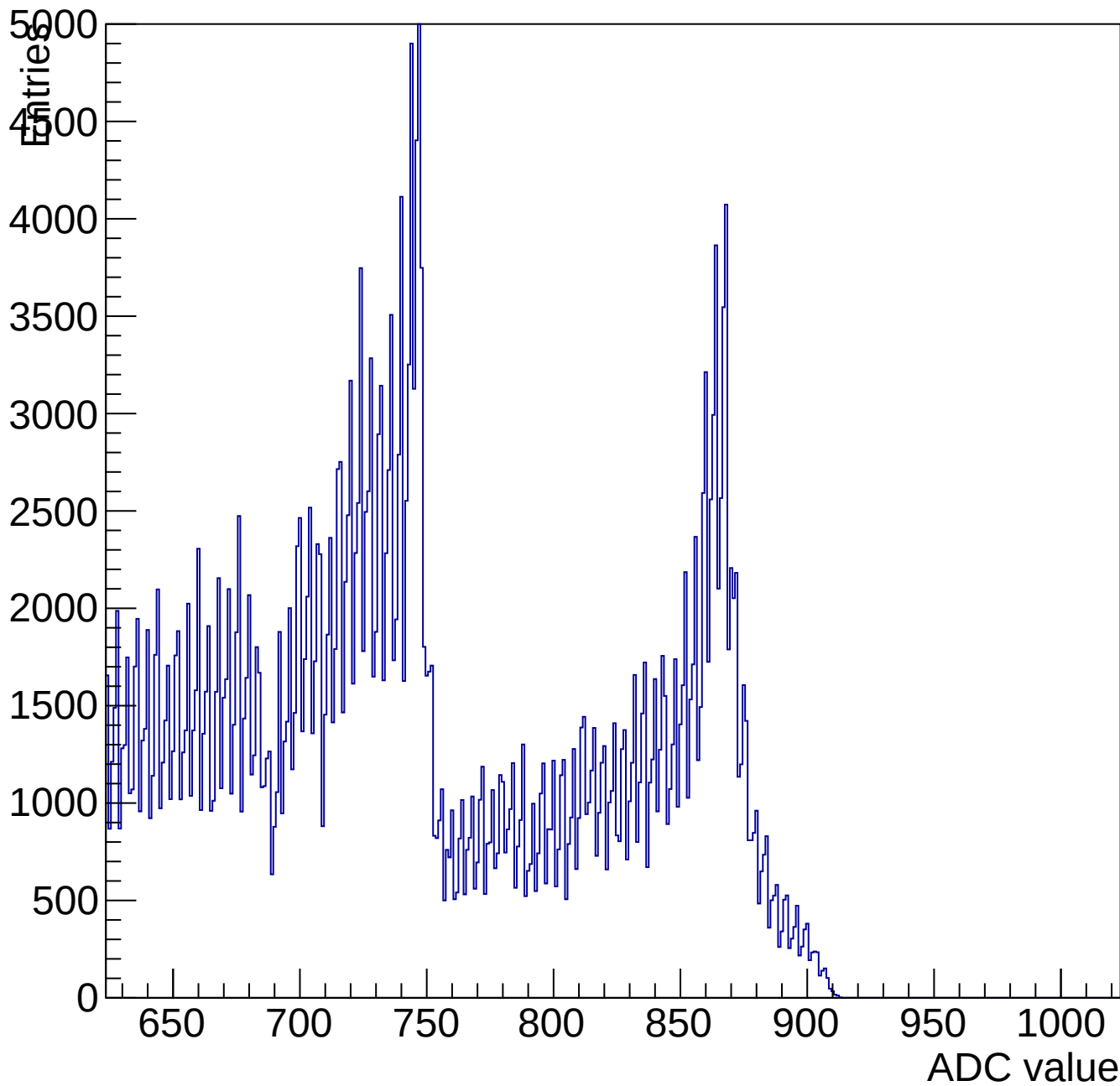
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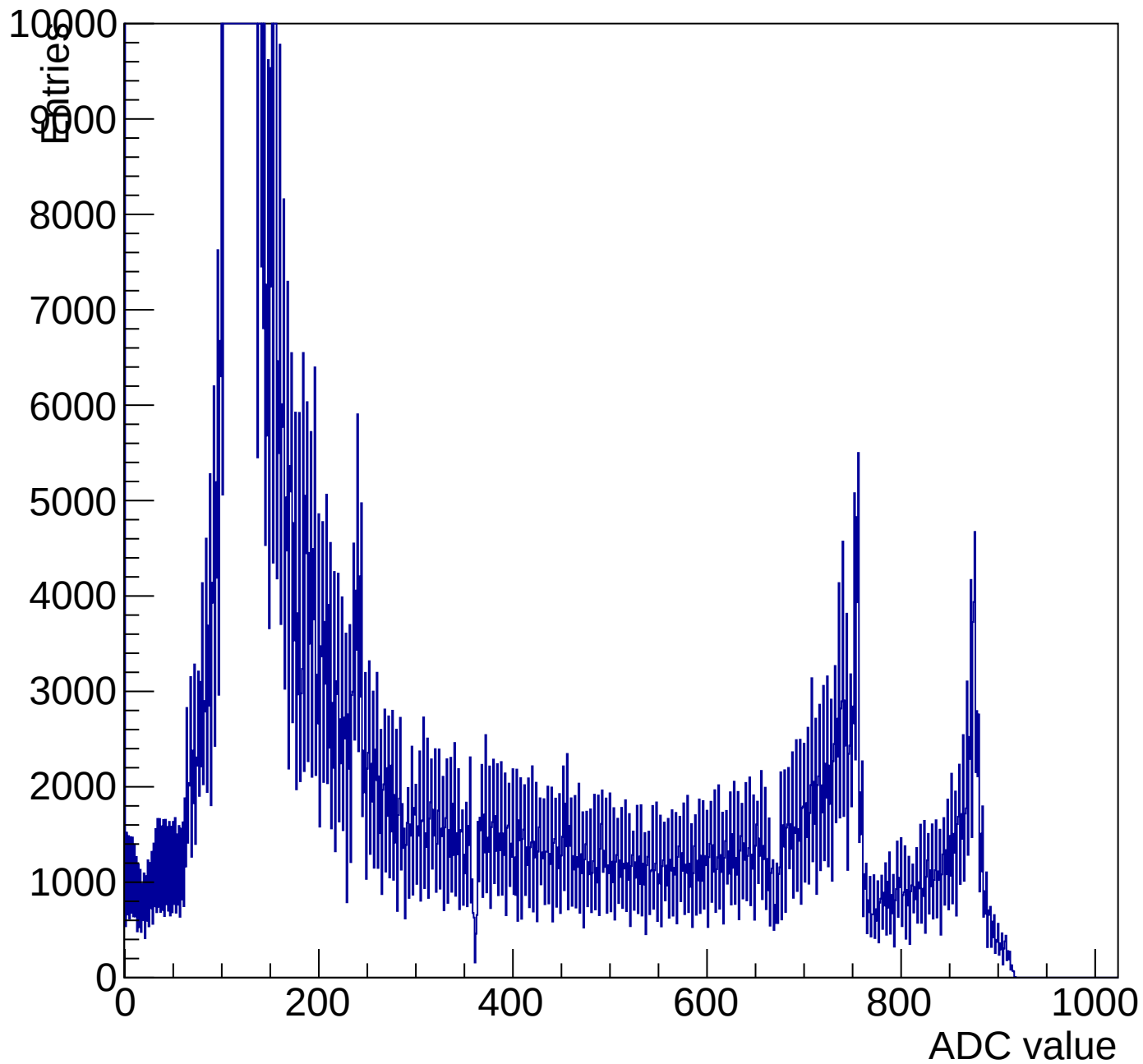
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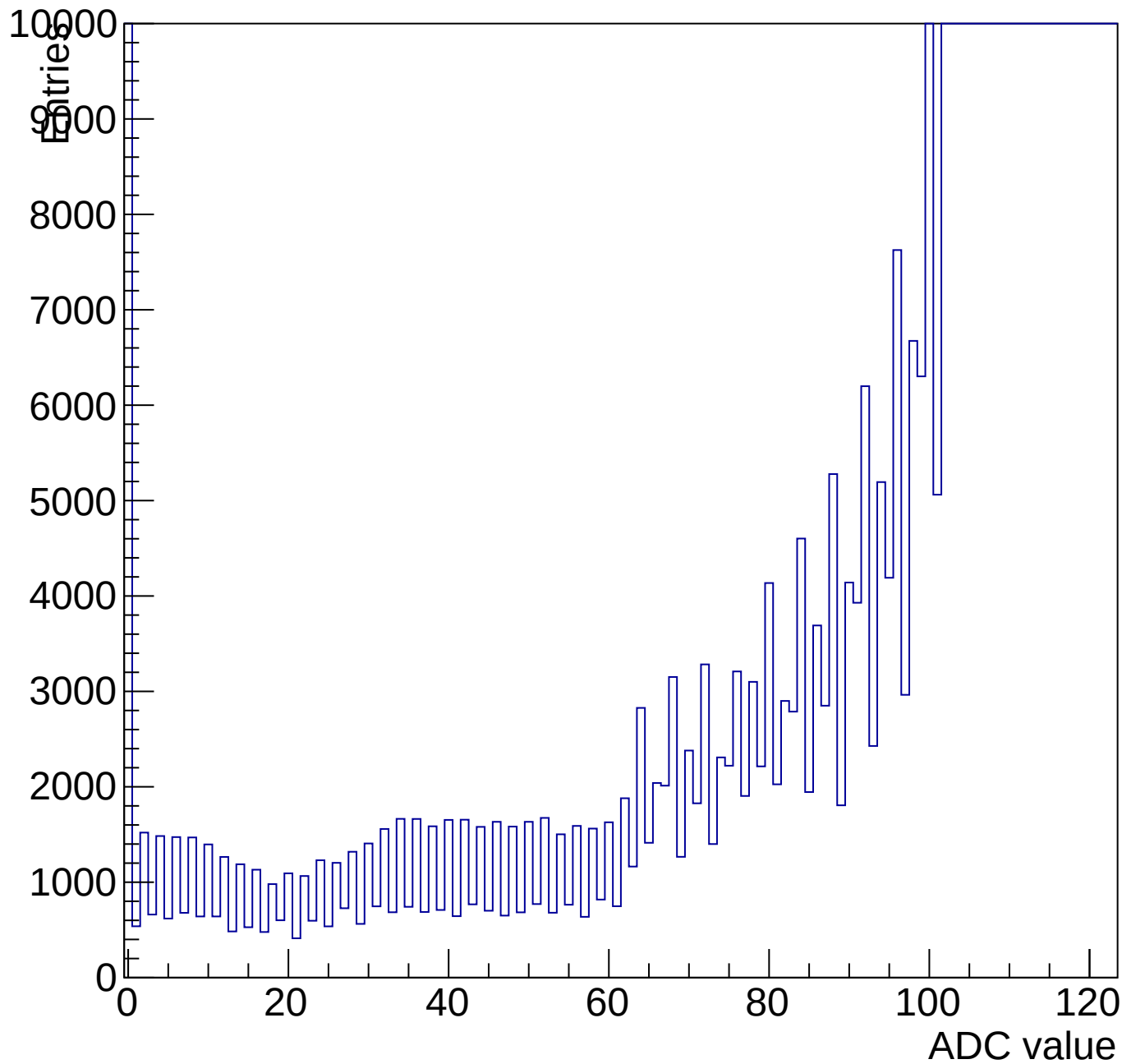
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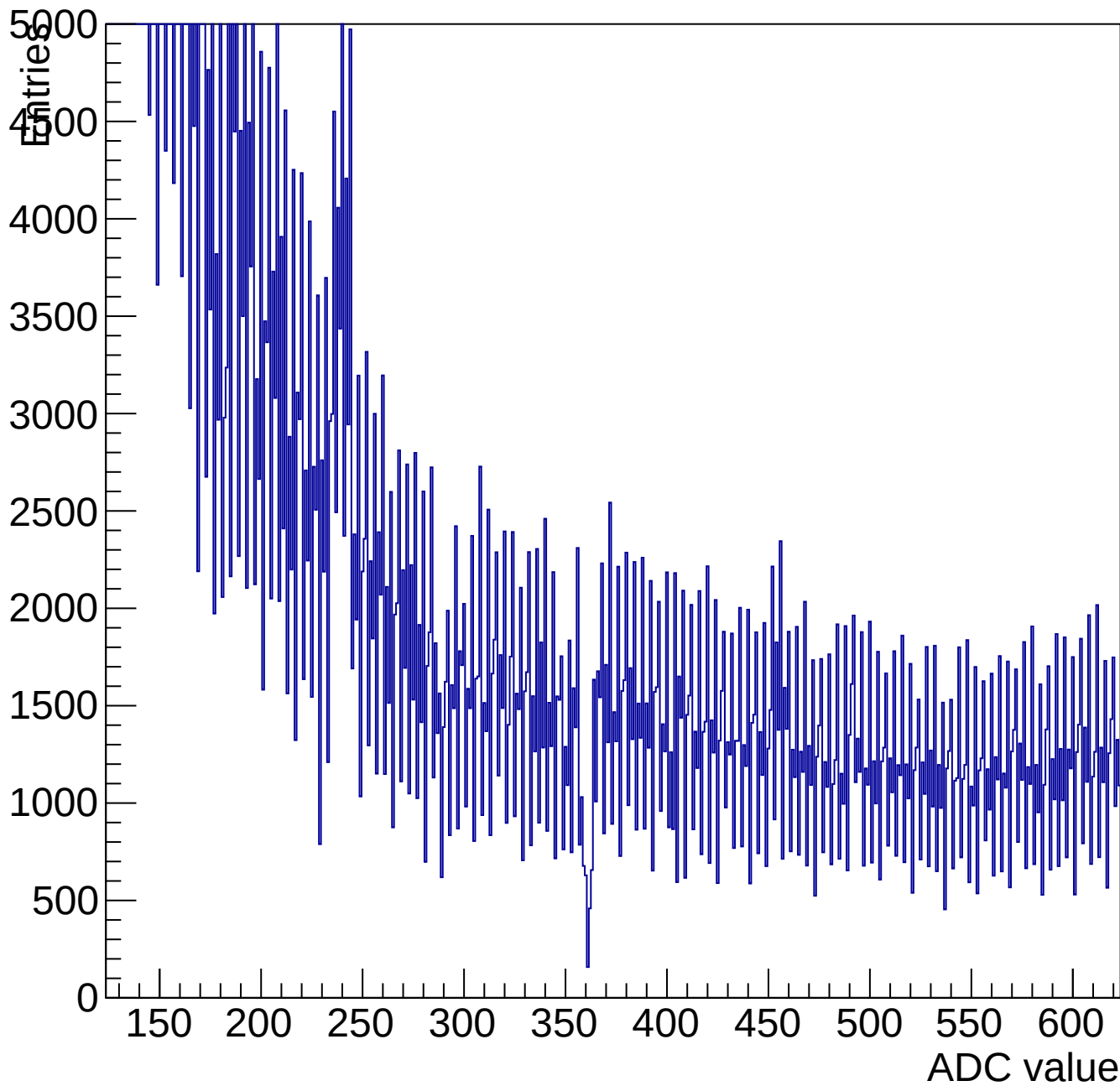
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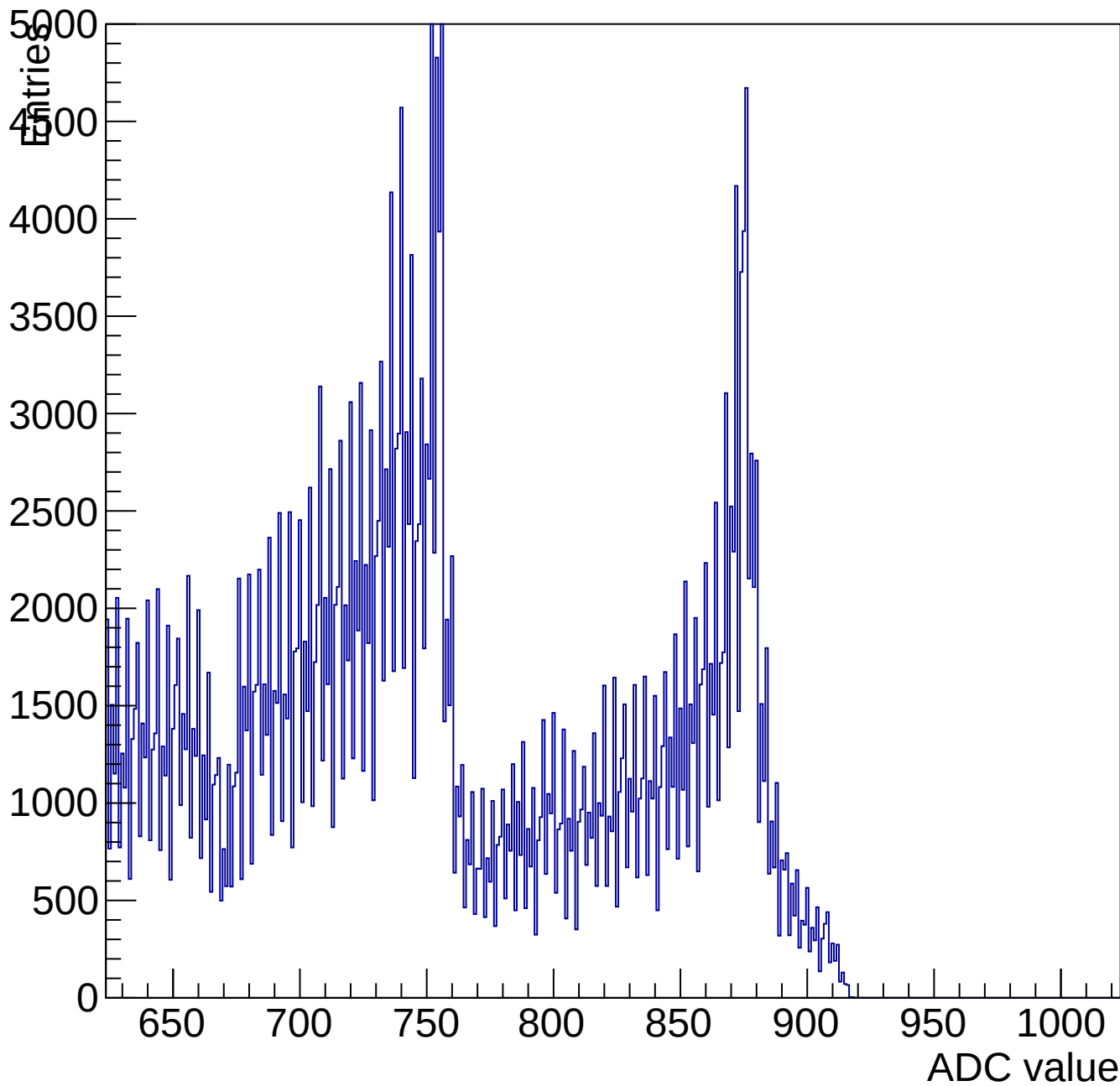
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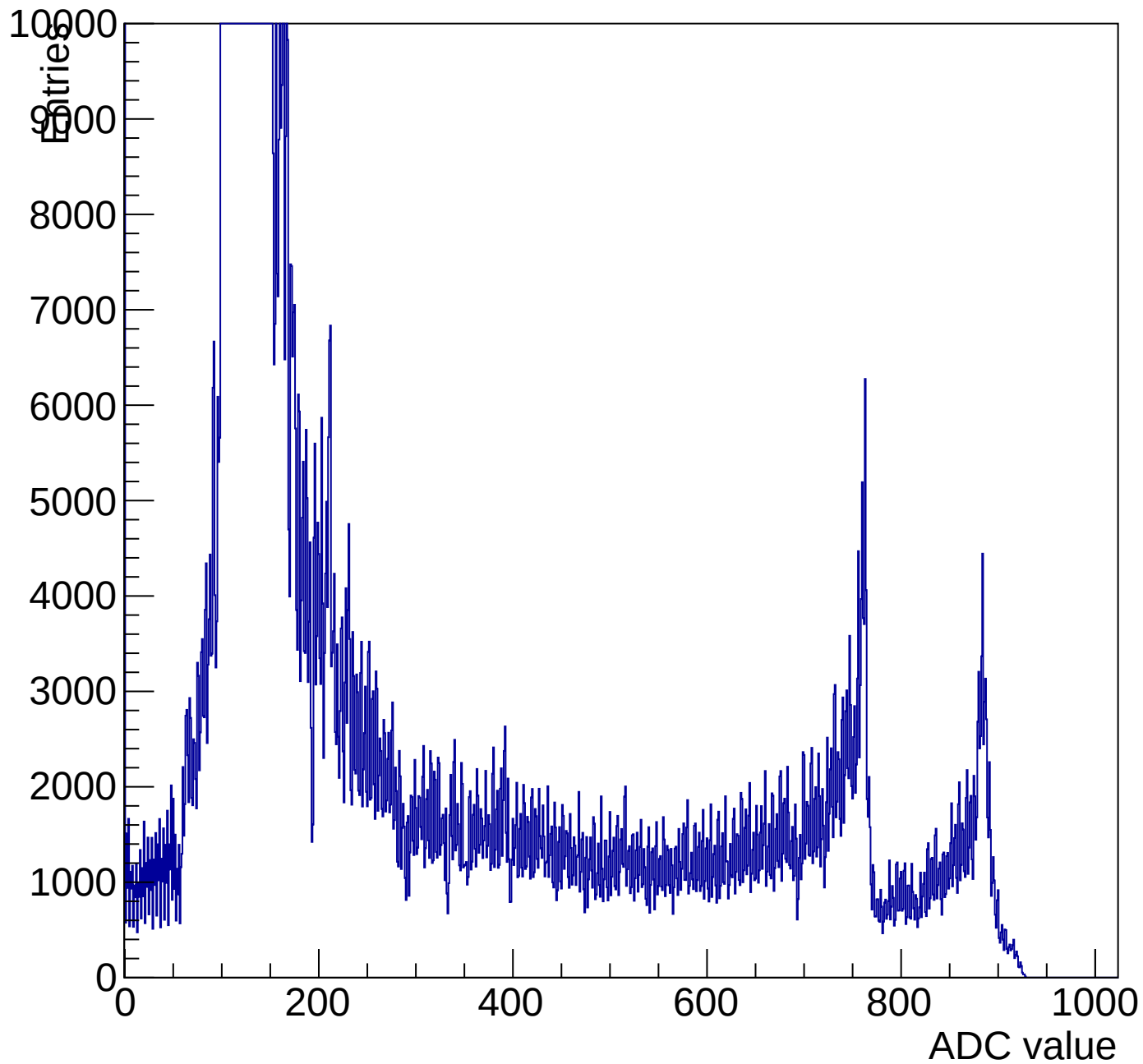
Pulse height chip 65 ch 13



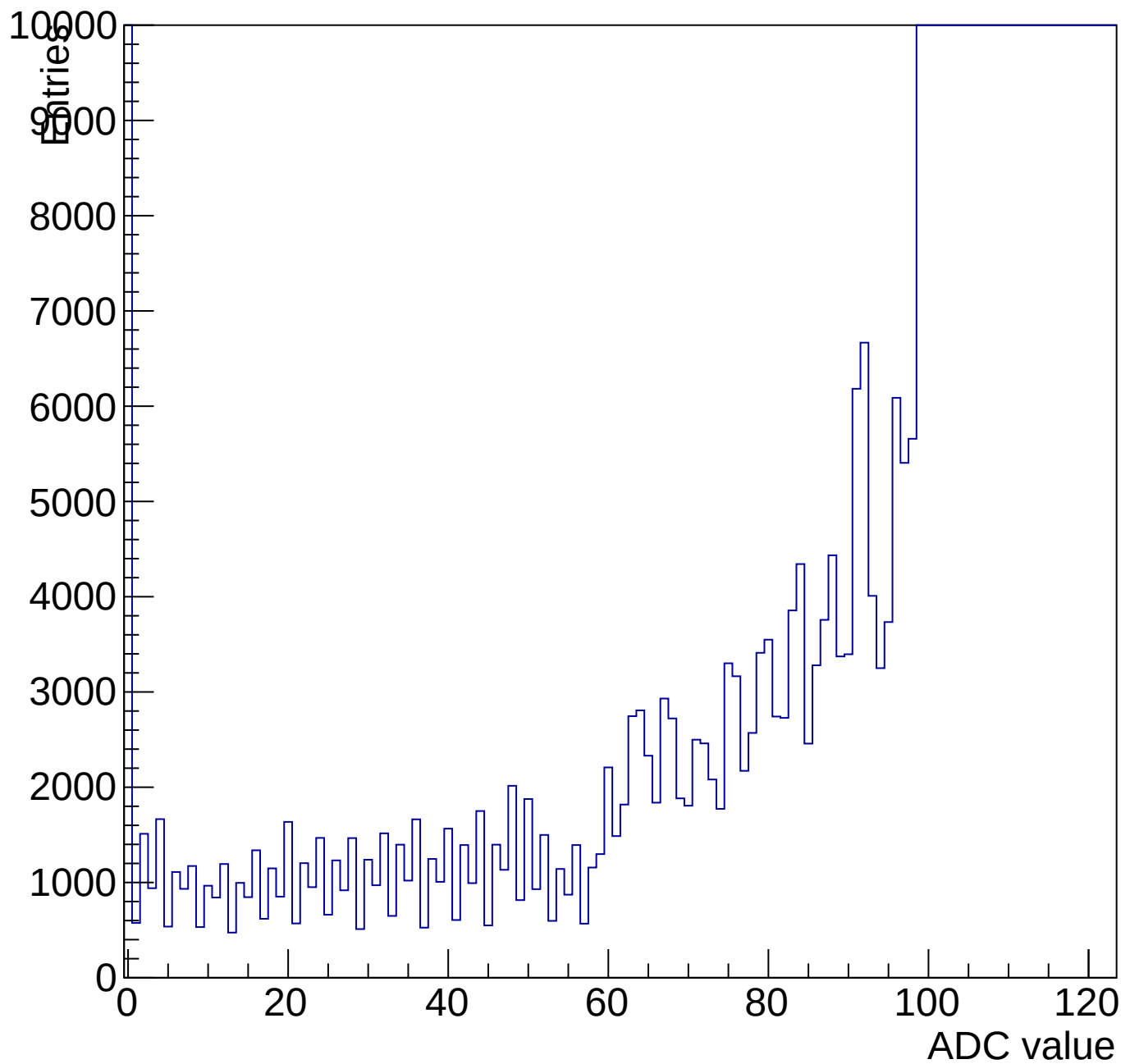
Pulse heightchip 65 ch 13



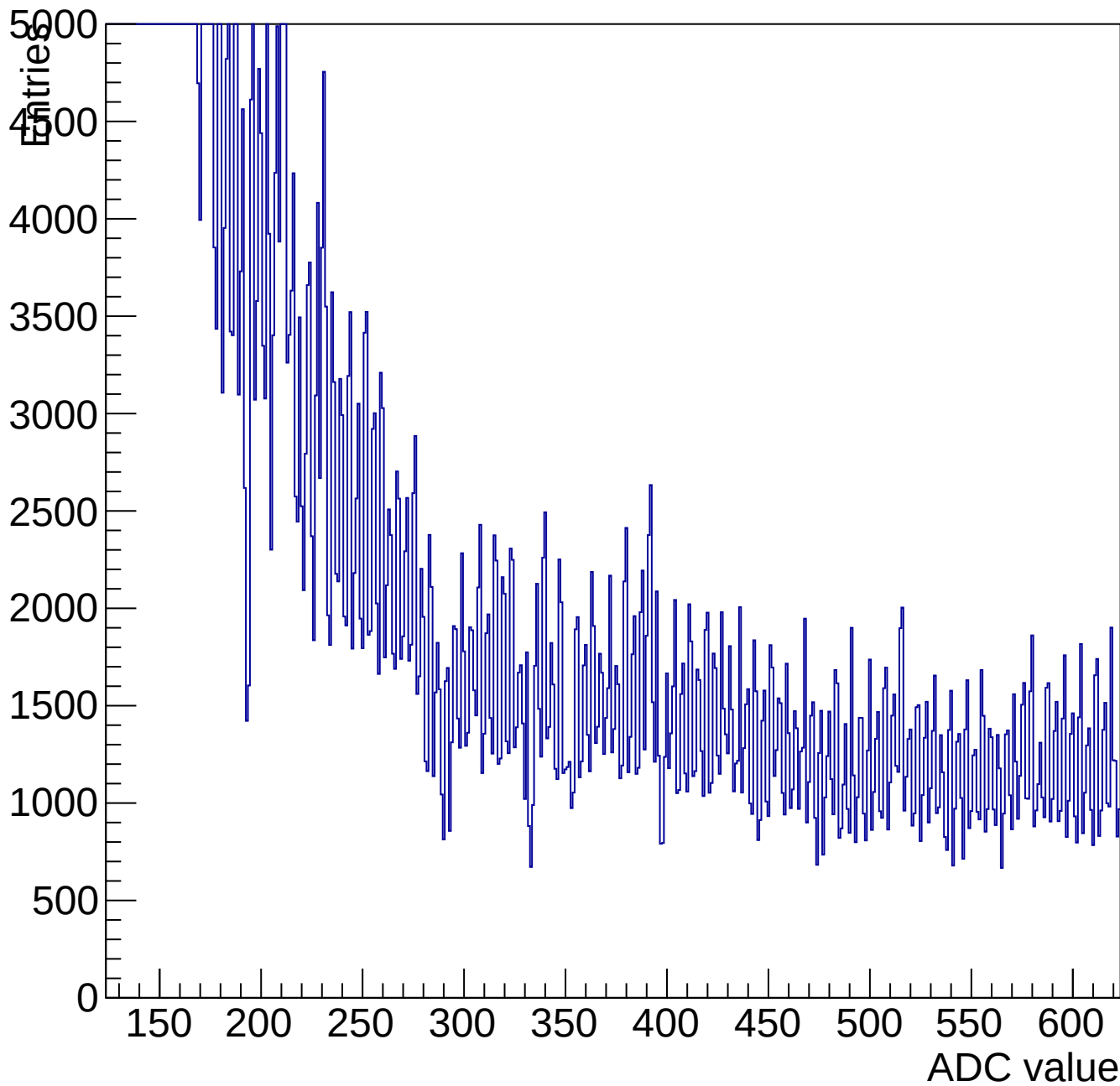
Pulse height chip 65 ch 14



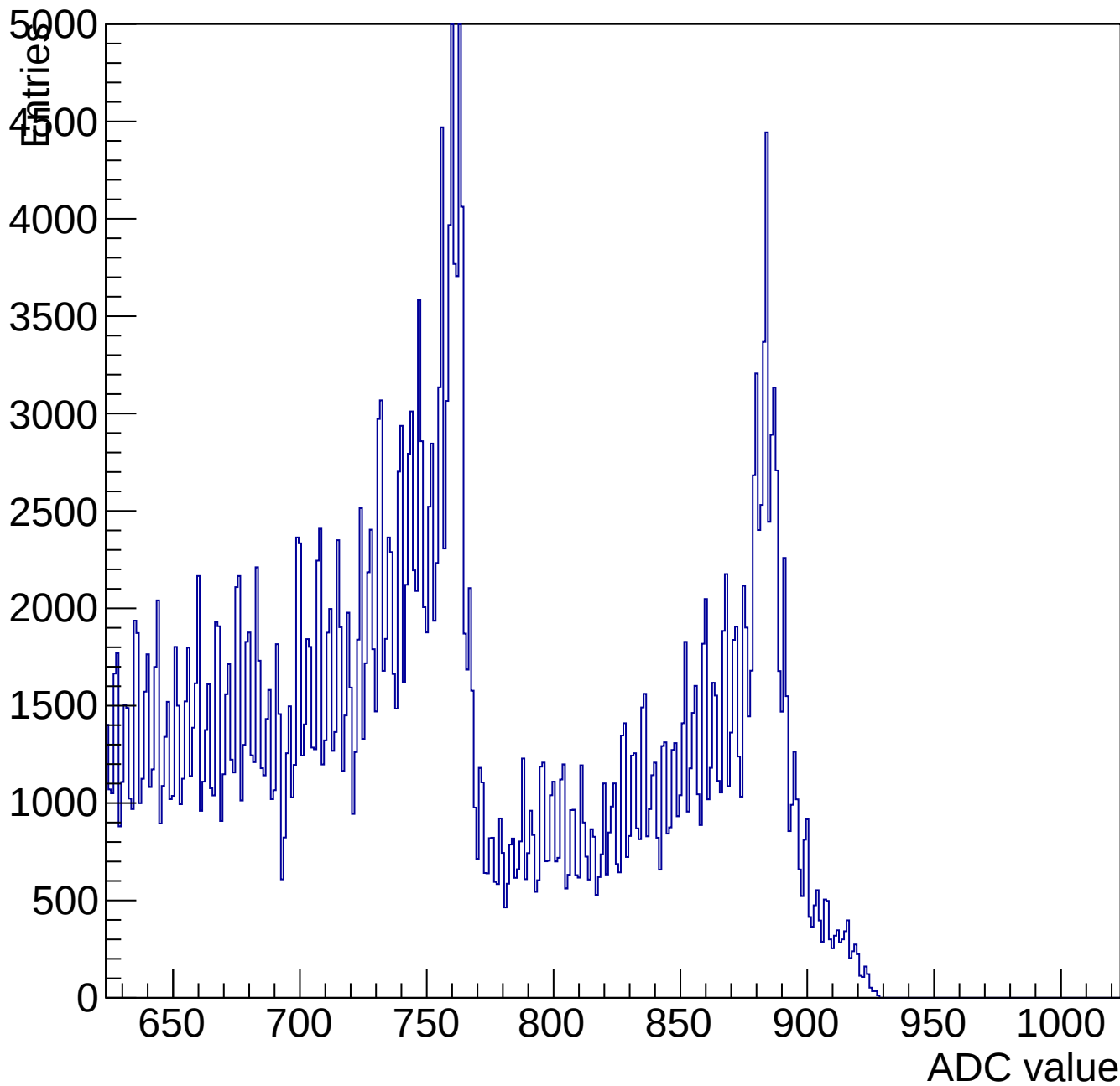
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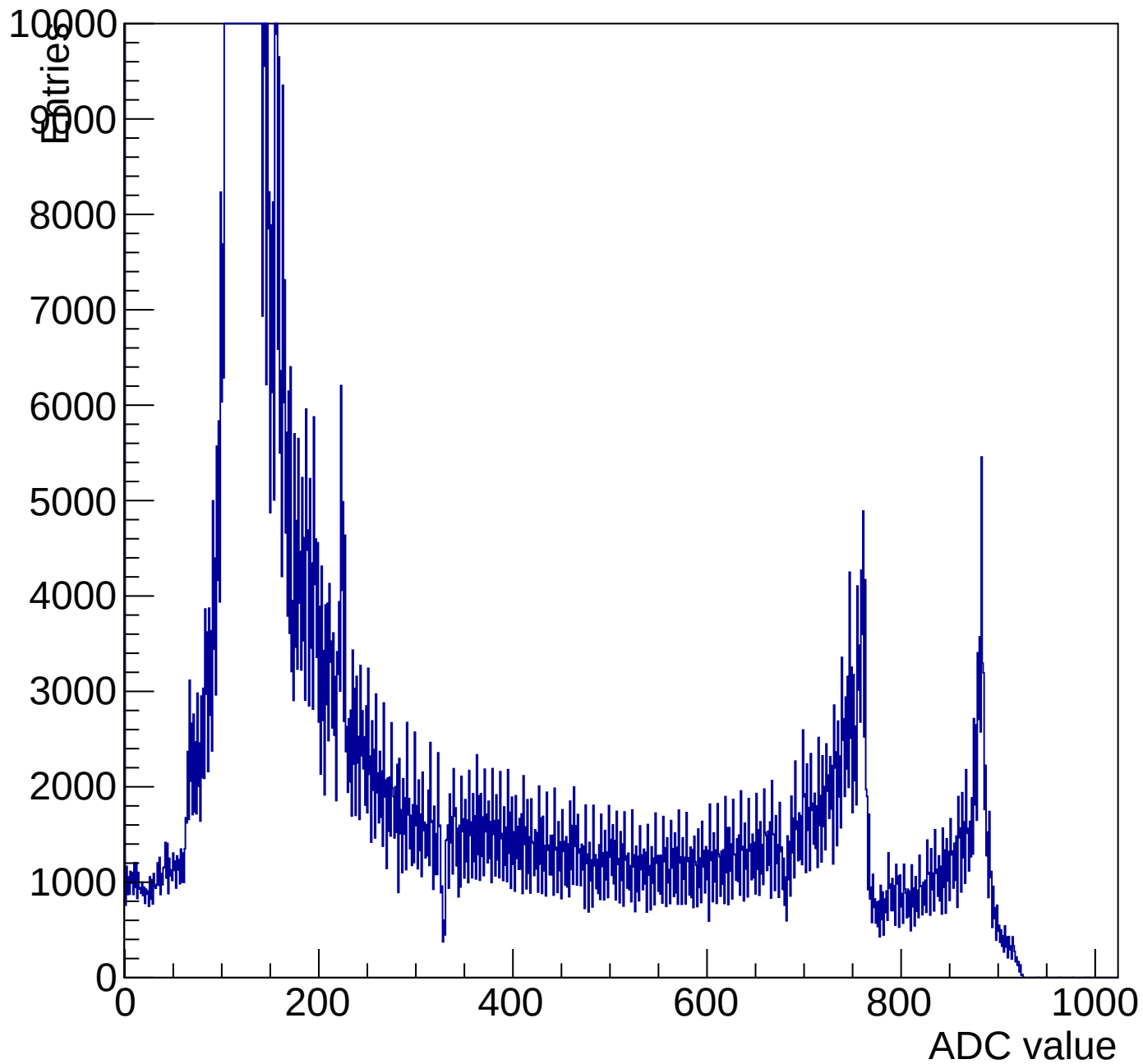
Pulse height chip 65 ch 14



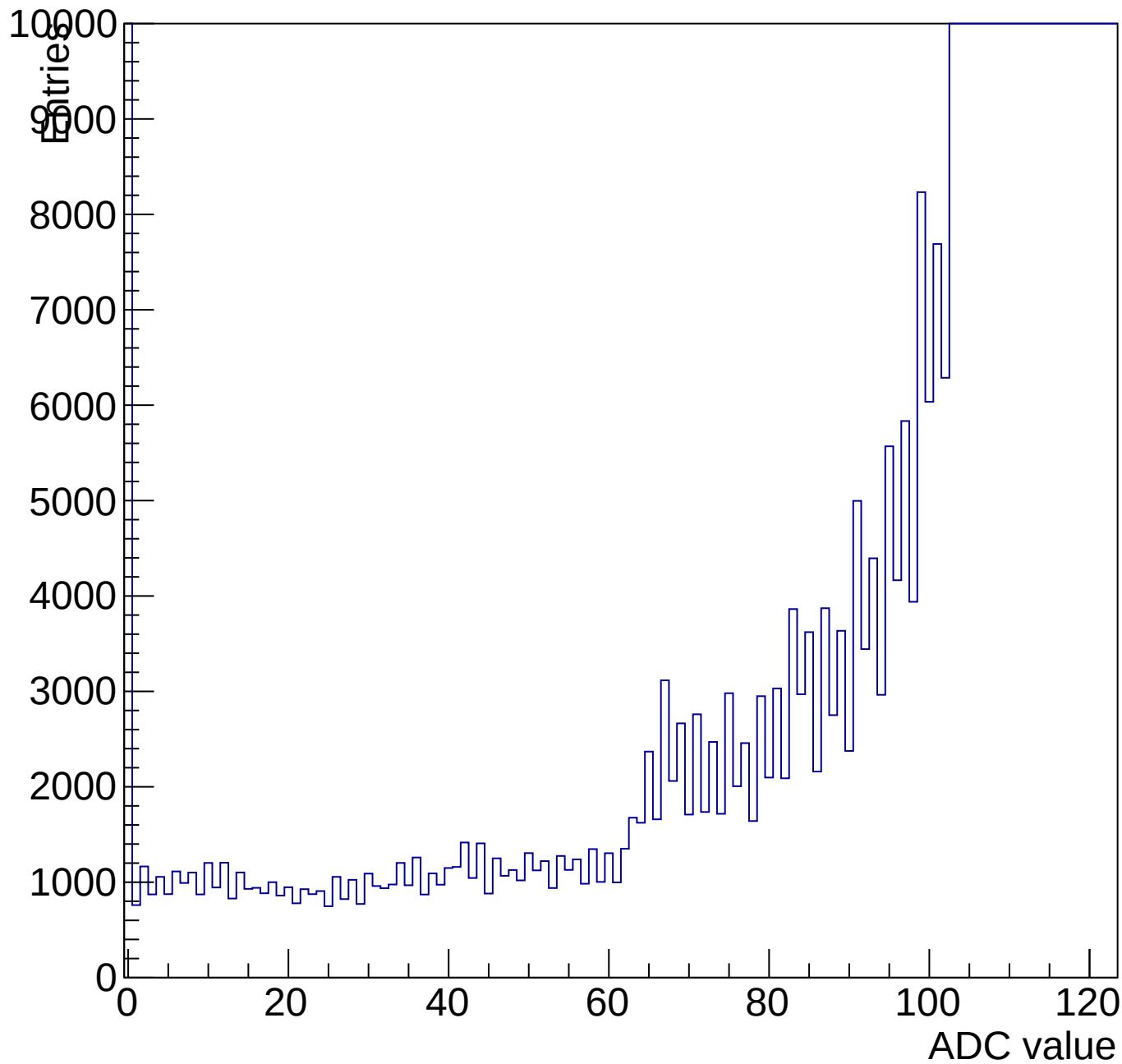
Pulse heightchip 65 ch 14



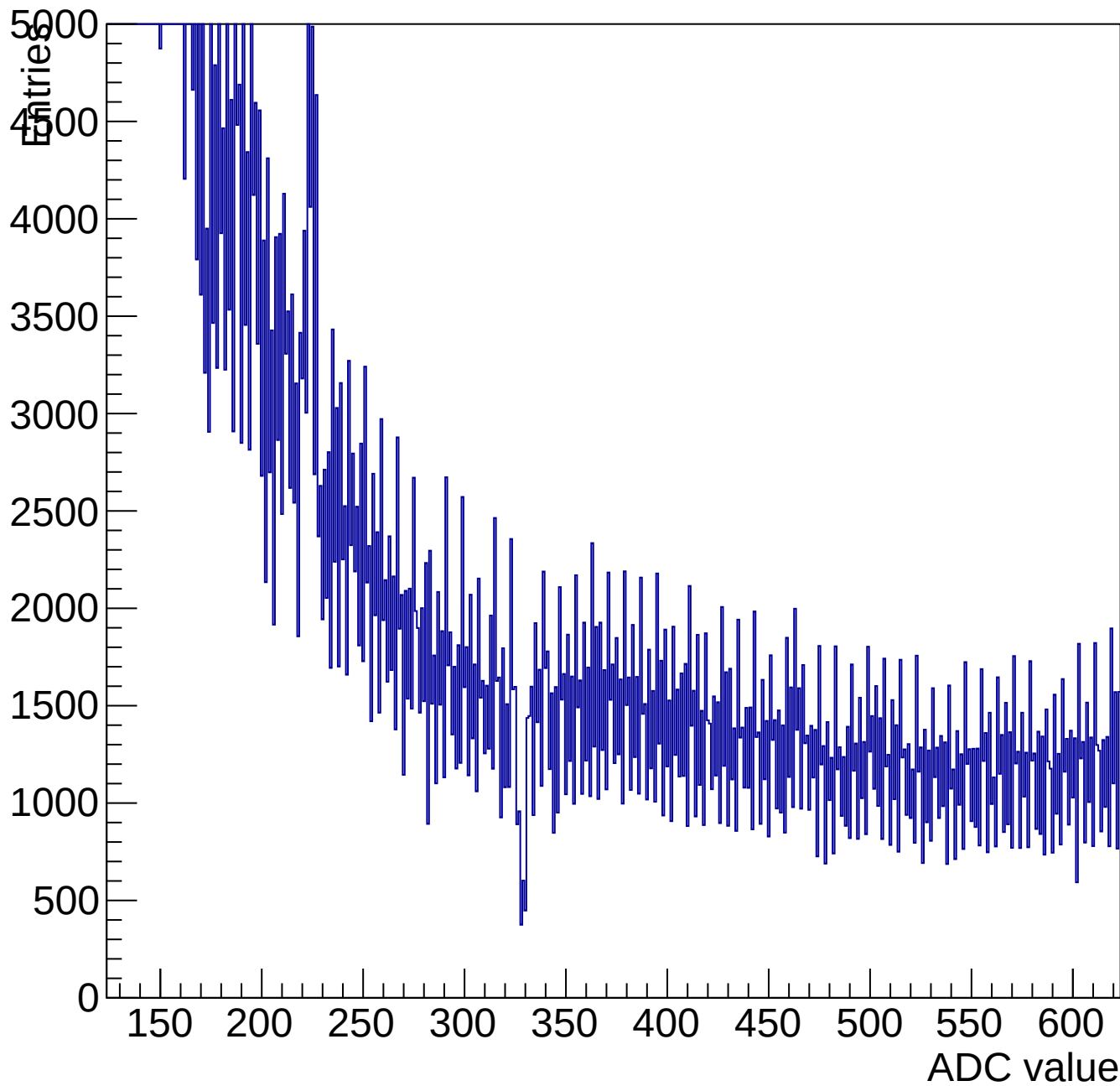
Pulse height chip 65 ch 15



Pulse height chip 65 ch 15



Pulse height chip 65 ch 15



Pulse heightchip 65 ch 15

