

	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	
A	Shaper1	Gain1	In0	In1	In2	In3	In4	In5	In6	In7	In8	In9	In10	In11	In12	In13	In14	In15	Shutdown	TestMode	
B	Shaper3	Gain2	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaGnd	PasaTestP	PasaTestN
C	Polarity	Shaper2	AdcAPower	AdcAPower	AdcAPower	AdcAPower	AdcAPower	AdcAPower	PasaPower	PasaPower	PasaPower	PasaPower	PasaPower	PasaPower	PasaPower	PasaPower	PasaPower	DecayBias	PreamplifierMode	AdcTestN	RefN
D	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd	AdcAGnd				Bias	AdcTestP	PasaGnd
E	Vcm	RefN																		Vcm	RefP
F																					
G	CmOut1	RefP	ClkAux				AdcDPower	AdcDPower	AdcDPower	AdcDPower	AdcDPower	AdcDPower	AdcDPower	AdcDPower	AdcDPower				DigPower	AdcDGnd	CmOut2
H	AdcClk	ClkSel	DigGnd	DigGnd	DigGnd	DigGnd	DigGnd	DigGnd	DigGnd	DigGnd	DigGnd	DigGnd	AdcDGnd	AdcDGnd	AdcDGnd	AdcDGnd	ChipGating	ChipGating	DigPower	DigPower	ChipAddress0
J	ChipAddress7	ChipAddress5	Rst	ChipAddress4	Cstb	IoGnd	IoGnd	IoGnd	IoGnd	IoGnd	IoGnd	IoGnd	IoGnd	IoGnd	IoGnd	IoGnd	IoGnd	ChipAddress3	ChipAddress2	ChipAddress1	AdcAdd1
K	Write	Grst	DoloEn	ChipAddress6	IoPower	IoPower	IoPower								IoPower	IoPower	DigPower	DigPower	Tms	AdcAdd0	Trg
L	Error	TRSFEN	Trsf	DSTB	IoPower	IoPower	IoPower	IoPower	ScanMode	RdoClk	IoPower	IoPower	IoPower	IoPower	ScanEn	IoPower	IoPower	Ack	AckEn	L2Y	TSTOUT
M	BD39	BD37	BD35	BD33	BD31	BD29	BD27	BD25	BD23	BD21	BD19	BD17	BD15	BD13	BD11	BD09	BD07	BD05	BD03	BD01	
N	BD38	BD36	BD34	BD32	BD30	BD28	BD26	BD24	BD22	BD20	BD18	BD16	BD14	BD12	BD10	BD08	BD06	BD04	BD02	BD00	