	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
A	TestMode	PasaTestP	PasaTestN	VddPasaA	AdcTestN	AdcTestP	GndAdcA	VddAdcA	l2yb	ackb	ack_en	bd[00]	DVDD	tstout	bd[03]
B	VddPasaA	RefN	GndAdcA	VddAdcA	Vcm	GndAdcA	VddAdcA	RefP	adc_add0	GND	VDD	tsm/tms	trgb	bd[10]	bd[04]
C	Shutdown	InHBM14	VddAdcA	GndAdcA	CmOut	BiasGate	VddAdcA	GndAdcA	chipadd [1]	chipadd [2]	chipadd [3]	adc_add1	bd[13]	bd[11]	bd[05]
D	PreampMode	GndPasaA	VddPasaA	GndChipGuardRi	VddAdcDig	VddAdcDig	GndAdcDig	chipadd [0]	DVSS	bd[01]	bd[02]	bd[18]	bd[14]	ScanEn	bd[06]
E	BiasDecay	InCDM14	GndPasaA	GndPasaA				GndAdcDig				bd[19]	bd[15]	DVSS	bd[07]
F	InHBM15	InCDM13	InCDM11	InCDM9								bd[20]	bd[16]	DVDD	bd[08]
G	GndPasaA	GndPasaA	GndPasaA	GndPasaA								GND	bd[17]	bd[12]	bd[09]
H	InCDM15	InCDM12	InCDM10	InCDM8	VddPasaA						rdoclk	VDD	bd[24]	DVSS	bd[31]
J	GndPasaA	VddPasaA	InCDM6	VddPasaA								bd[21]	bd[25]	tstout2	bd[32]
K	InCDM0	GndPasaA	GndPasaA	InHBM7								bd[22]	bd[26]	DVDD	bd[33]
L	Gain1	InCDM3	InCDM5	GndPasaA				ClkAux				bd[23]	bd[27]	ScanMode	bd[34]
M	Gain2	GndPasaA	GndPasaA	InCDM7	Shaping Time2	Shaping Time3	VddPasaA	GndAdcA	GndAdcDig	VddAdcDig	ClkSelect	sclk	bd[28]	bd[29]	bd[35]
N	VddPasaA	InCDM2	InCDM4	VddAdcA	GndAdcA	CmOut	VddAdcA	VddAdcDig	GndAdcDig	chipadd [4]	chipadd [5]	chipadd [6]	chipadd [7]	bd[30]	bd[36]
P	VddPasaA	InCDM1	VddAdcA	Vcm	GndAdcA	VddAdcA	RefP	SoftRst	grstb	GND	VDD	writeb	cstbb	dolo_en	bd[37]
R	Shaping Time1	Polarity	VddPasaA	GndAdcA	VddAdcA	RefN	GndAdcA	trsf_en	errorb	trsfb	DVSS	dstb	DVDD	bd[39]	bd[38]

Top view

2014−10−13