20120504

We are not sure of the connectors on the Testboard. The drawing we have seems not to describe the board completely, e.g. the power connectors on the board are split in 2*4, but he drawing show
1x6 (SL508_6_90B) with 3.3 PASA, +4V0_AN, +4V0_DIG
1x4 (SL508_4_90B) with +5V TTL.

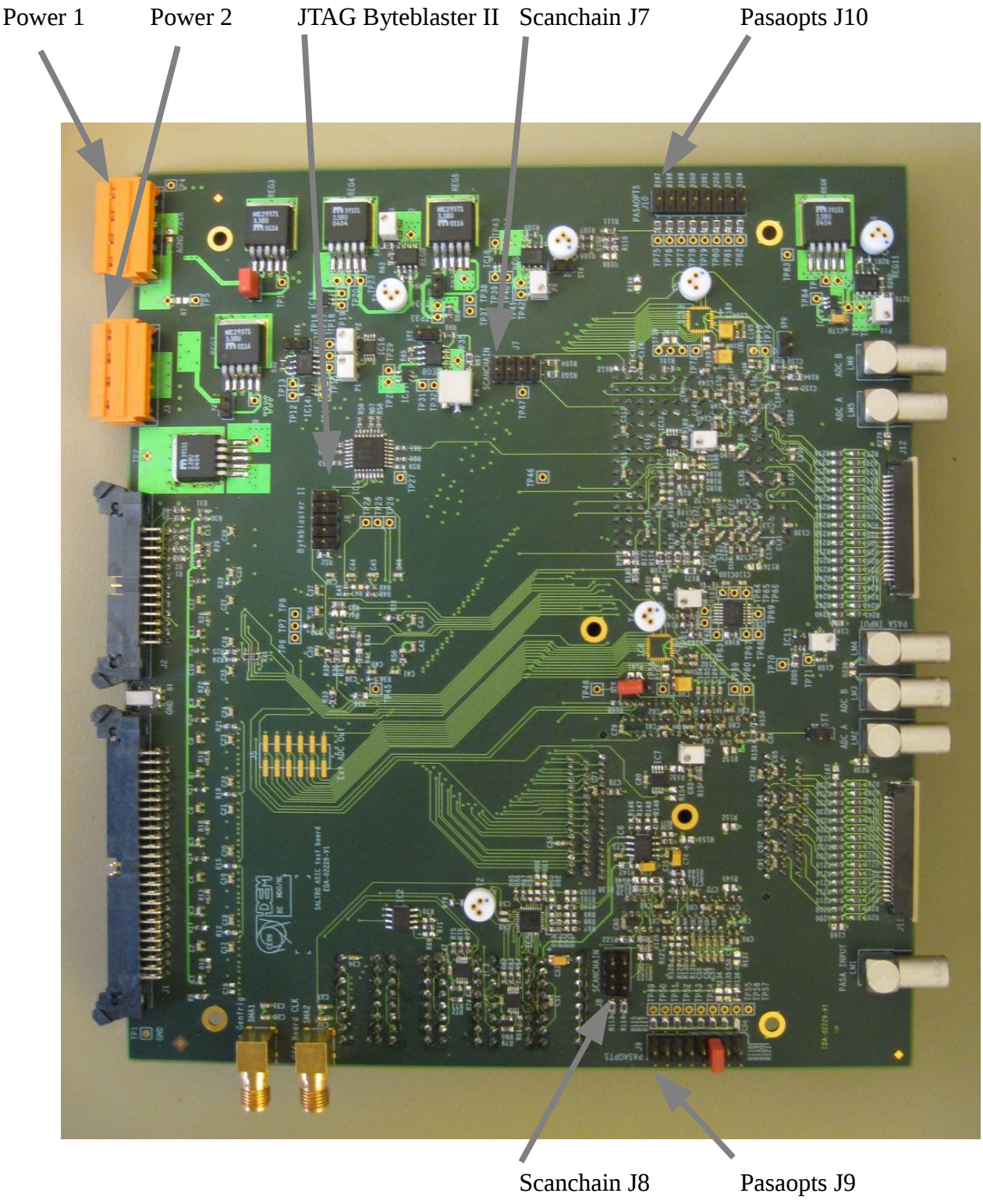
So one need four voltages: 3.3 PASA, 4.0 analog, 4.0 digital, and 5 V?

We could probably find out which is pin 1 on the Byteblaster connector, but we want to be sure.

So I wonder if you could fill in what the different connector and jumper pins are, I have tried to make a list further down in this document.

The drawing we have is labeled LAST_MODIFIED=Wed Jan 12 09:58:41 2011.
Is this the correct one?

TESTBOARD


Power 1
Power 2
JTAG Byteblaster II
Scanchain J7
Pasaopts J10
Scanchain J8
Pasaopts J9

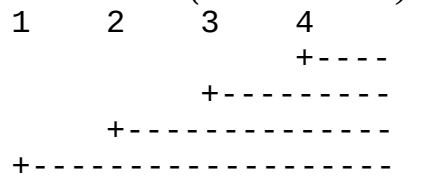
All descriptions are from top to bottom or left to right as seen in the picture

Power 1 (J4)
1) 3.3V Pasa
2) Gnd Pasa
3) 4.0V analog
4) Gnd analog

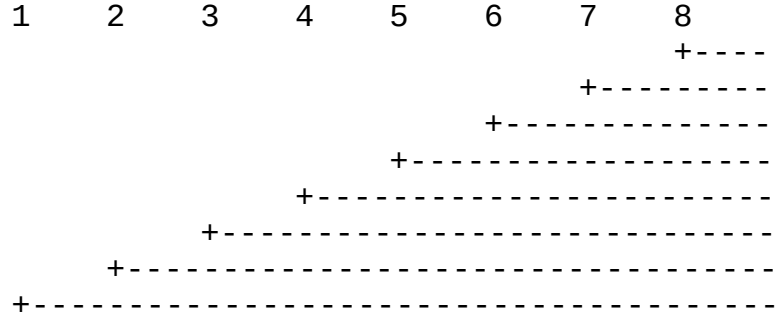
Power 2 (J3)
1) 4V digital
2) gnd
3) TTL gnd
4) 5V TTL

JTAG ByteBlaster II pin numbering
x x
x x
x x
x x
x 1

Scanchain J7 (NOT NEEDED)

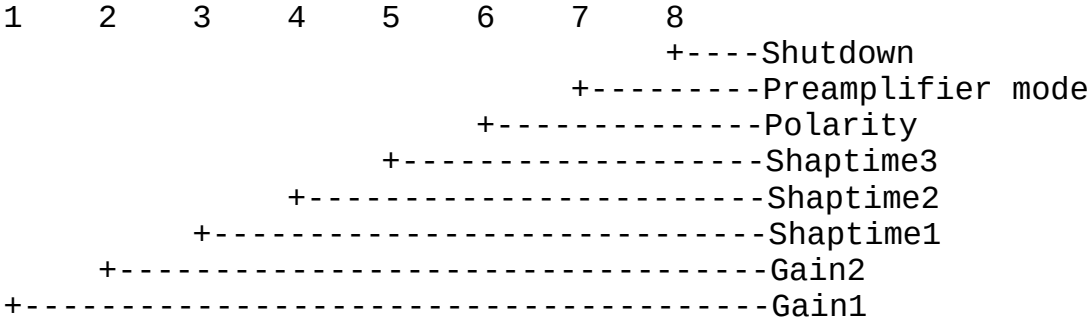


Pasaopts J10



Scanchain J8 (NOT NEEDED)
1
2
3
4

Pasaopts J9



Comment about ordering on schematics and picture above

I asked about the ordering on picture and schematics:

Power 1 (J4)

1) 3.3V Pasa
2) Gnd Pasa
3) 4.0V analog
4) Gnd analog

Power 2 (J3)

1) 4V digital
2) gnd
3) TTL gnd
4) 5V TTL

I want to be sure about the corresponding numbering on the drawing, as far as I understand is the correspoing numbering:
4
3
2
1

Answer is:
“Yes, about J3 and J4, the numbers in the picture that you sent me and the numbers in the schematic that I sent you are flipped.”